


Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

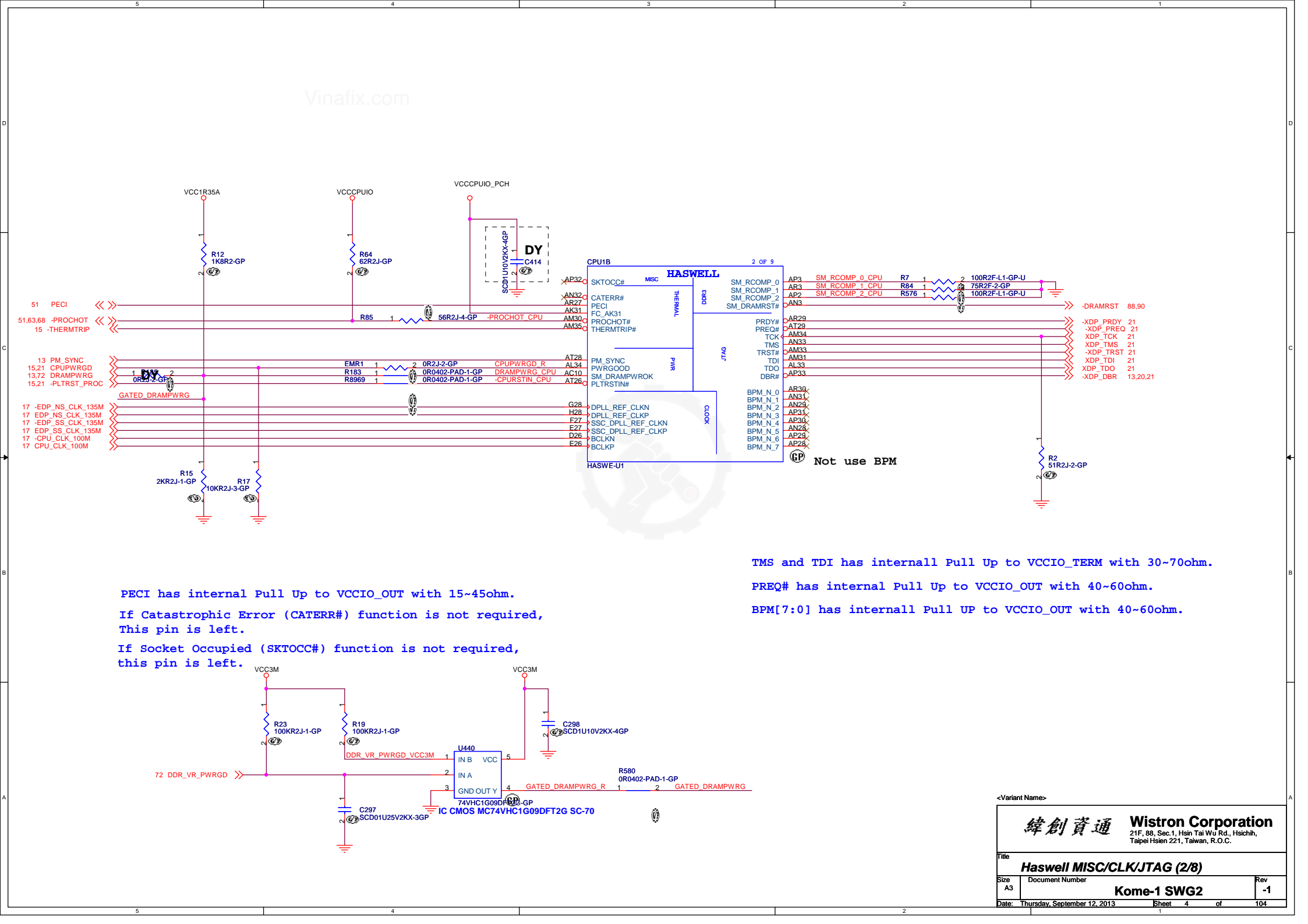
Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

PCH GPIOn	39	38	49	48	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	SDV (SWG1: Native eDP)	SA
	0	0	0	1	SDV (SWG2: DDI-D)	SA
	0	0	1	0	ME FVT	SB
	0	0	1	1	FVT	SC
	0	1	0	0	SiT	SD
	0	1	0	1	SVT on 8/12	-1
	0	1	1	0	SOVP on 10/8	-2

[illegible]

		DUMMY property					
		(No mark)	SWG	UMA	EH(47W)	ET(37W)	DY
PCBA	UMA ENTRY	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM
	UMA ENHANCE	ASM	NO_ASM	ASM	ASM	NO_ASM	NO_ASM
	SWG	ASM	ASM	NO_ASM	ASM	NO_ASM	NO_ASM

 緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
COMMENTS			
Size A3	Document Number		Rev -1
Kome-1 SWG2			
Date:	Thursday, September 12, 2013	Sheet 2 of	104

[illegible][illegible][illegible]

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51 Peci
51,63,68 -PROCHOT
15 -THERMTRIP

13 PM_SYNC
15,21 CPUPWRGD
13,72 DRAMPWRG
15,21 -PLTRST_PROC

17 -EDP_NS_CLK_135M
17 EDP_NS_CLK_135M
17 -EDP_SS_CLK_135M
17 EDP_SS_CLK_135M
17 -CPU_CLK_100M
17 CPU_CLK_100M

GATED_DRAMPWRG

R12 1K8R2-GP
R64 62R2J-GP
R85 1 56R2J-4-GP
R15 2KR2J-1-GP
R17 10KR2J-3-GP
R23 100KR2J-1-GP
R19 100KR2J-1-GP
C297 SCD01U25V2KX-3GP
C298 SCD1U10V2KX-4GP
R580 0R0402-PAD-1-GP
R2 51R2J-2-GP
R7 1 100R2F-L1-GP-U
R84 1 75R2F-2-GP
R576 1 100R2F-L1-GP-U

CPU1B HASWELL 2 OF 9

MISC
SKTOCC#
CATERR#
PECI
FC_AK31
PROCHOT#
THERMTRIP#

THERMAL
PM_SYNC
PWRGOOD
SM_DRAMPWROK
PLTRSTIN#

JTAG
PRDY#
PREQ#
TCK
TMS
TRST#
TDO
DBR#

BPM_N_0
BPM_N_1
BPM_N_2
BPM_N_3
BPM_N_4
BPM_N_5
BPM_N_6
BPM_N_7

Not use BPM

HASWE-U1

-DRAMRST 88,90
->XDP_PRDY 21
->XDP_PREQ 21
->XDP_TCK 21
->XDP_TMS 21
->XDP_TRST 21
->XDP_TDI 21
->XDP_TDO 21
->XDP_DBR 13,20,21

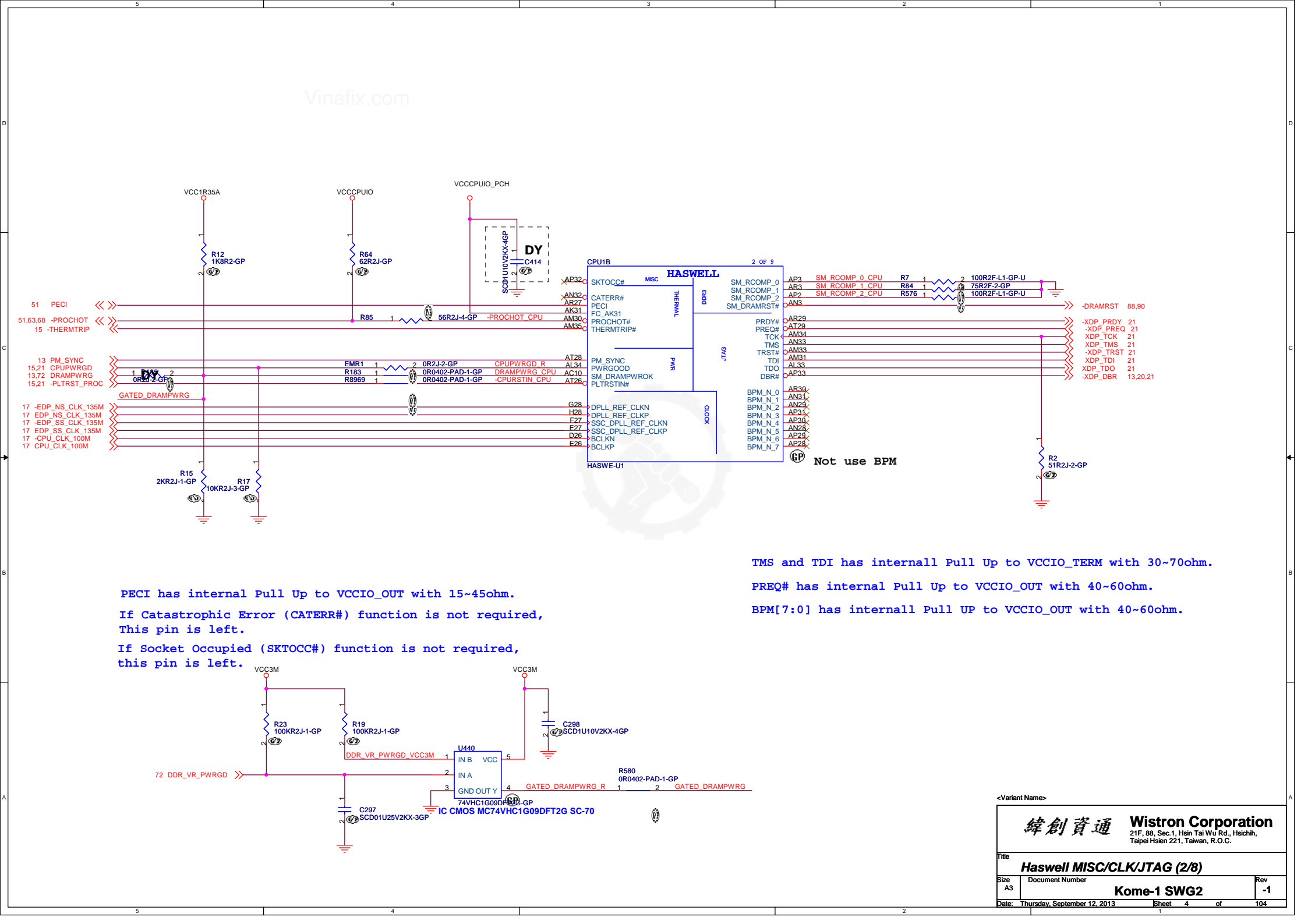
PECI has internal Pull Up to VCCIO_OUT with 15~45ohm.
If Catastrophic Error (CATERR#) function is not required,
This pin is left.
If Socket Occupied (SKTOCC#) function is not required,
this pin is left.

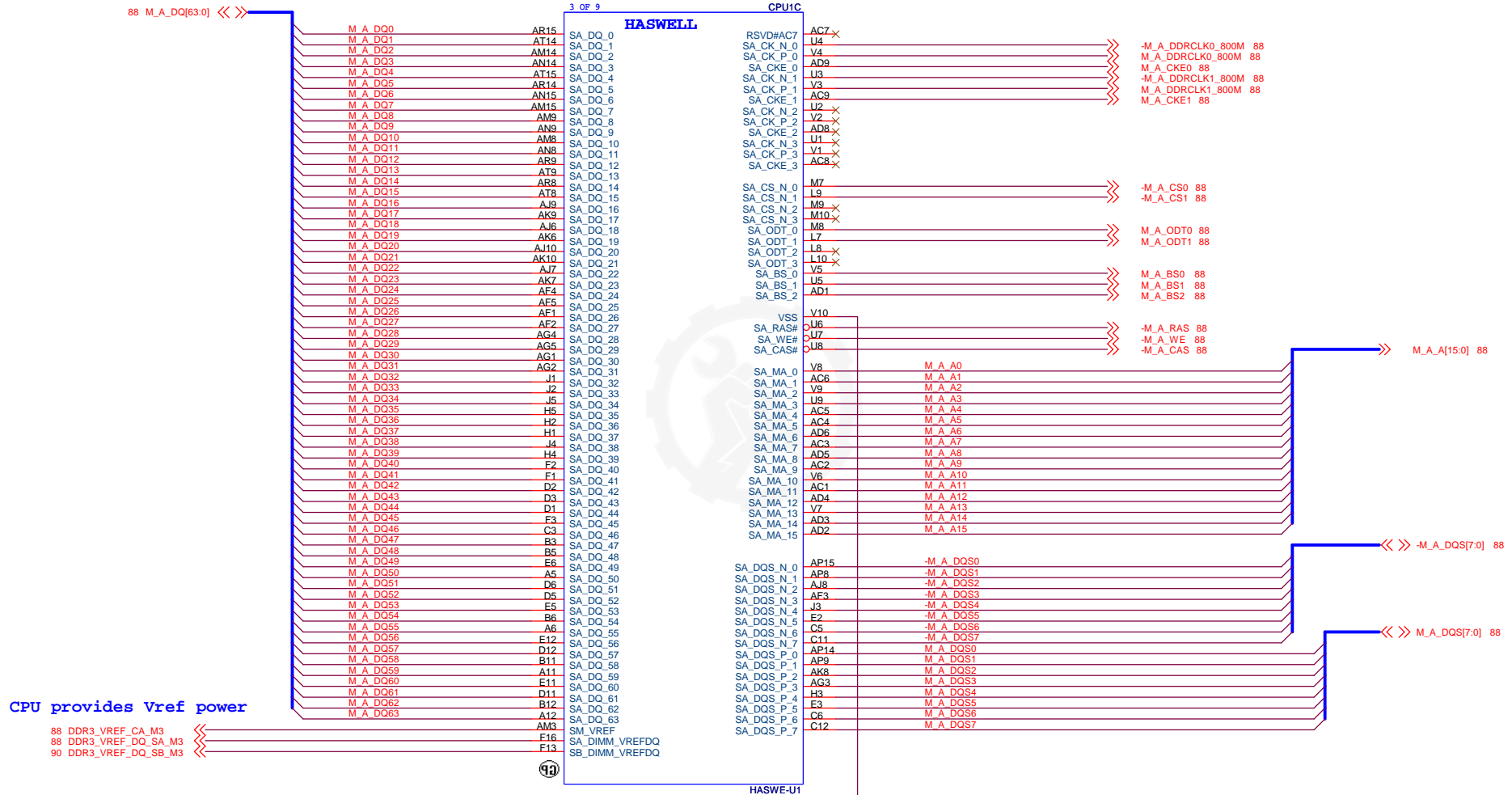
TMS and TDI has internall Pull Up to VCCIO_TERM with 30~70ohm.
PREQ# has internal Pull Up to VCCIO_OUT with 40~60ohm.
BPM[7:0] has internall Pull UP to VCCIO_OUT with 40~60ohm.

IC CMOS MC74VHC1G09DFT2G SC-70

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title Haswell MISC/CLK/JTAG (2/8)
Size A3 Document Number Kome-1 SWG2 Rev -1
Date: Thursday, September 12, 2013 Sheet 4 of 104

[illegible][illegible]



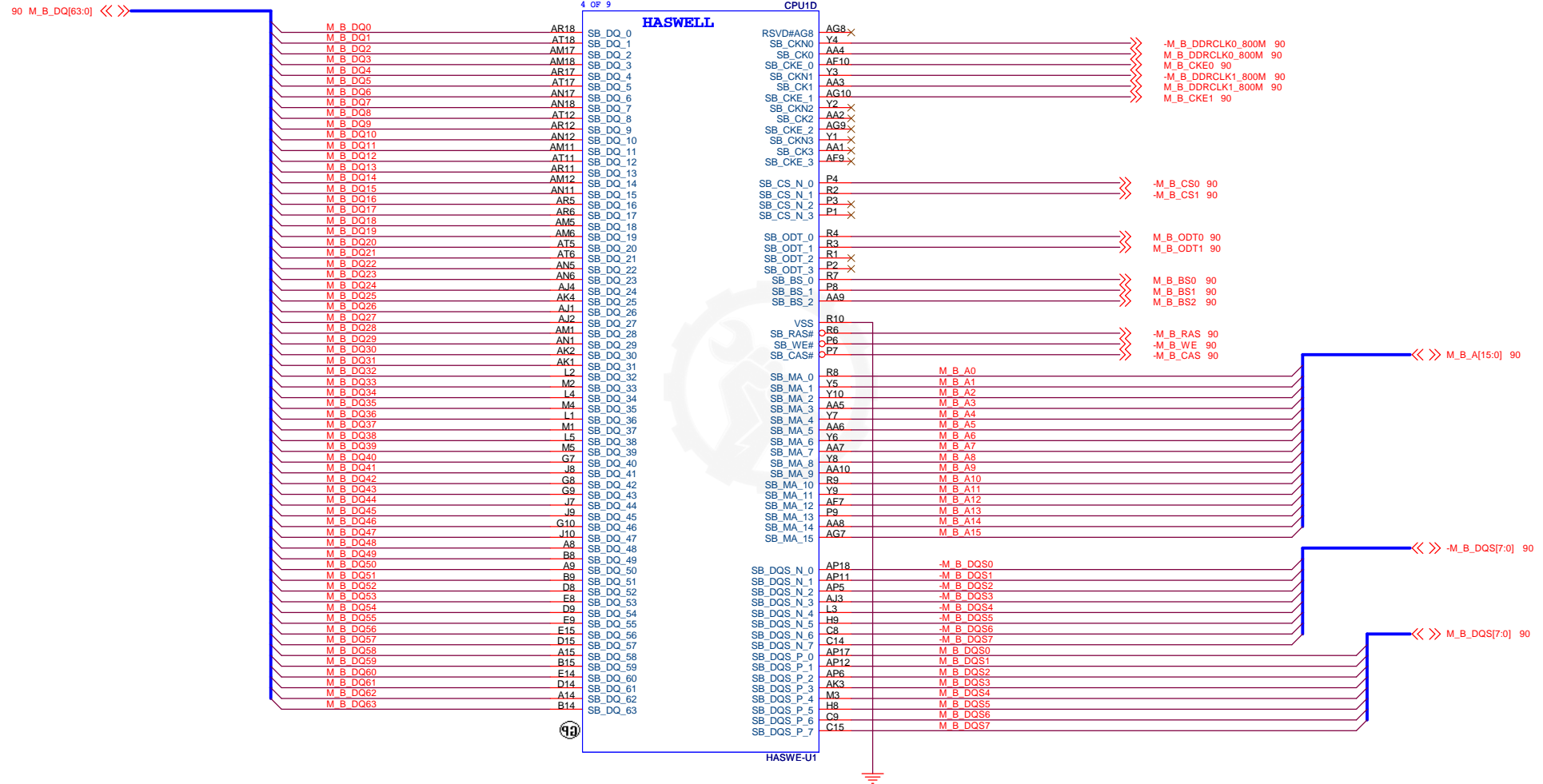
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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
Haswell DDR3L CH-A 1DIMM Per Channel (3/8)

Size A3 Document Number **Kome-1 SWG2** Rev -1

Date: Thursday, September 12, 2013 Sheet 5 of 104



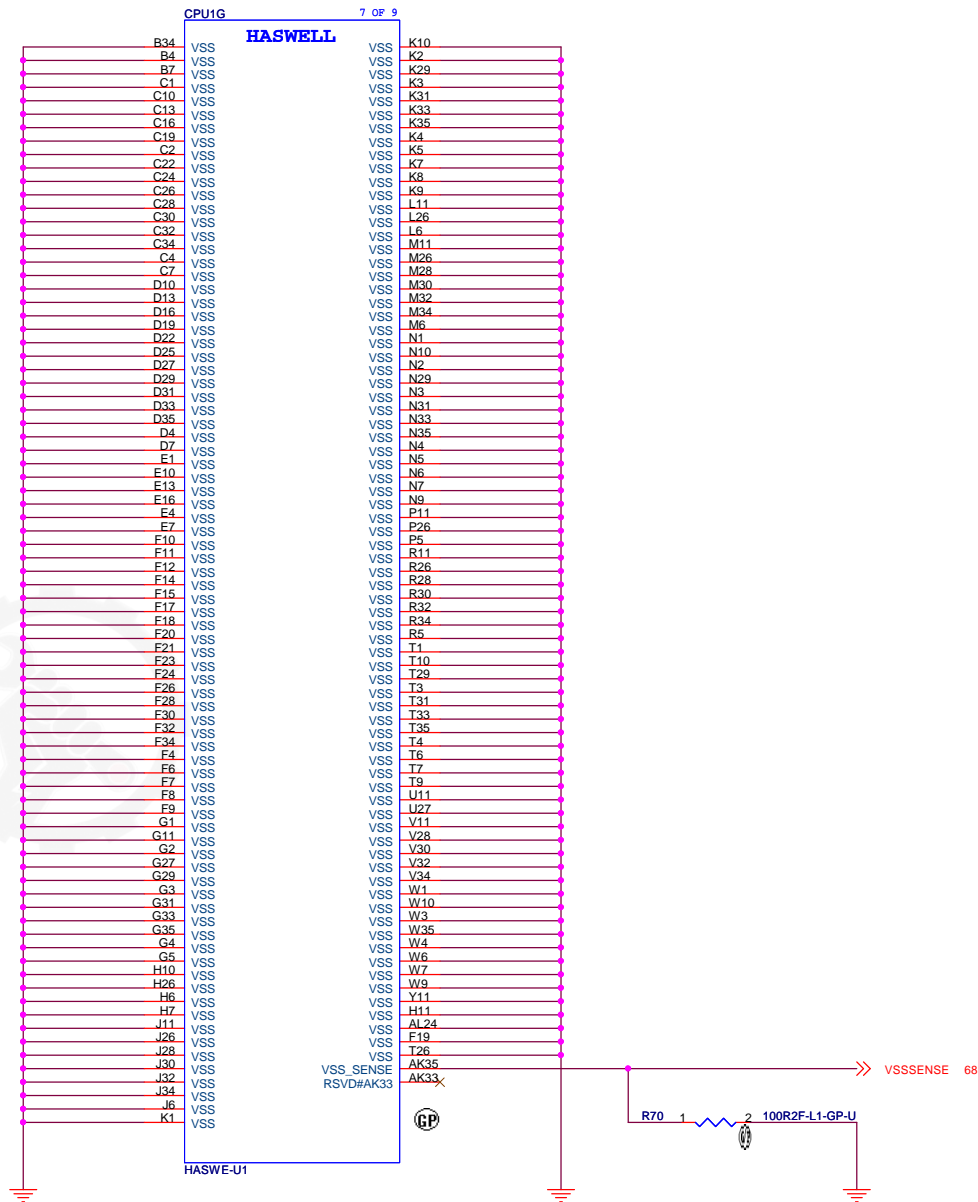
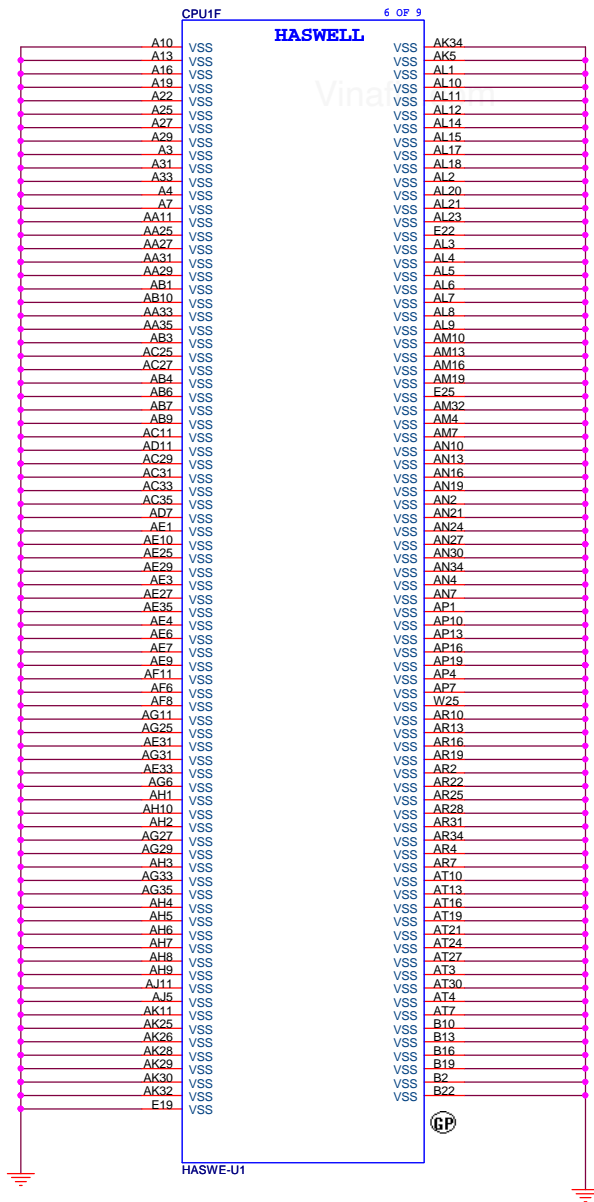
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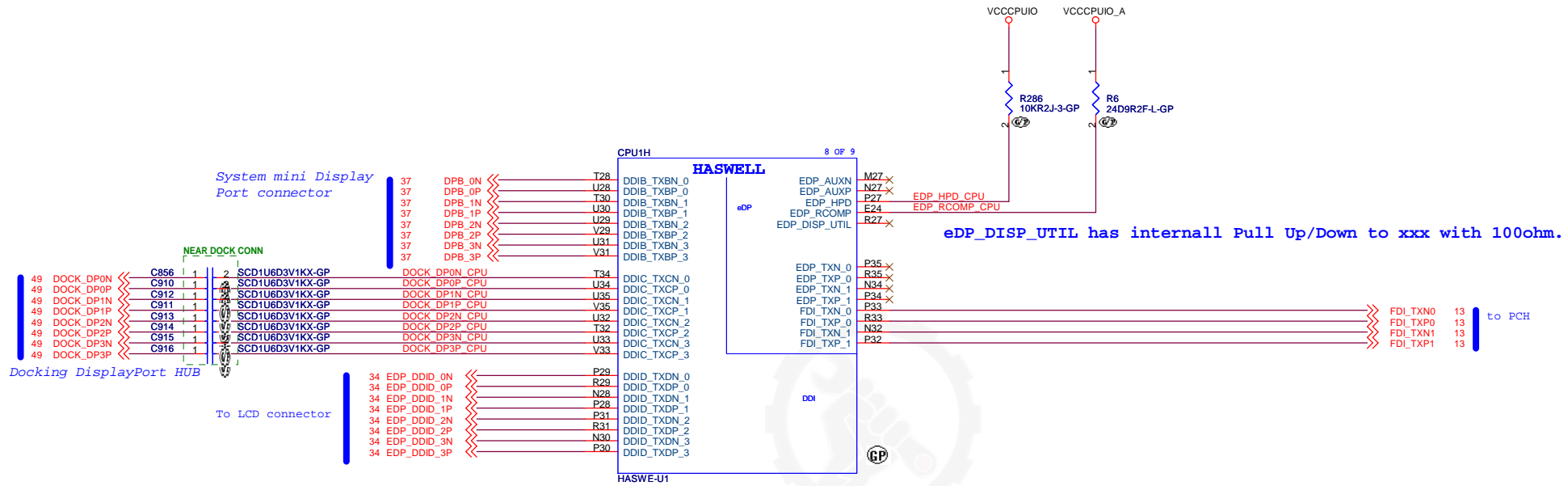
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
Haswell DDR3L CH-B 1DIMM Per Channel (4/8)

Size A3 Document Number **Kome-1 SWG2** Rev -1

Date: Thursday, September 12, 2013 Sheet 6 of 104





<Variant Name>

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title

Haswell EDP/DDI (7/8)Size
A3

Document Number

Kome-1 SWG2Rev
-1

Date: Thursday, September 12, 2013

Sheet 9 of 104

Near SW CONN: SW1 (p.011)

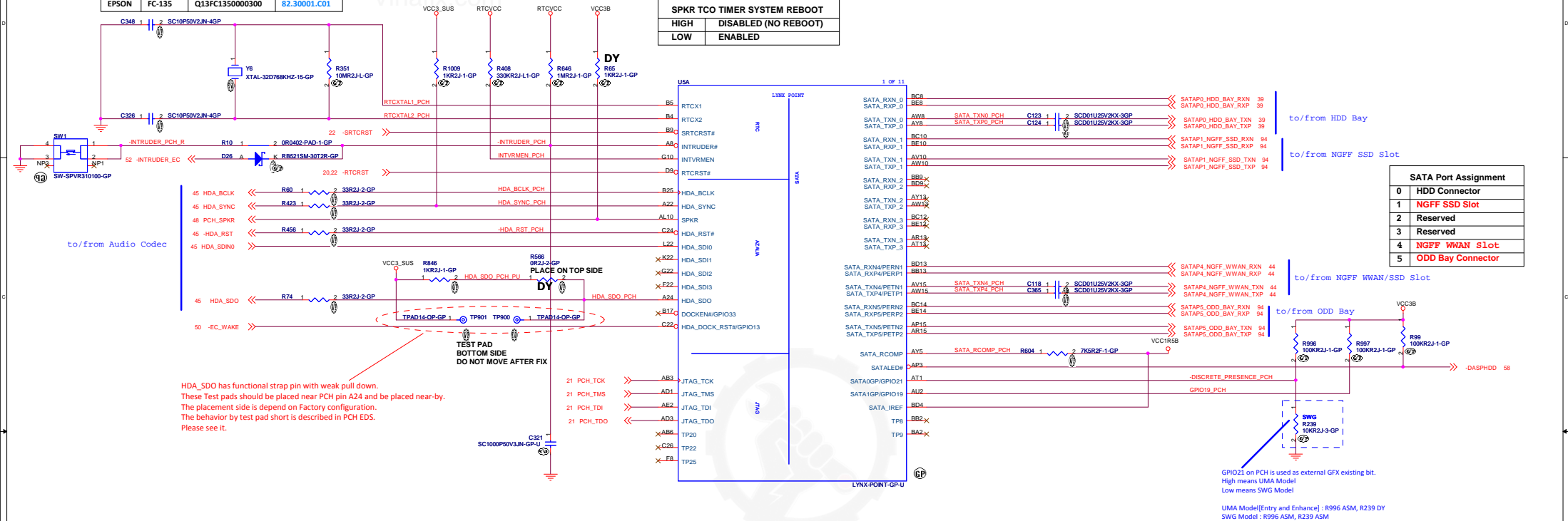


Crystal 32.768KHz 9pF 20ppm			
KDS	DST310S	1TJF090DDP1AI00T	82.30001.C21
TXC	9H T10	9H03200033	82.30001.F31
EPSON	FC-135	Q13FC1350000300	82.30001.C01

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TABLE

SPKR TCO TIMER SYSTEM REBOOT	
HIGH	DISABLED (NO REBOOT)
LOW	ENABLED



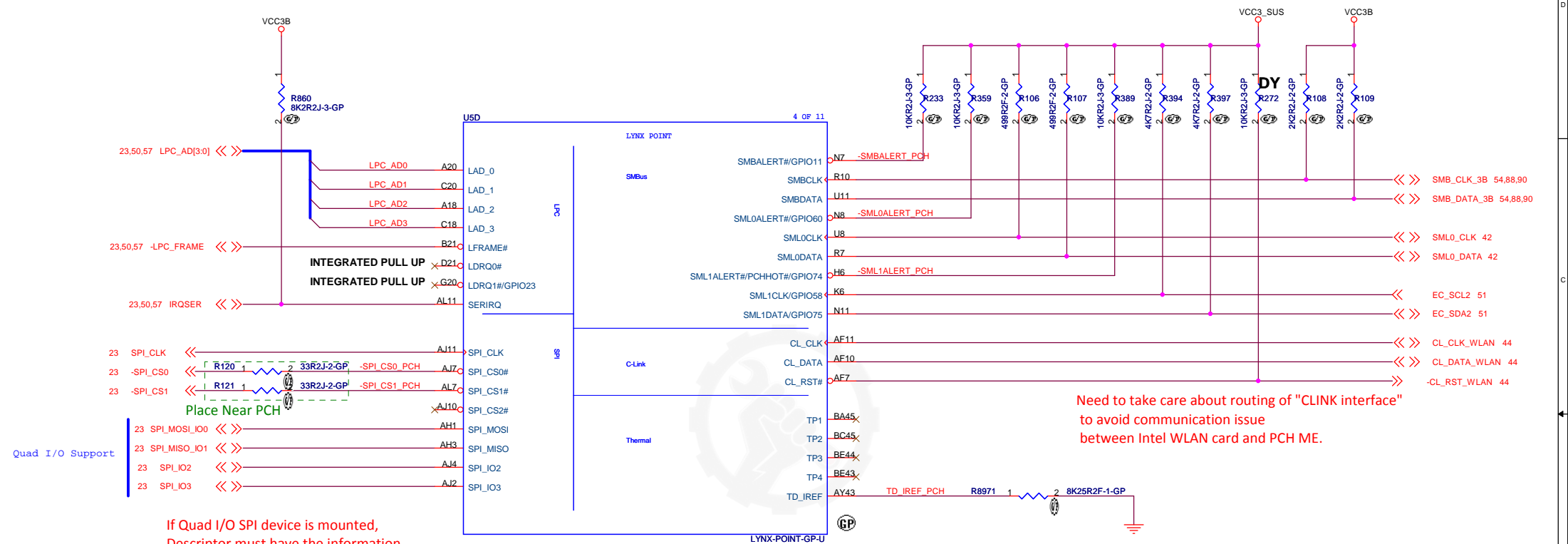
SATA Port Assignment	
0	HDD Connector
1	NGFF SSD Slot
2	Reserved
3	Reserved
4	NGFF WWAN Slot
5	ODD Bay Connector

<Variant Name>

緯創資通 Wistron Corporation	
21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsichang, Taipei Hsien 221, Taiwan, R.O.C.	
Title LynxPoint RTC/HDA/SATA/JTAG (1/10)	
Size A2	Document Number Kome-1 SWG2
Date: Thursday, September 12, 2013	Sheet 11 of 104



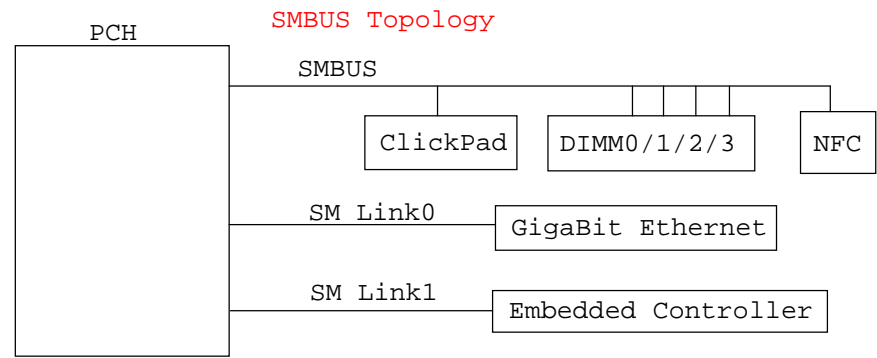
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Quad I/O Support

If Quad I/O SPI device is mounted,
Descriptor must have the information.

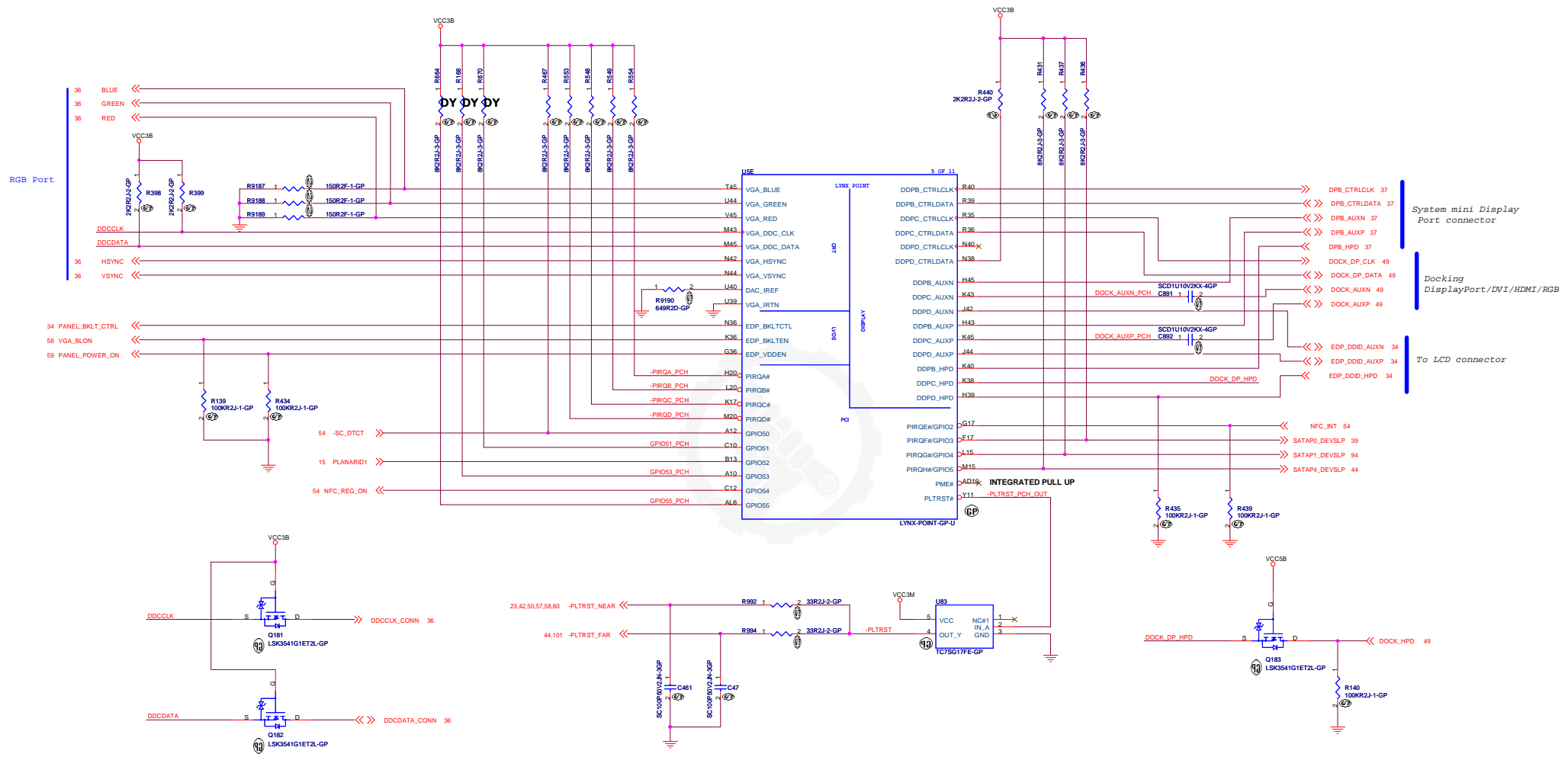
Need to take care about routing of "CLINK interface"
to avoid communication issue
between Intel WLAN card and PCH ME.





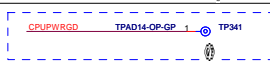


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Which function is disabled in Deep Sx state?

1. USB 2.0
2. USB 3.0
3. PCI Express
4. SATA
5. Clock
6. DMI
7. FDI
8. LPC
9. VGA
10. Display Port
11. High Definition Audio
12. SMBUS
13. CLINK
14. SPI

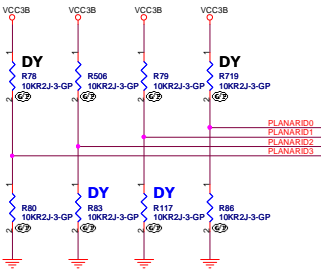
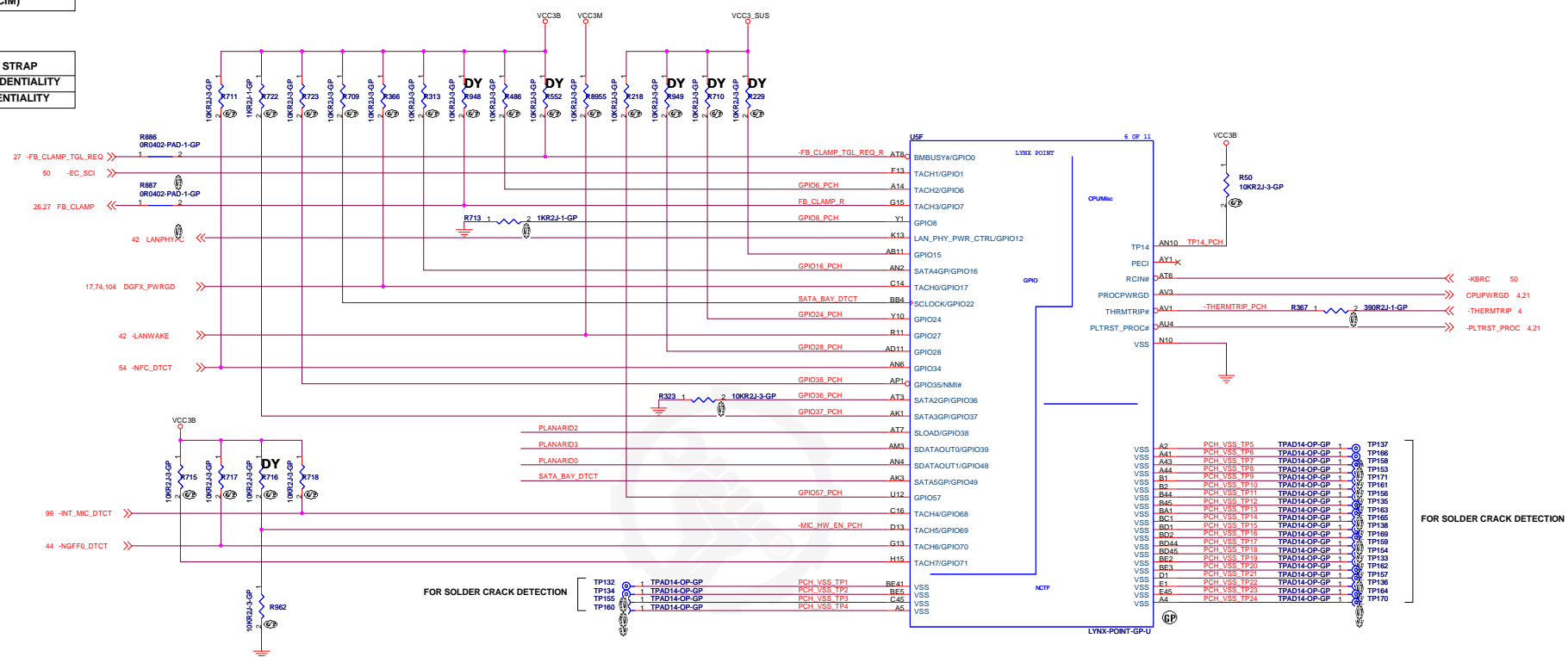


TABLE

GPIO8	INTEGRATED CLOCKING
HIGH	DISABLED(BTM)
LOW	ENABLED(FCIM)

TABLE

GPIO37	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY



TABLE

LEVEL	PLANAR ID			
	3	2	1	0
1	R78	R506	R79	R719
0	R80	R83	R117	R86

TABLE

LEVEL	PLANARID[3..0]
SDV (SWG1: Native eDP)	0000B
SDV (SWG2: DDI-D)	0001B
ME FVT	0010B
FVT	0011B
SIT	0100B
SVT	0101B
SOVP	0110B

Need to assign SubCard Detection ID Bit

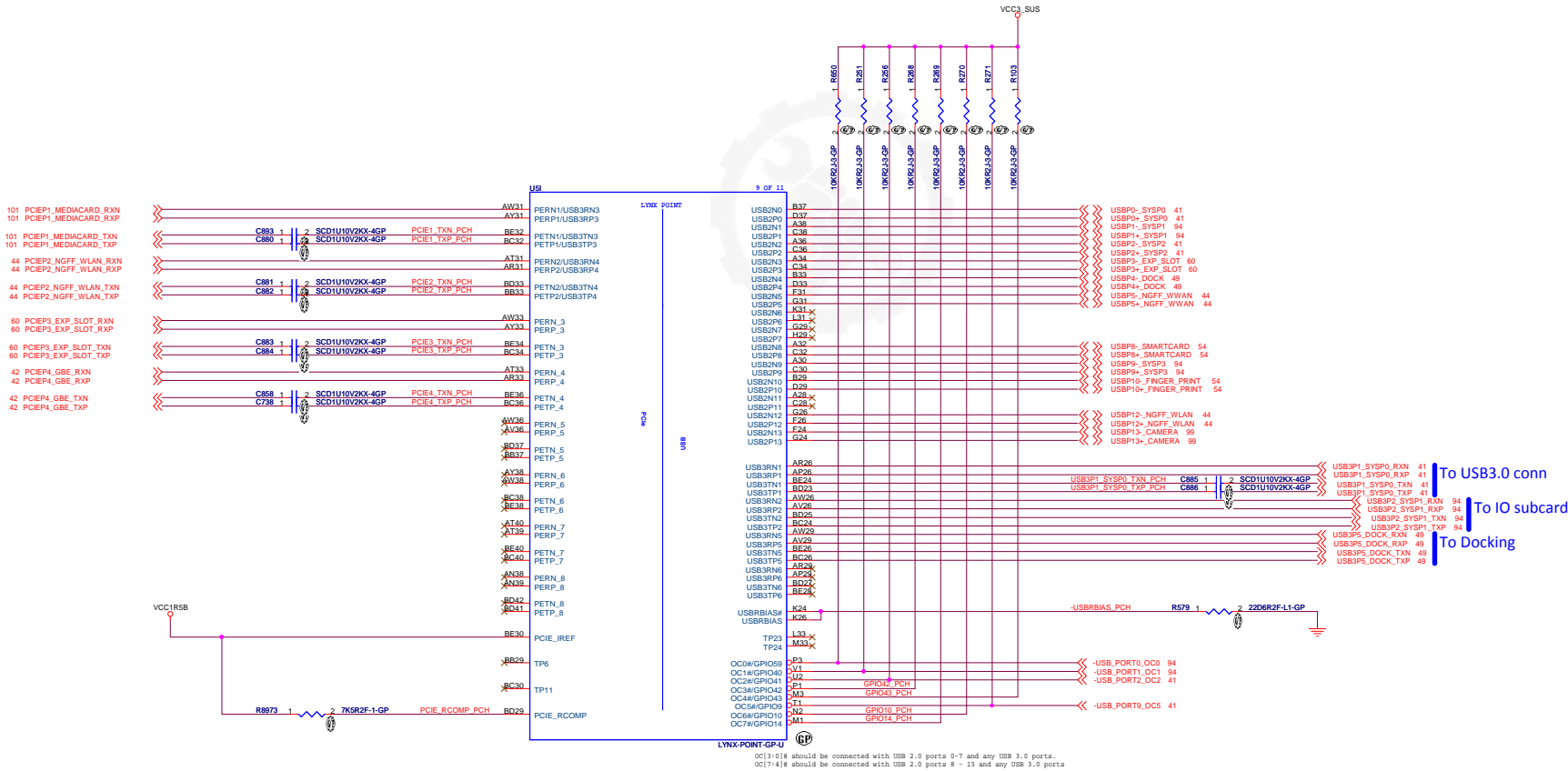
Flexible I/O Configuration			
I/O	High Speed Signals	Configuration	Net Name
Port 1	USB3 1	USB3 1 (System Port)	USB3P1_SYSP0
Port 2	USB3 2	USB3 2 (System Port)	USB3P2_SYSP1
Port 3	USB3 5	USB3 5 (Docking Port)	USB3P5_DOCK
Port 4	USB3 6	USB3 6 (Reserved)	NC
Port 5	PCIE 1/USB3 3	PCIE 1 (MediaCard)	PCIE1_MEDIACARD
Port 6	PCIE 2/USB3 4	PCIE 2 (NGFF_WLAN)	PCIEP2_NGFF_WLAN
Port 7	PCIE 3	PCIE 3 (Express slot)	PCIEP3_EXP_SLOT
Port 8	PCIE 4	PCIE 4 (GBE)	PCIEP4_GBE
Port 9	PCIE 5	PCIE 5 (Thunderbolt)	PCIEP5_THUNDER_L0
Port 10	PCIE 6	PCIE 6 (Thunderbolt)	PCIEP6_THUNDER_L1
Port 11	PCIE 7	PCIE 7 (Thunderbolt)	PCIEP7_THUNDER_L2
Port 12	PCIE 8	PCIE 8 (Thunderbolt)	PCIEP8_THUNDER_L3
Port 13	SATA 4/PCIE 1	SATA 4(NGFF_WLAN)	SATA4_NGFF_WWAN
Port 14	SATA 5/PCIE 2	SATA 5 (ODD Bay)	SATAP5_ODD_BAY
Port 15	SATA 0	SATA 0 (HDD Bay)	SATAP0_HDD_BAY
Port 16	SATA 1	SATA 1 (NGFF_SSD)	SATAP1_NGFF_SSD
Port 17	SATA 2	SATA 2 (Reserved)	NC
Port 18	SATA 3	SATA 3 (Reserved)	NC

PCIe Port Assignment	
1	Media Card Controller
2	NGFF WLAN Slot
3	EXPress Slot
4	GbE PHY
5	NC
6	NC
7	NC
8	NC

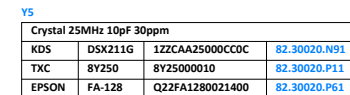
USB 3.0 Port Assignment	
1	USB 3.0 System Port 0
2	USB 3.0 System Port 1 (Debug)
3	Reserved
4	Reserved
5	USB 3.0 Docking IF
6	Reserved

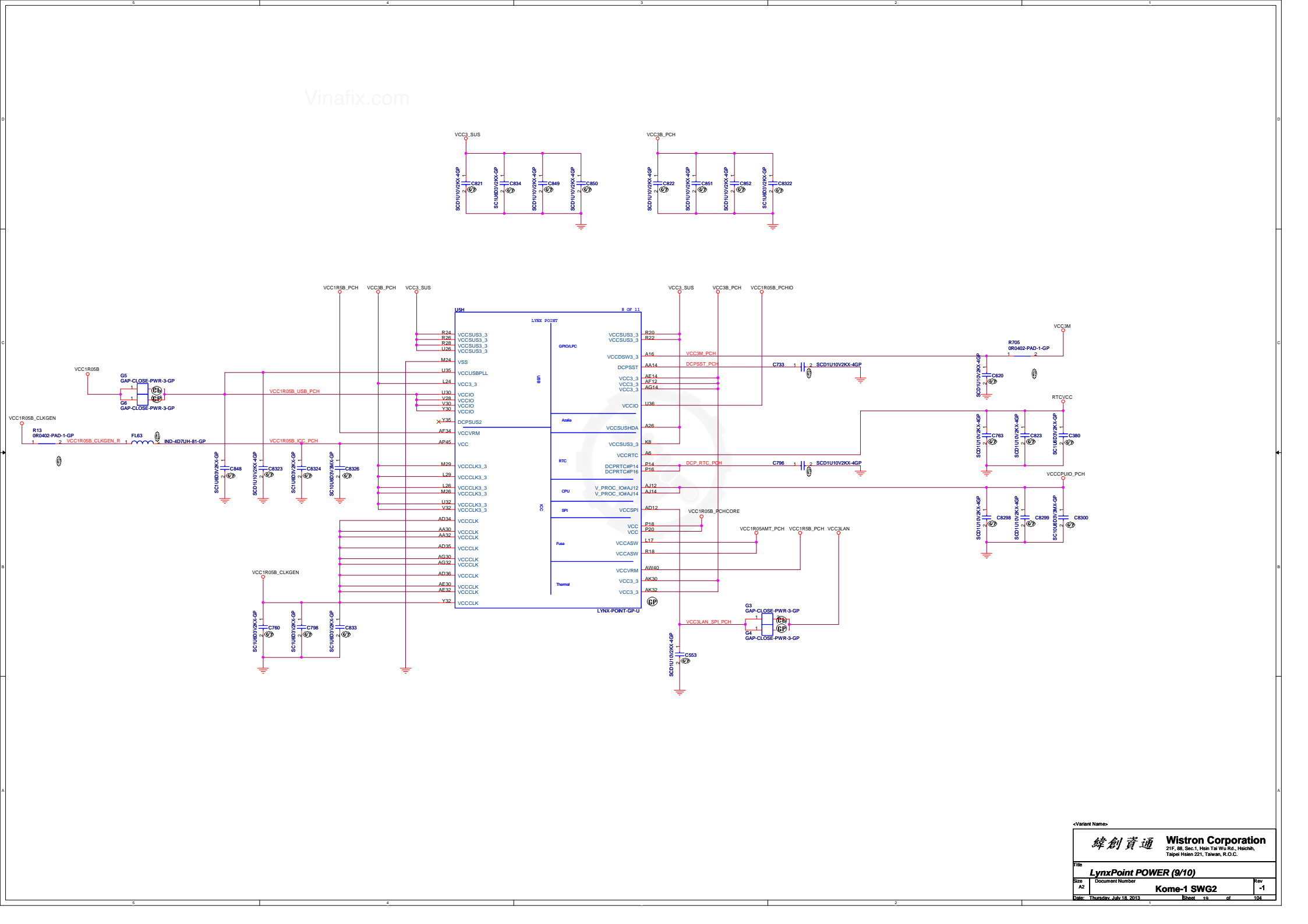
USB Port Assignment	
0	USB 2.0 System Port 0
1	USB 2.0 System Port 1(Debug)
2	USB 2.0 System Port 2 (AOU)
3	Express Slot
4	USB 2.0 Docking
5	NGFF WWAN Slot
6	Color Sensor [WS Model only]
7	Multi Touch (WS only)
8	Smart Card Slot
9	USB 2.0 System Port3
10	Fingerprint Reader
11	Reserved
12	NGFF WLAN Slot (bluetooth)
13	USB Camera in LCD

SATA Port Assignment	
0	HDD Connector
1	NGFF SSD Slot
2	Reserved
3	Reserved
4	NGFF WWAN Slot
5	ODD Bay Connector

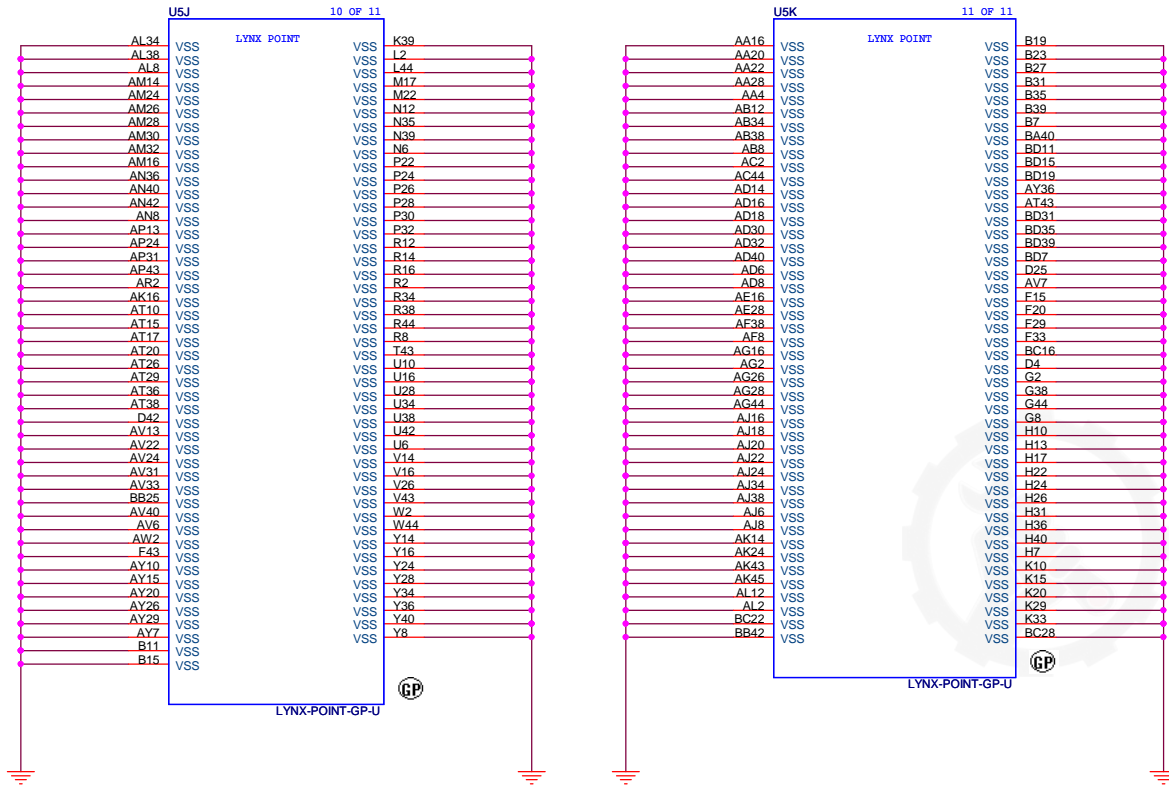


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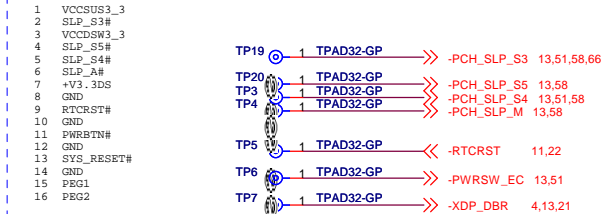




TEST PAD FOR METS/APS



APS interface for the Software ME validation



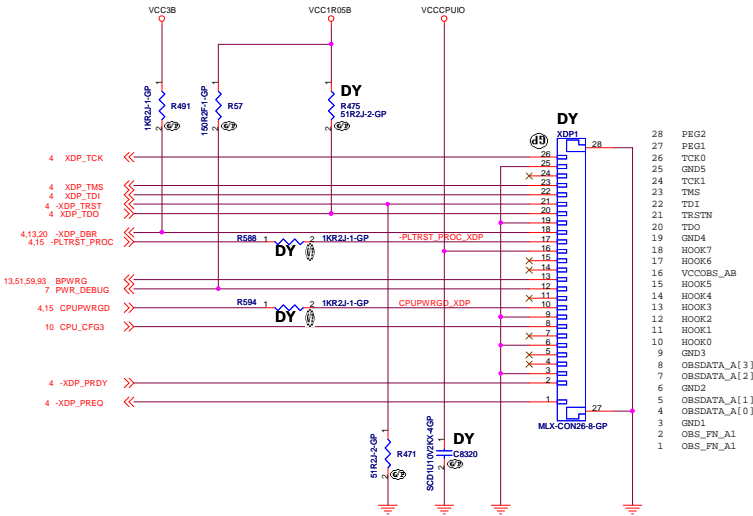
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Taipei Hsien 221, Taiwan, R.O.C.

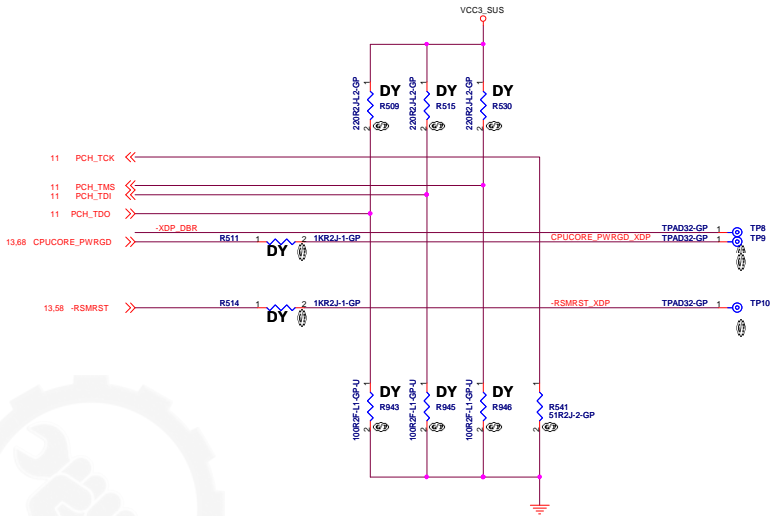
Title		LynxPoint GND (10/10)	
Size	A3	Document Number	Kome-1 SWG2
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Rev	-1		

TCK has external Pulldown(51ohm) on CPU page.
TMS and TDI has internal pull up in CPU Module.
TDO should have external pullup(51ohm) near Debug port.
TRST# should have external pulldown(51ohm) near debug port.
HOOK7(DBR) needs to connect to PCH_SYS_RESET# with xternal pullup.
HOOK6(RESET#) needs to connect to RESET# with 1K series.
HOOK3 needs to connect to PCH_SYS_PWROK.
HOOK2 needs to connect to PWR_DEBUG in CPU with external pullup(150ohm).
HOOK1 is optional
HOOK0(PWRGOOD) needs to connect system power good. with 1K series.
OBSDATA_A[3] must be connected to CPU_CFG[3].

XDP-SFF-26pin Interface



- 28 PEG2
- 27 PEG1
- 26 TCK0
- 25 GND5
- 24 TCK1
- 23 TMS
- 22 TDI
- 21 TRSTN
- 20 TDO
- 19 GND4
- 18 HOOK7
- 17 HOOK6
- 16 VCCOBS_AB
- 15 HOOK5
- 14 HOOK4
- 13 HOOK3
- 12 HOOK2
- 11 HOOK1
- 10 HOOK0
- 9 GND3
- 8 OBSDATA_A[3]
- 7 OBSDATA_A[2]
- 6 GND2
- 5 OBSDATA_A[1]
- 4 OBSDATA_A[0]
- 3 GND1
- 2 OBS_FN_A1
- 1 OBS_FN_A0



- 28 PEG2
- 27 PEG1
- 26 TCK0
- 25 GND5
- 24 TCK1
- 23 TMS
- 22 TDI
- 21 TRSTN
- 20 TDO
- 19 GND4
- 18 HOOK7
- 17 HOOK6
- 16 VCCOBS_AB
- 15 HOOK5
- 14 HOOK4
- 13 HOOK3
- 12 HOOK2
- 11 HOOK1
- 10 HOOK0
- 9 GND3
- 8 OBSDATA_A[3]
- 7 OBSDATA_A[2]
- 6 GND2
- 5 OBSDATA_A[1]
- 4 OBSDATA_A[0]
- 3 GND1
- 2 OBS_FN_A1
- 1 OBS_FN_A0

TABLE NOTE: XDP1 "ASM" FOR PDV/SDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
PWR_DEBUG	R57	ASM	ASM
XDP1.P16	C8320	ASM	NO ASM
	XDP1	ASM	NO ASM

↑
LOGIC

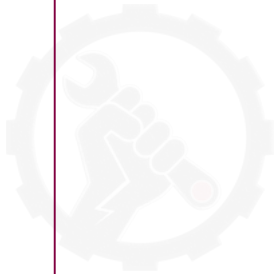
TABLE NOTE: XDP2 "ASM" FOR PDV/SDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509	220	NO ASM
	R943	100	NO ASM
TMS	R530	220	NO ASM
	R946	100	NO ASM
TDI	R515	220	NO ASM
	R945	100	NO ASM
TCK	R541	51	51
CPUCORE_PWRGD	R511	ASM	NO ASM
-RSMRST	R514	ASM	NO ASM
	XDP2	ASM	NO ASM

↑
LOGIC

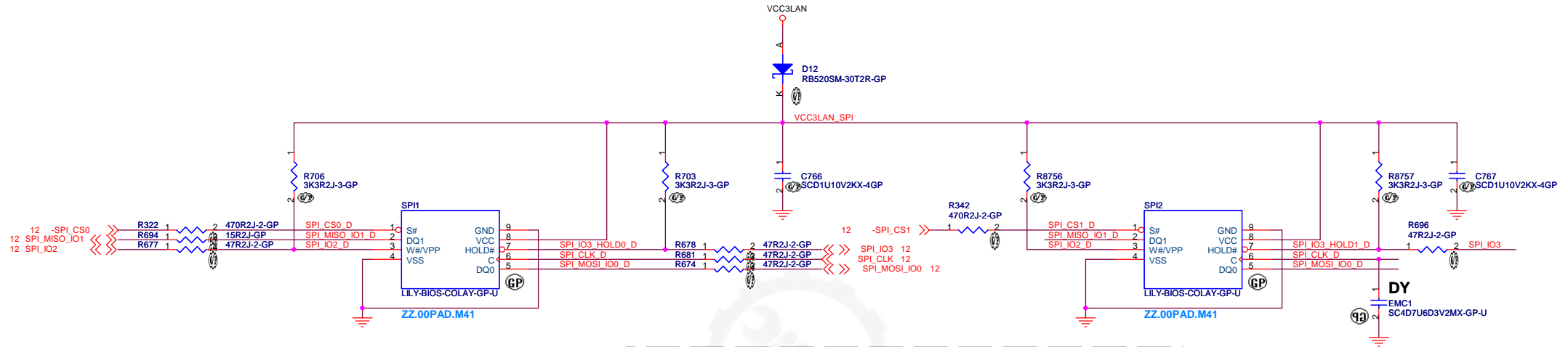
Near RTC BAT CONN: RTC1 (p.022)

The diagram shows a red wire labeled **RTC_CONN1** connected to a ground symbol. A blue wire labeled **AFTE14P-GP** is connected to a terminal block with two pins, both labeled **1**. The terminal block is connected to **AFTP1** and **AFTP2**. A circular logo with the text "Vina.com" is visible in the background.



緯創資通

Title			
RTC BATTERY			
Size	Document Number	Rev	
A4	Kome-1 SWG2	-1	
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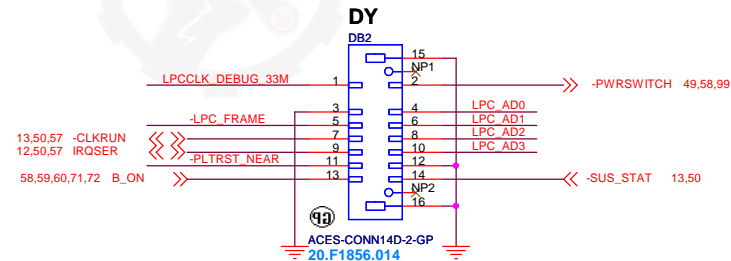
64Mbits SPI FLASH (SPI1) :

Package	Supplier	Vendor P/N	Lenovo P/N	Wistron P/N
SO8	Macronix	MX25L6473EM2I-10G		72.25647.00A
	Winbond	W25Q64FVSSIQ		72.25Q64.K01
	EON	EN25QH64-104HIP		72.02564.001
	MICRON	N25Q064A13ESEC0F		72.25Q64.G01

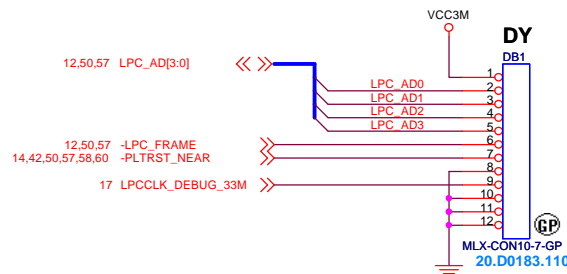
32Mbits SPI FLASH (SPI2) :

Package	Supplier	Vendor P/N	Lenovo P/N	Wistron P/N
SO8	Macronix	MX25L3273EM2I-10G		72.25327.A01
	Winbond	W25Q32FVSSIQ		72.25Q32.H01
	EON	EN25QH32-104HIP		72.02532.B01
	MICRON	N25Q032A13ESEC0F		72.25Q32.C01

Lenovo Debug Tool I/F



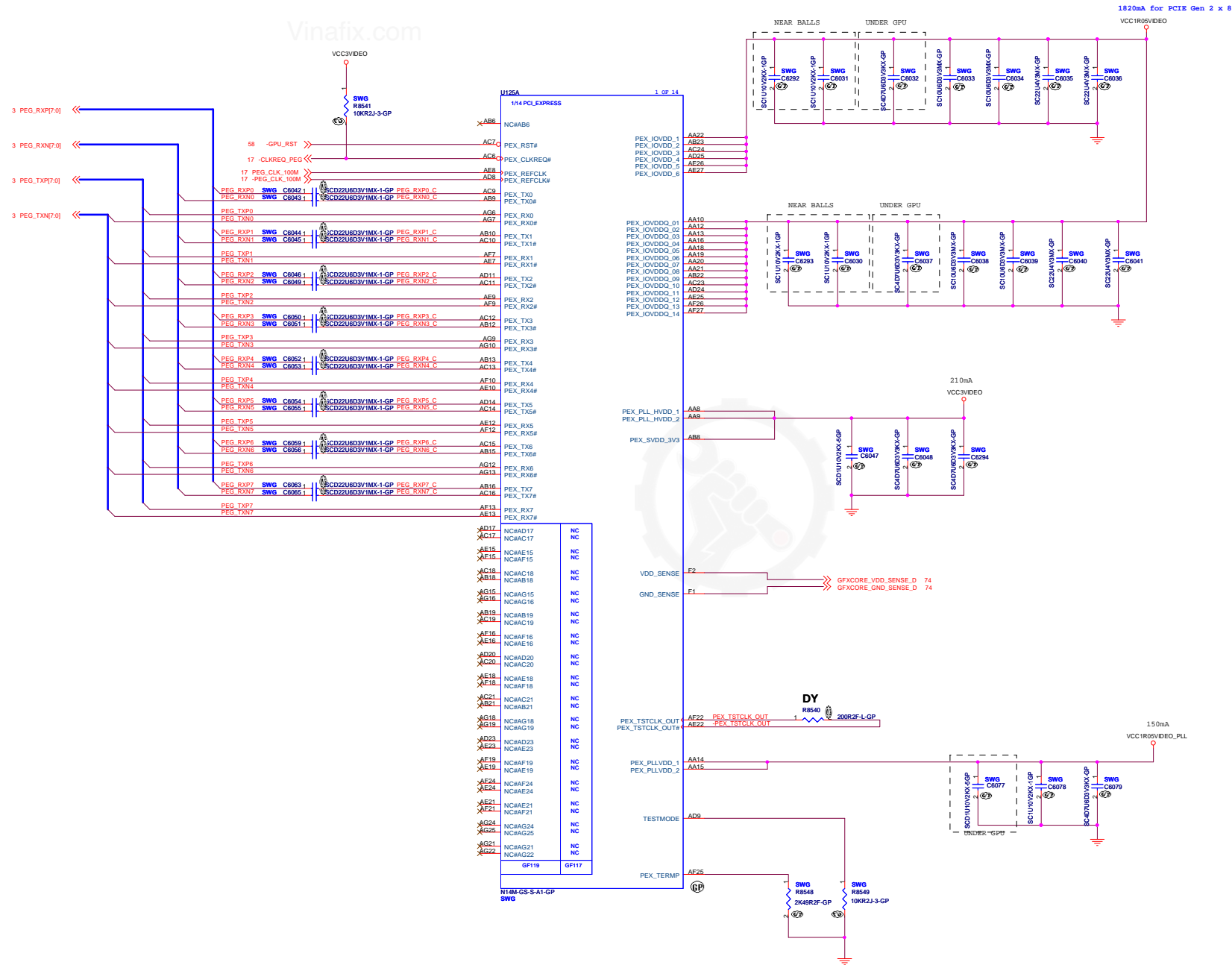
LPC for Debug Card Connector

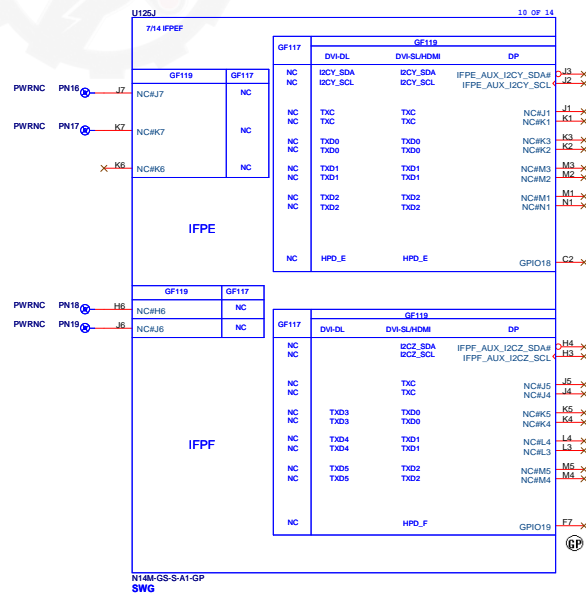
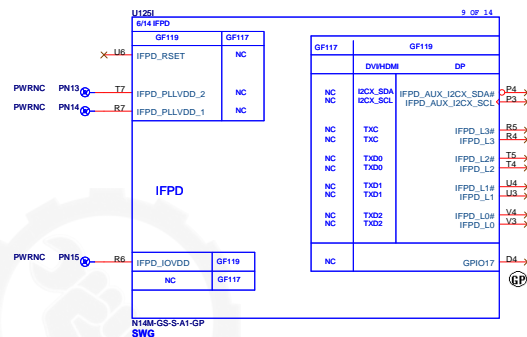
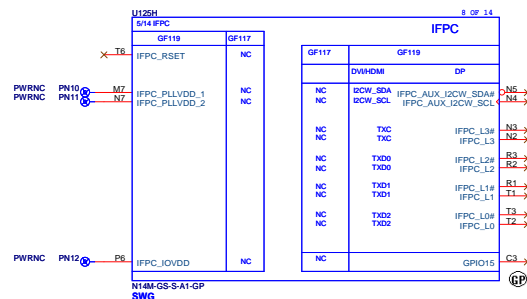
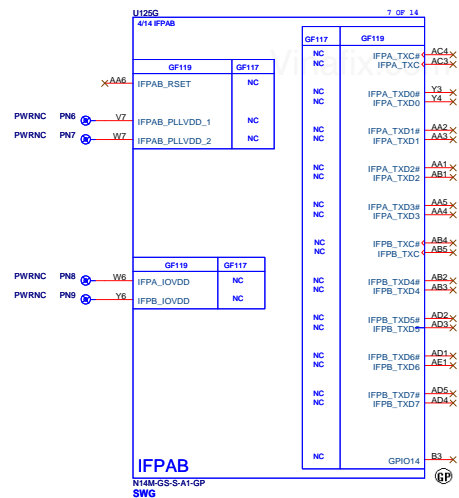


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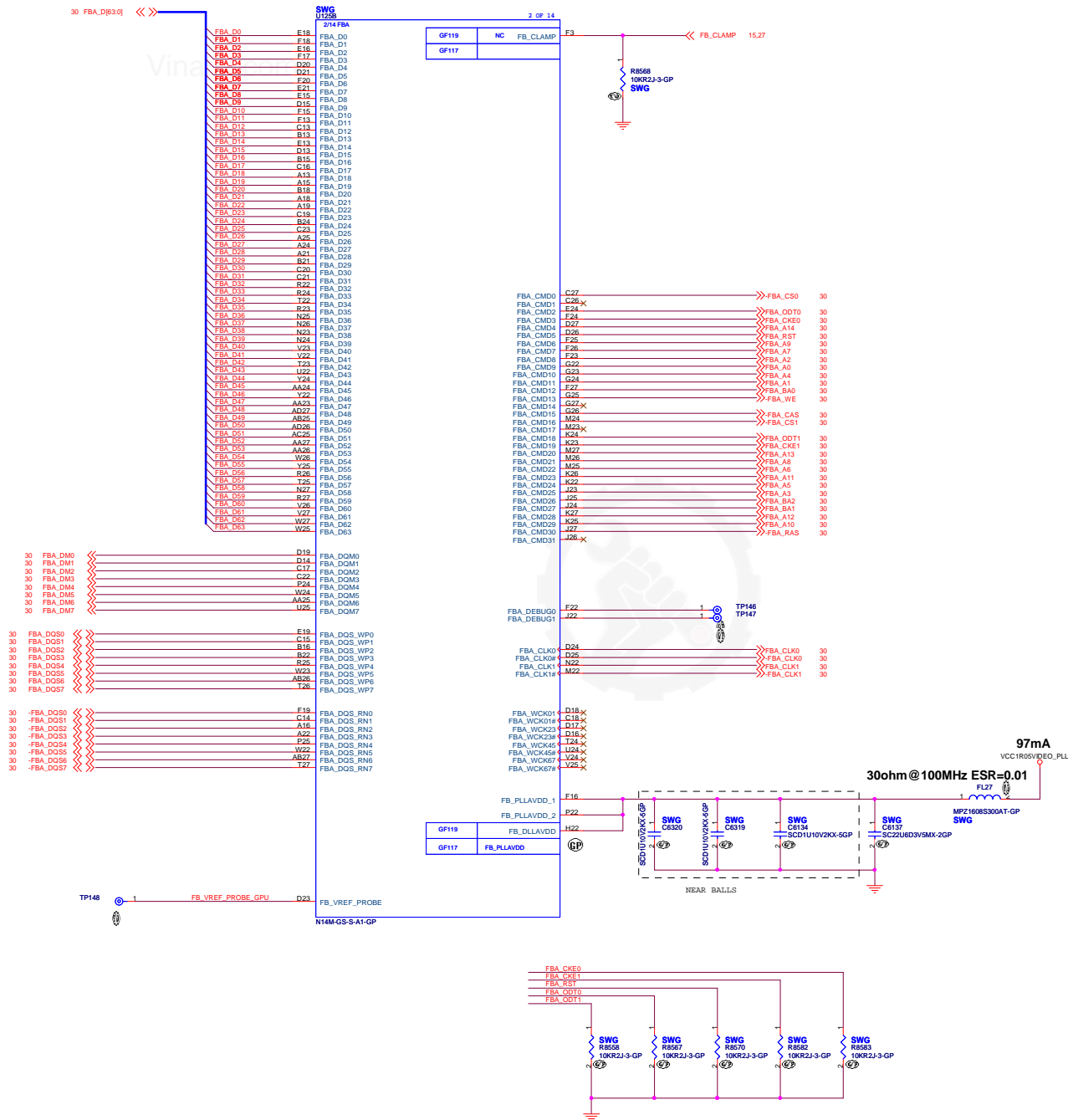
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	SPI Flash, DEBUG PORT		
Size	Document Number	Rev	
A3		-1	
Date:	Thursday, September 12, 2013	Sheet	23 of 104

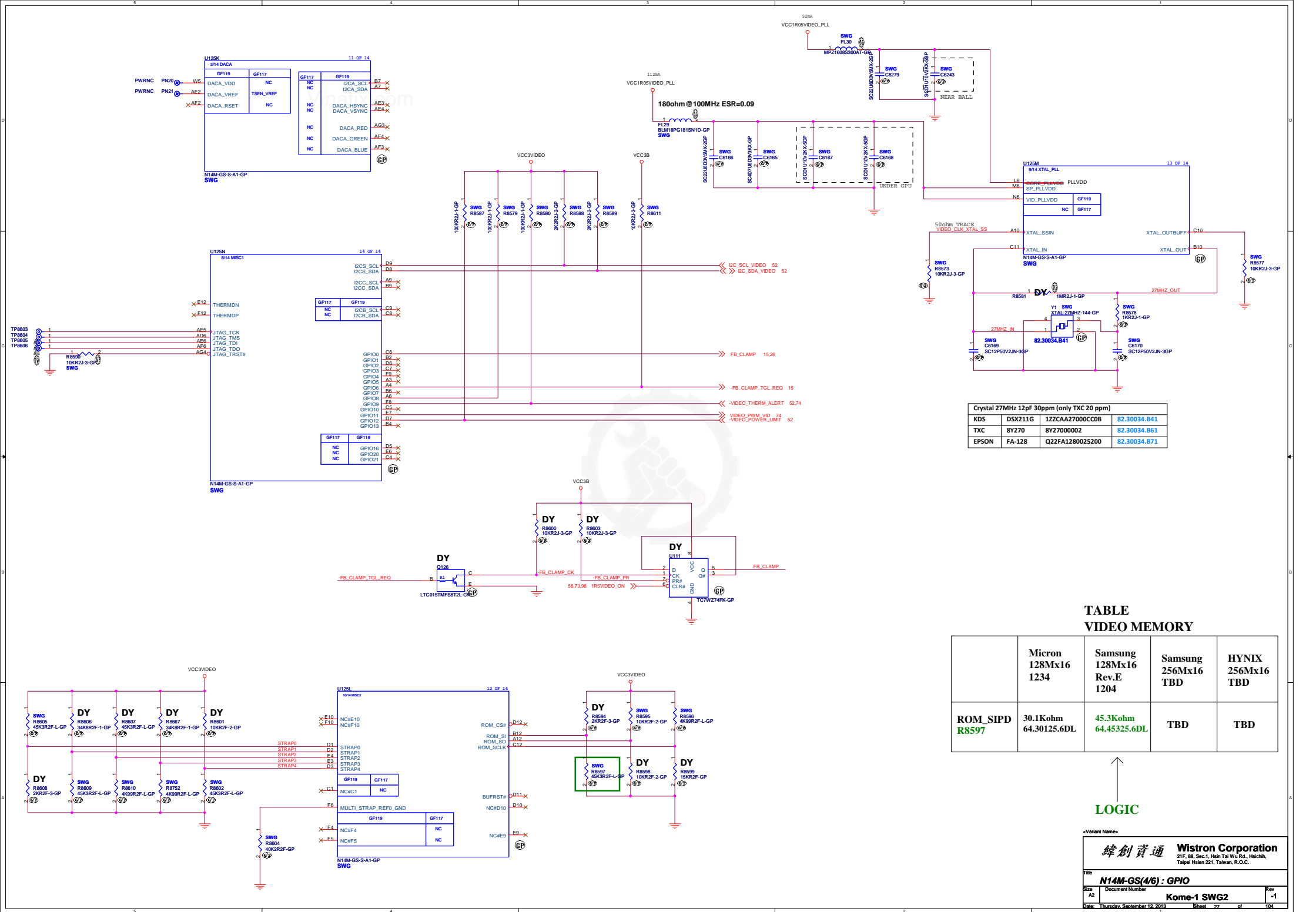




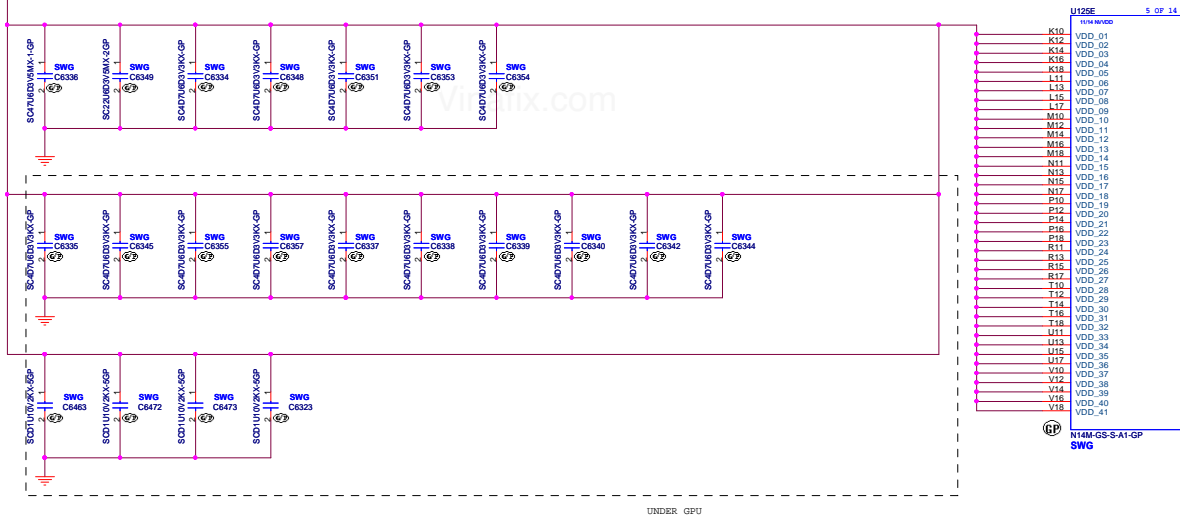
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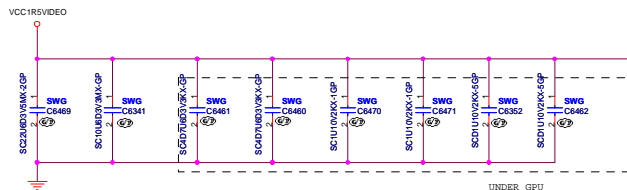
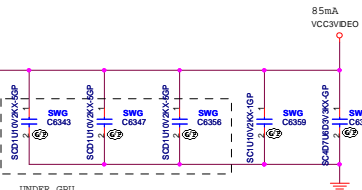
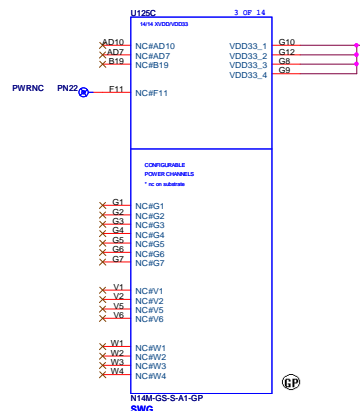


VCCGFXCORE_D

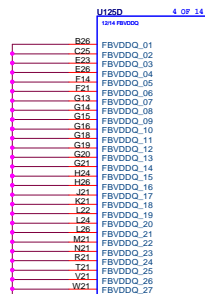


UNDER GPU

85mA VCCSVVIDEO



UNDER GPU



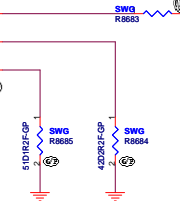
FB_CAL_PD_VDDQ
FB_CAL_PU_GND
FB_CAL_TERM_GND

N14M-GS-S-A1-GP
SWG

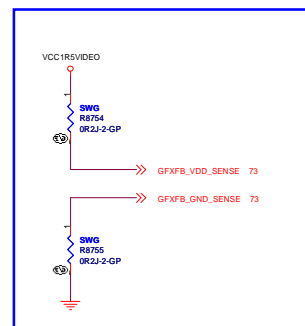
D22

C24

B25



VCC1RSVIDEO

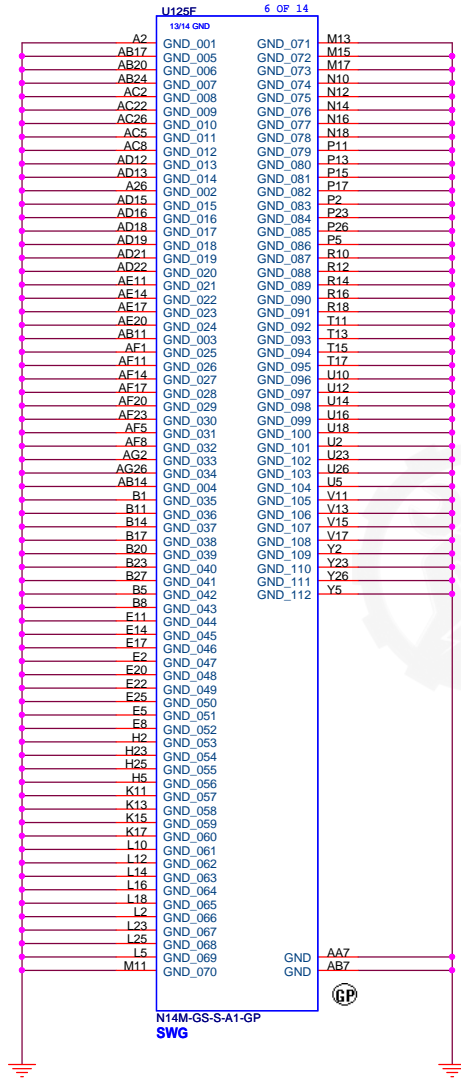


PLACE NEAR GPU

<Variant Name>

緯創資通 Wistron Corporation
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsuehshuei, Taipei Hsien 221, Taiwan, R.O.C.

File			N14M-GS(5/6) : POWER
Size	Document Number		Rev
A2	Kome-1 SWG2		-1
Date:	Thursday, September 12, 2013	Sheet	28 of 104



<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

N14M-GS(6/6) : GND

Size
A3

Document Number

Kome-1 SWG2

Rev
-1

Date: Thursday, July 18, 2013

Sheet 29 of 104

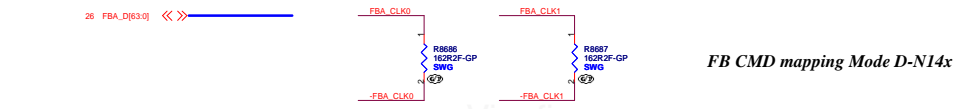
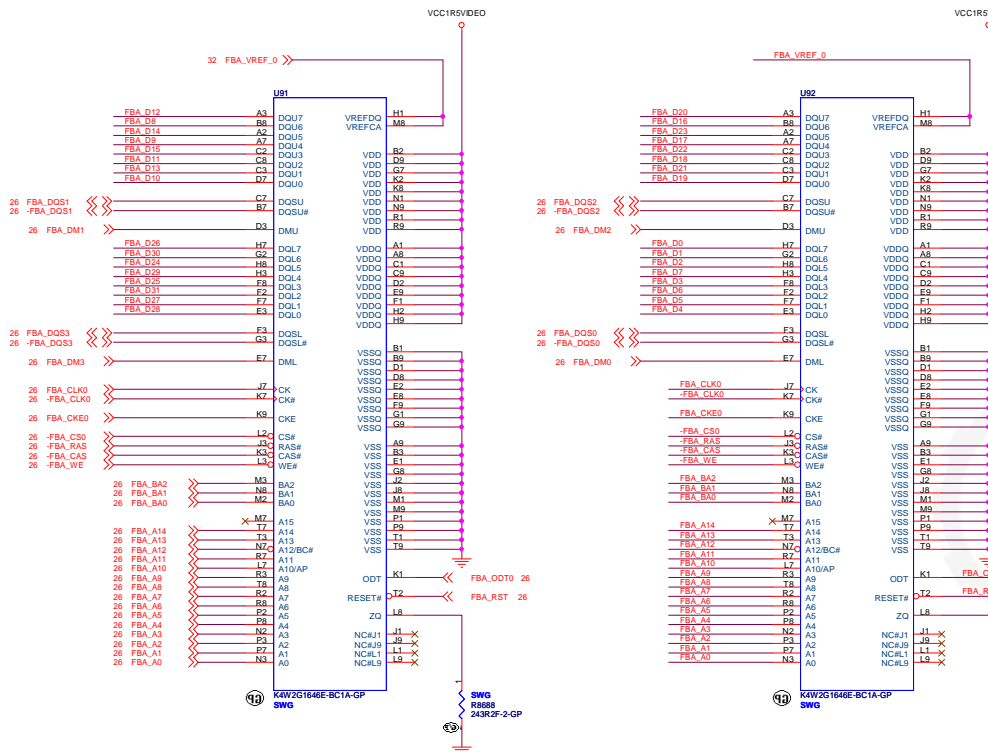


TABLE
DDR3 VIDEO MEMORY

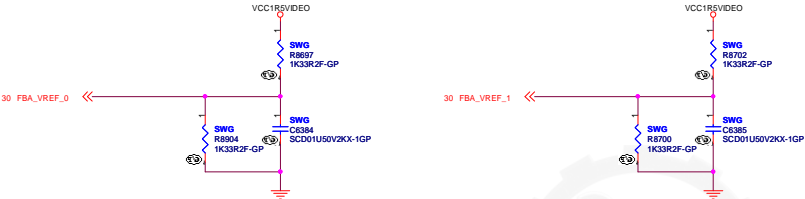
	Micron 2GBITS (128Mx16)	SAMSUNG 2GBITS (128Mx16)	SAMSUNG 4GBITS (256Mx16)	HYNIX 4GBITS (256Mx16)
U91				
U92	MT41J128M16JT-093G:K	K4W2G1646E-BC1A	K4W4G1646B-HC11	TBD
U93	72.41128.10U	72.41264.NOU	72.41646.00U	
U94	OC51633AA	OC51505AA		

LOGIC



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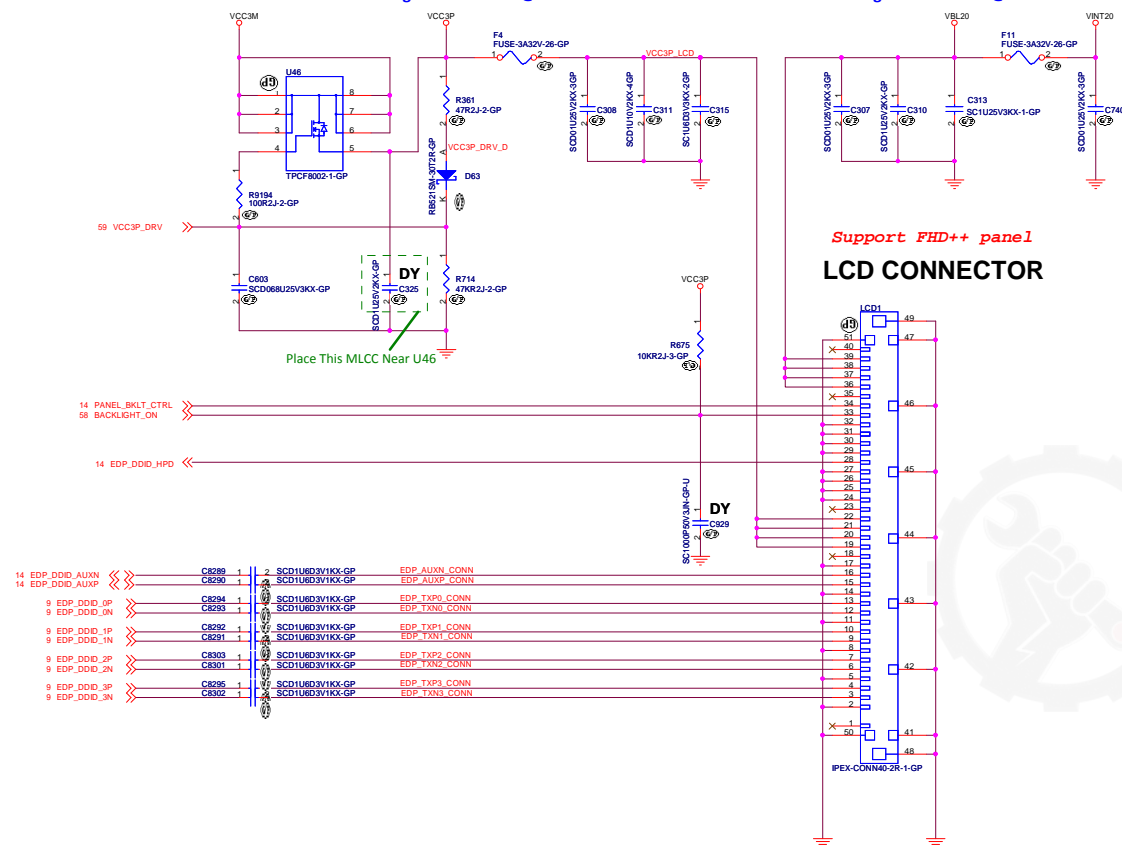
VBL20
 VCC3P
 4 PANEL_BKLT_CTRL
 BACKLIGHT_ON
 EDP_D0D1_HPD
 VCC3P_LCD

AFTE14P-GP 1
 TPAD14-OP-GP 1
 AFTE14P-GP 1
 AFTE14P-GP 1
 AFTE14P-GP 1
 AFTE14P-GP 1
 AFTE14P-GP 1

AFTP3
 TP338
 AFTP4
 AFTP5
 AFTP6
 AFTP7
 AFTP8

Max Average Current 0.9A @ 3.0V

Max Average Current 1.3A @ 6.0V



Support FHD++ panel

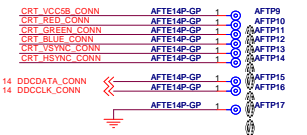
LCD CONNECTOR

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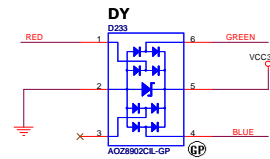


<Variant Name>		
<div><div>緯創資通</div><div>Wistron Corporation</div><div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
Title		
eDP SWITCH		
Size	Document Number	Rev
A3	Kome-1 SWG2	-1
Date:	Thursday, July 18, 2013	Sheet 35 of 104

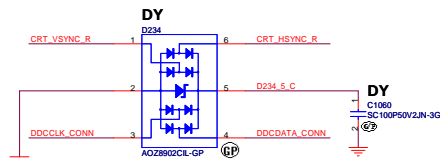
Near CRT CONN: CRT1 (p.036)



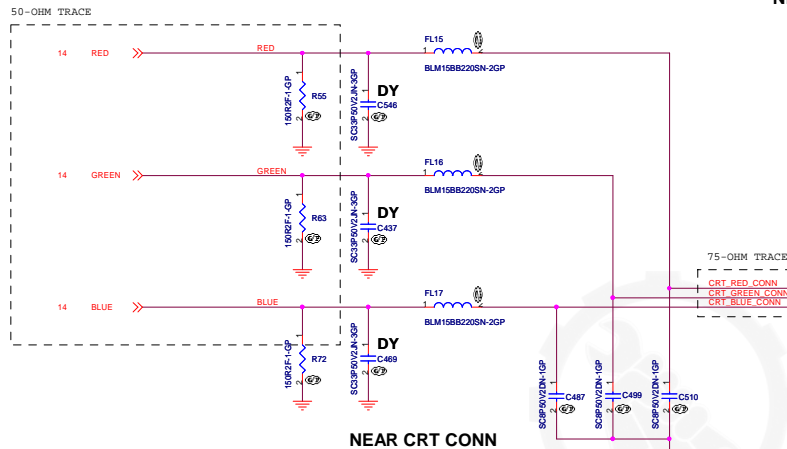
Vinafix.com



NEAR CRT CONN

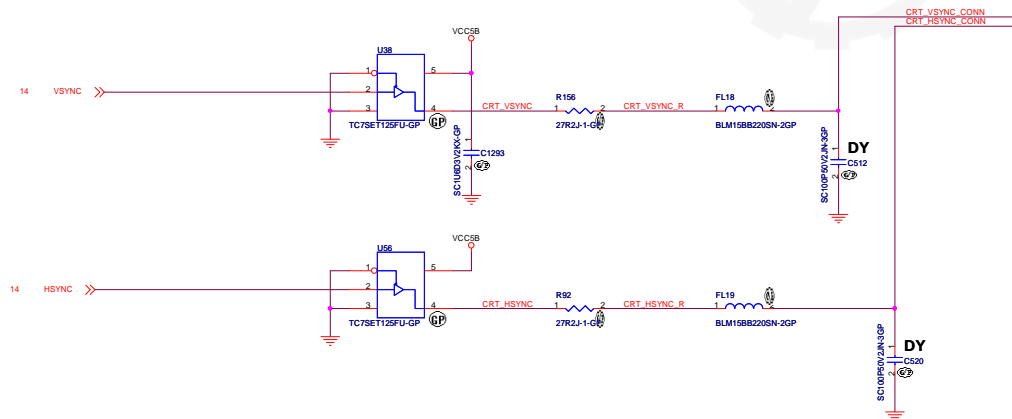
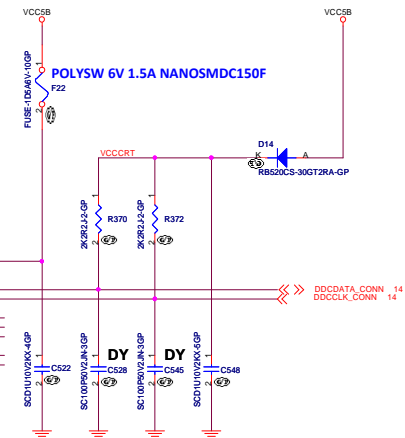
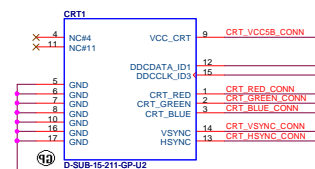


NEAR CRT CONN



NEAR CRT CONN

CRT CONNECTOR

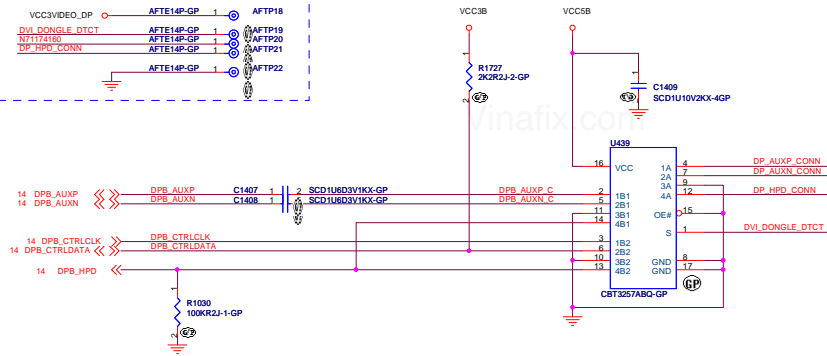
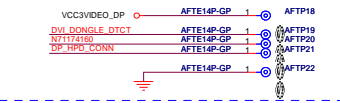


<Variant Name>

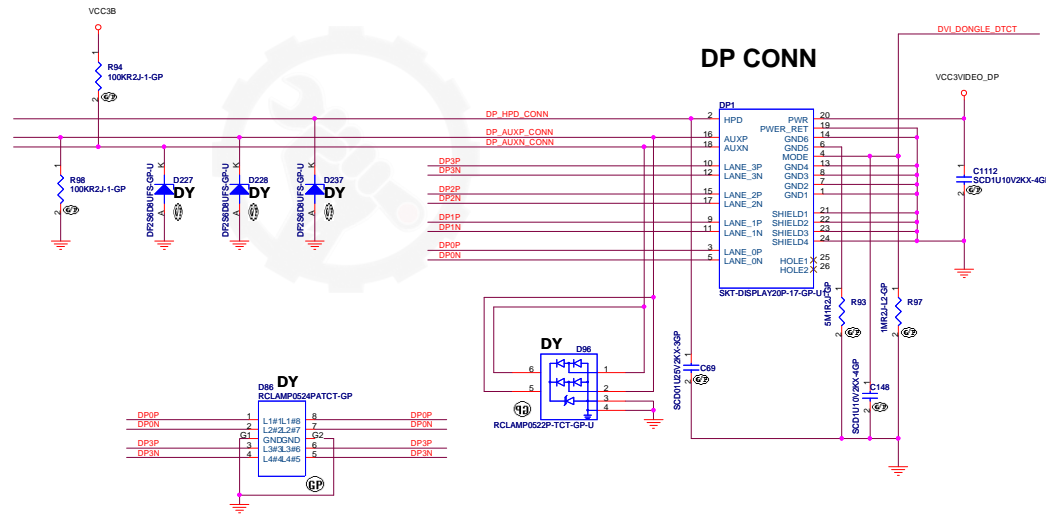
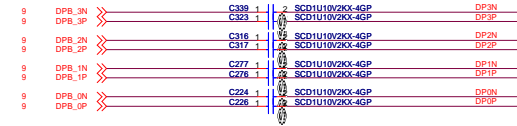
緯創資通 Wistron Corporation
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsichu, Taipei Hsien 221, Taiwan, R.O.C.

EXT CRT INTERFACE		
Size A2	Document Number	Rev -1
Kome-1 SWG2		
Date: Thursday, September 12, 2013	Sheet 36	of 104

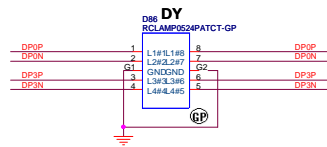
Near THUNDERBOLT CONN: DP1 (p.037)



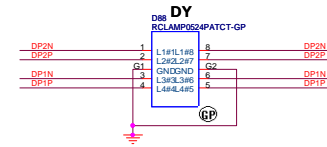
FOR SYSTEM DP NEAR DP CONN



NEAR DP CONN

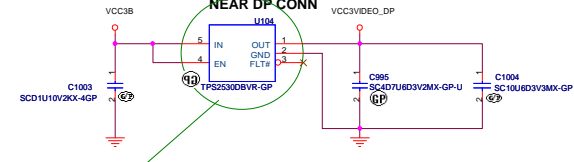


NEAR DP CONN



Typ. 500mA
Max 1.5A

NEAR DP CONN

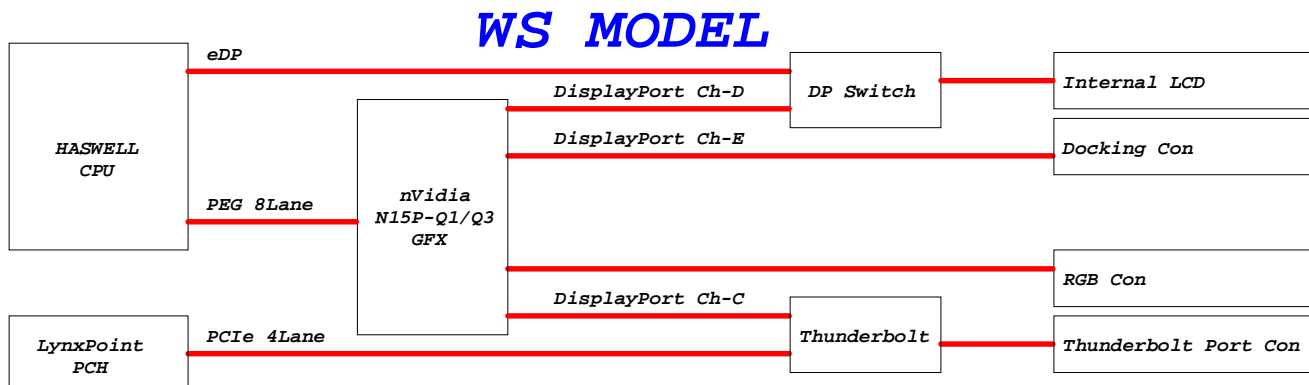
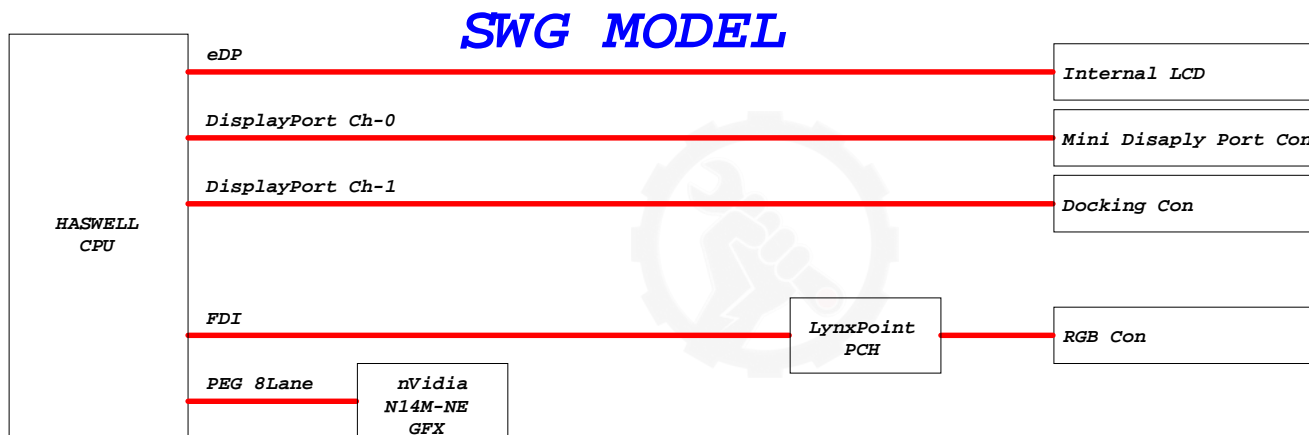
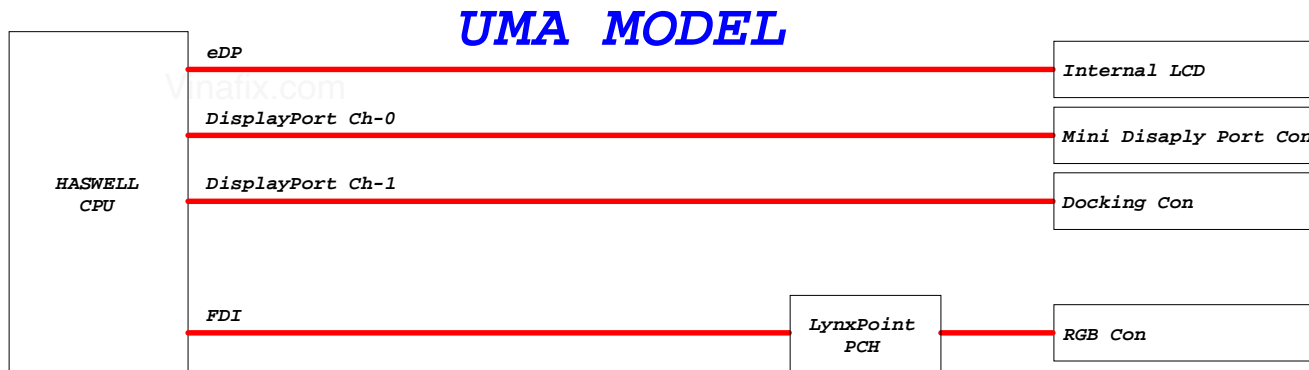


U104			
1st	TI	TP82051BDBVR	74.02051.07F
2nd	TI	TP82530DBVR	74.02530.07F
3rd	PERICOM	PI5PD2051BTAEX	74.52051.07F

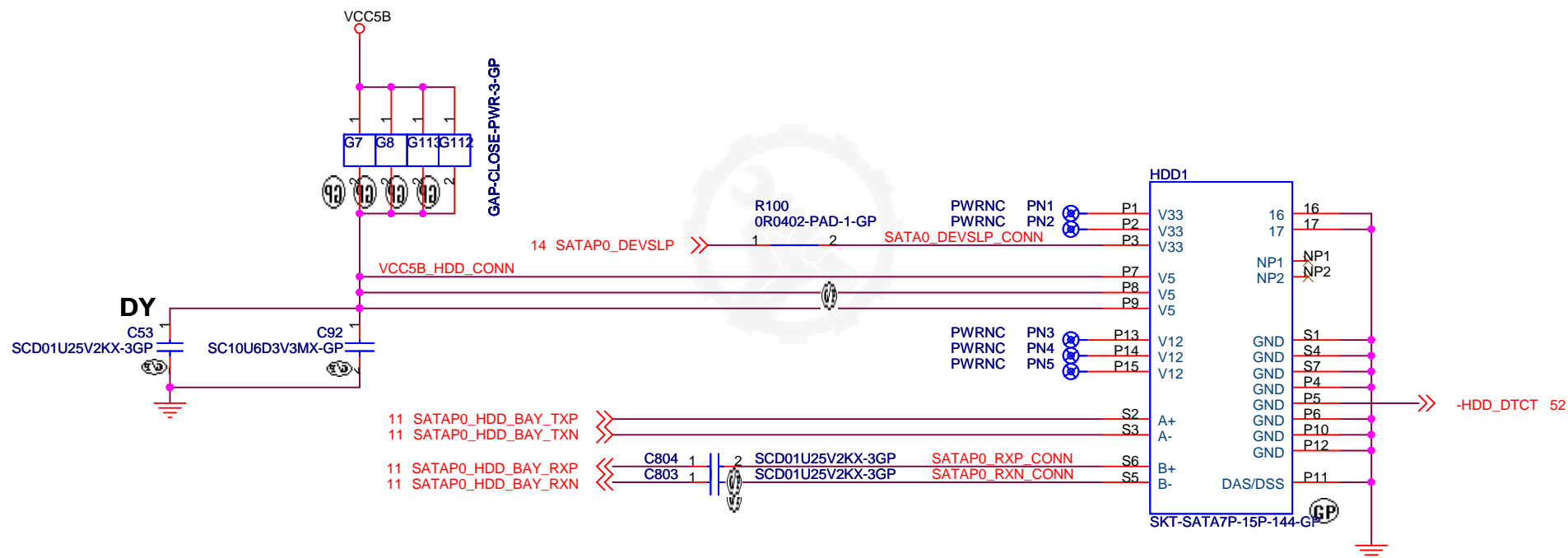
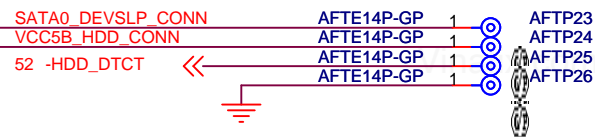
<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichang,
Taipei Hsien 221, Taiwan, R.O.C.

DISPLAY PORT CONNECTOR		
Size	Document Number	Rev
A2		-1
Kome-1 SWG2		
Date:	Thursday, September 12, 2013	Sheet 37 of 104



Near SATA HDD CONN: HDD1 (p.039)



<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SATA HDD I/F

Size
A4

Document Number

Kome-1 SWG2

Rev
-1

Date: Thursday, September 12, 2013

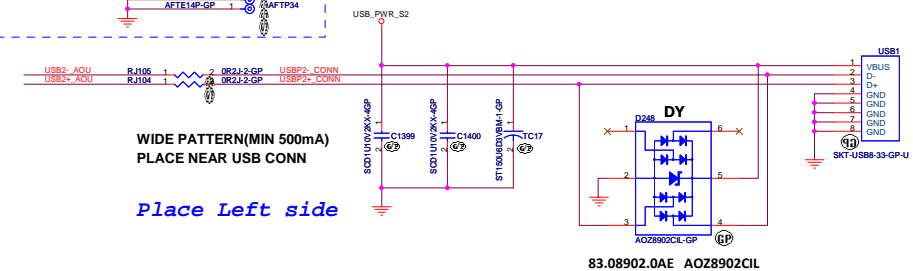
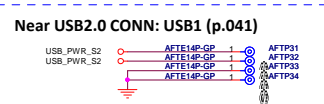
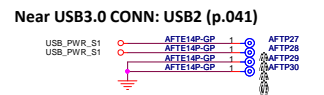
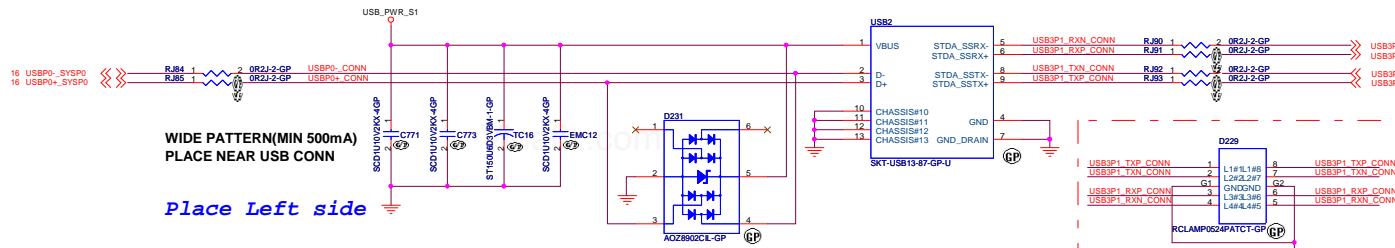
Sheet 39 of 104

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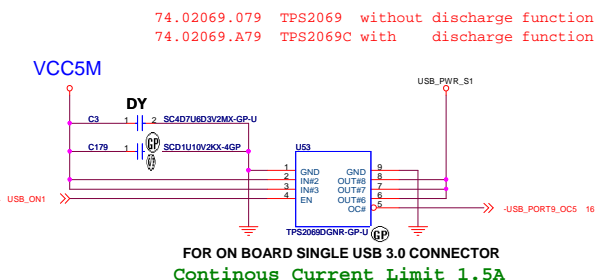
<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
-			
Size	Document Number		Rev
A4	Kome-1 SWG2		-1
Date: Thursday, July 18, 2013		Sheet 40	of 104



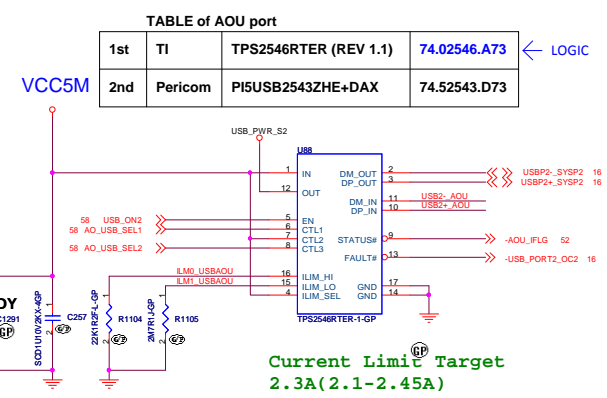
D229

	Vendor	Vendor P/N	Wistron P/N
1st	SEMTECH	RCIamp0524PATCT	83.00524.AA0
2nd	Littelfuse	SP3012-04UTG	83.03012.0A0
3th	Infineon	ESD3V3U4ULC	83.3V3U4.0A0



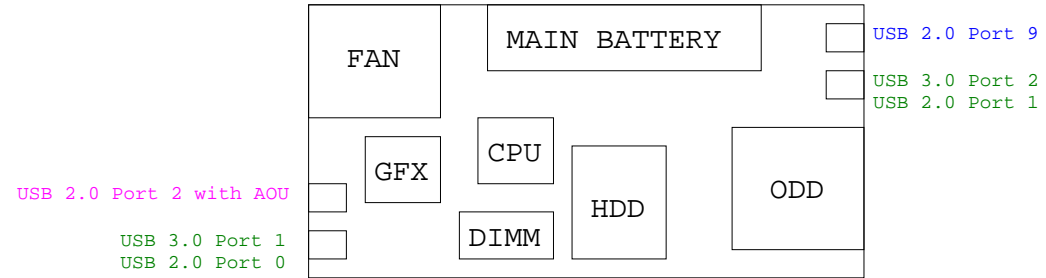
U53

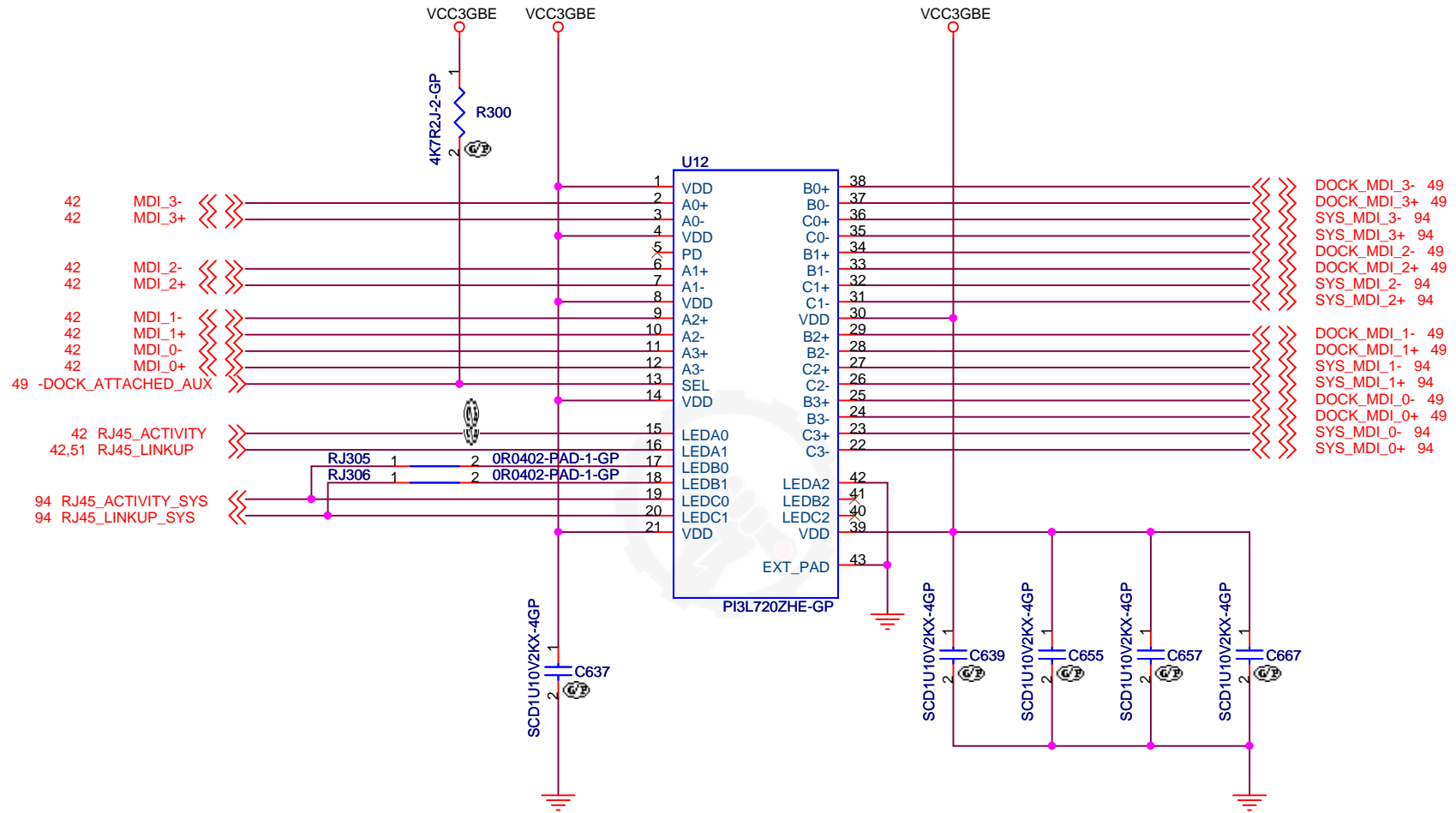
	Vendor	Vendor P/N	Wistron P/N
1st	TI	TPS2069DGN	74.02069.079
2nd	GMT	G548A1F51U	74.00548.A79



USB System Port Assignment List

Location	USB 2.0				USB 3.0	
	System Port	PCH Port	Signal Name	AOU Support	PCH Port	Signal Name
Left	Port 0	Port 0	USBP0_SYSP0	No	Port 1	USB3P1_SYSP0
Right	Port 1	Port 1	USBP1_SYSP1	No	Port 2	USB3P2_SYSP1
Left	Port 2	Port 2	USBP2_SYSP2	Yes		
Right	Port 3	Port 9	USBP9_SYSP3	No		





U12

	Vendor	Vendor P/N	Wistron P/N
1st	Pericom	PI3L720ZHE	73.03720.003
2nd	STmicro	STMUX1800E	73.01800.A03
3th	Onsemi	NCN7201	73.07201.003

← LOGIC

<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
GBE LAN Switch	
Size	Document Number
A4	Kome-1 SWG2
Date	Rev
Thursday, September 12, 2013	-1
Sheet	of
43	104

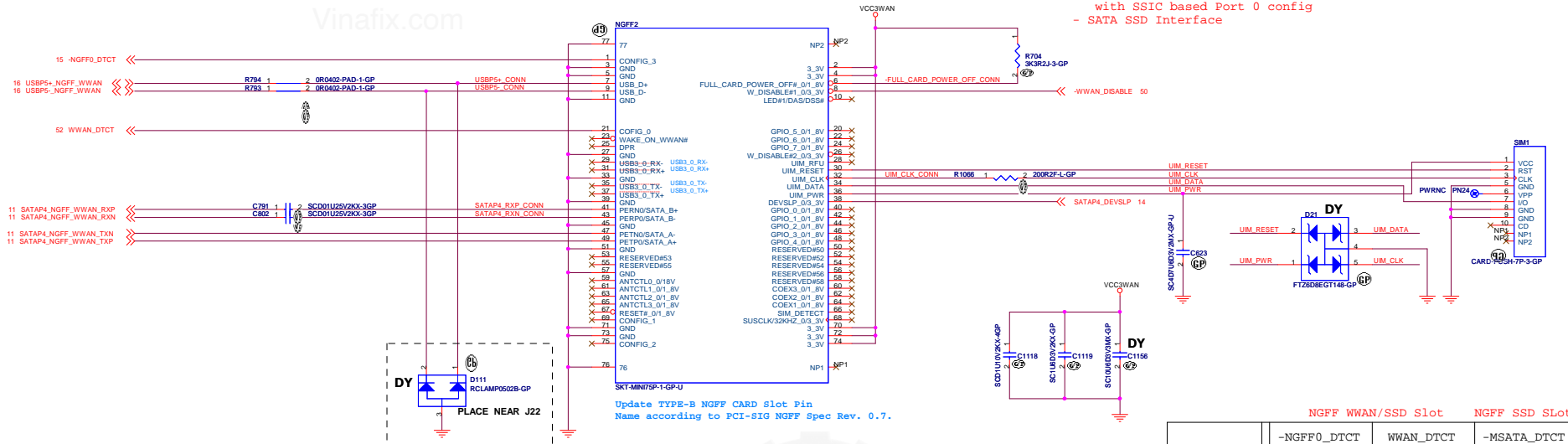
Near SIM CONN: SIM1 (p.044)

UIM_RESET AFTE14P-GP 1 AFTP35
 UIM_CLK AFTE14P-GP 1 AFTP36
 UIM_DATA AFTE14P-GP 1 AFTP37
 UIM_PWR AFTE14P-GP 1 AFTP38
 UIM_PWR AFTE14P-GP 1 AFTP39

TYPE-B (Socket2) NGFF CARD FOR WWAN/SSD

3.2H CONNECTOR

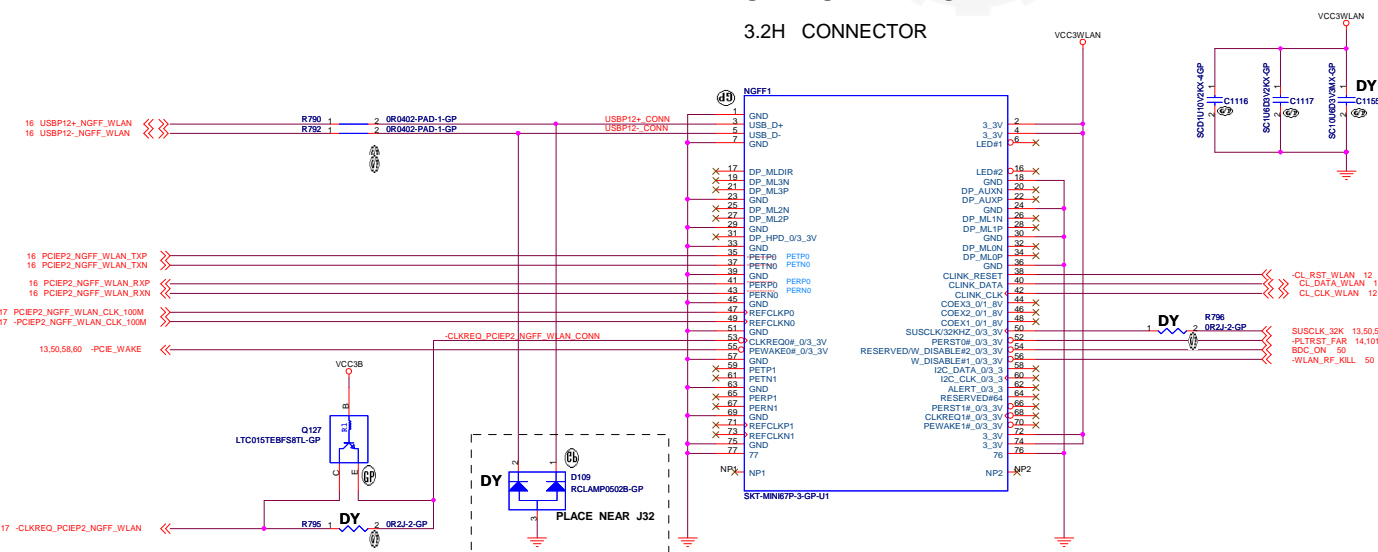
This Card slot supports
 - USB 2.0 based WWAN interface
 with SSIC based Port 0 config
 - SATA SSD Interface



NGFF WWAN/SSD Slot		NGFF SSD Slot	
	-NGFF0_DTCT	WWAN_DTCT	-MSATA_DTCT
WWAN	Low	High	N/A
SSD	Low	Low	Low
No Card	High	High	High
Reserved	High	Low	High
Reserved			Low

TYPE-A NGFF CARD FOR WLAN

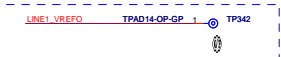
3.2H CONNECTOR



<Variant Name>

緯創資通 Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsueh-shan,
 Taipei Hsien 221, Taiwan, R.O.C.

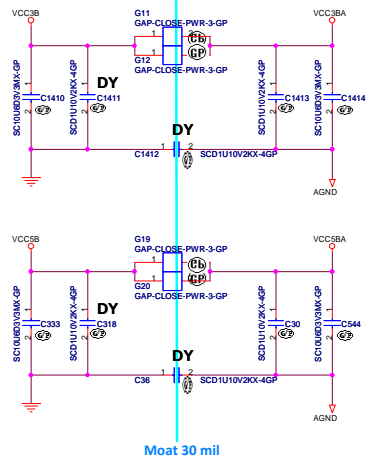
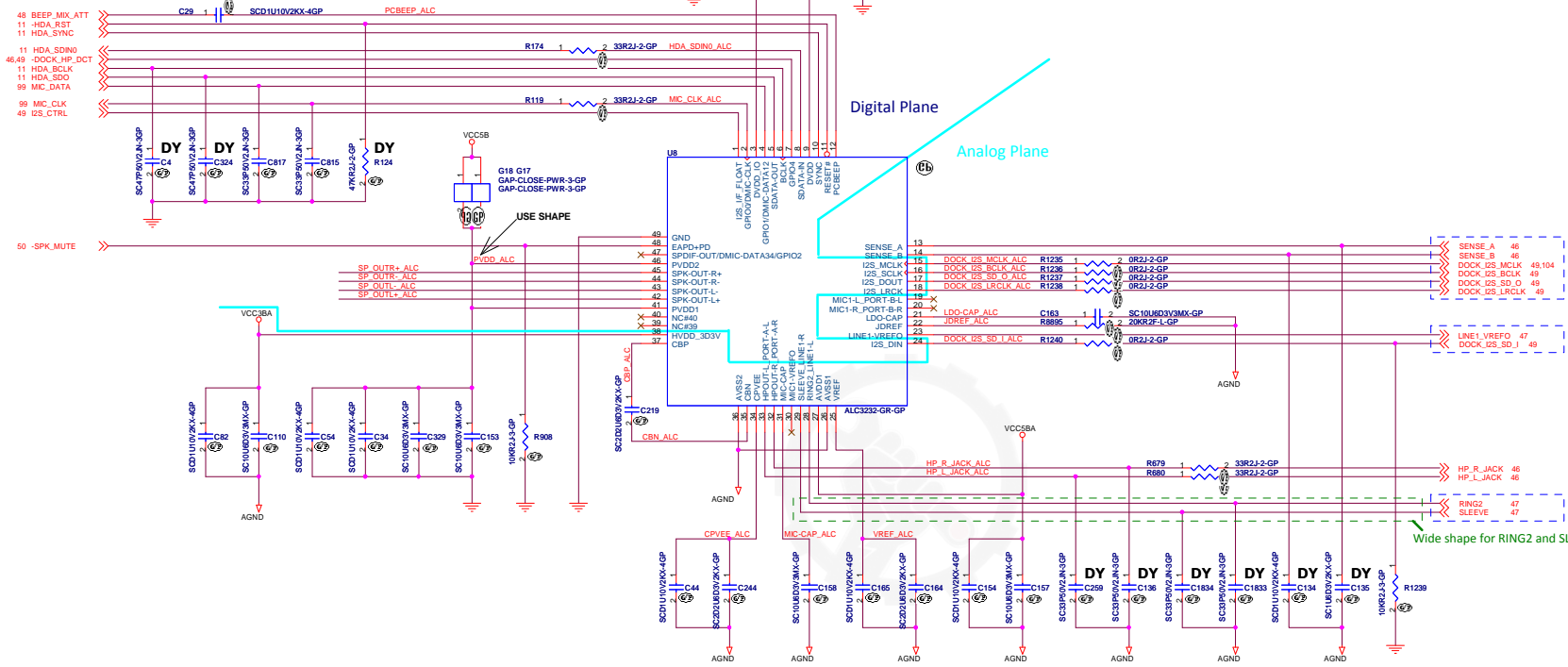
NGFF Slot x 2		
Size	Document Number	Rev
A2	Kome-1 SWG2	-1
Date: Thursday, September 12, 2013 Sheet: 44 of 104		



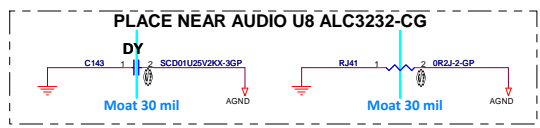
Vinafix.com

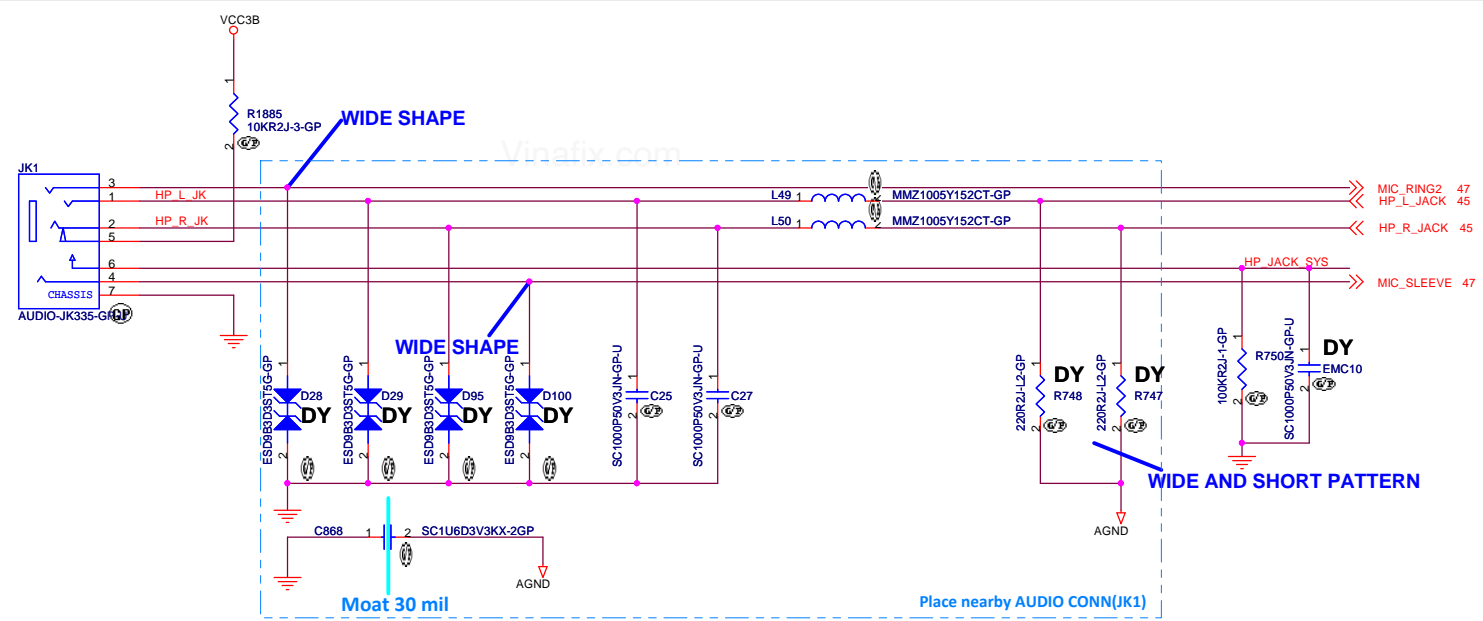
TABLE SYS MIC HW ENABLE/DISABLE			
	ENABLE	DISABLE	NOTE
R716	NO ASM	ASM	P15 (for BIOS)
R962	ASM	NO ASM	
R119	ASM	NO ASM	P45 (for DMIC)
FL14	ASM	NO ASM	P47 (for JACK)
FL20	ASM	NO ASM	
R154	NO ASM	ASM	
R155	NO ASM	ASM	

LOGIC



PLACE NEAR AUDIO U8 ALC3232-CG





Near Audio Jack CONN: JK1 (p.046)

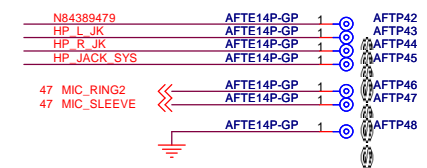
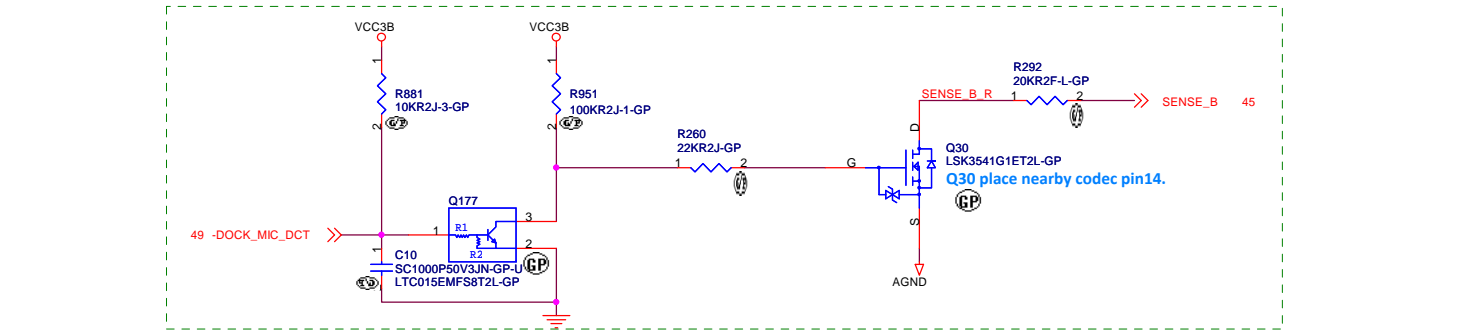
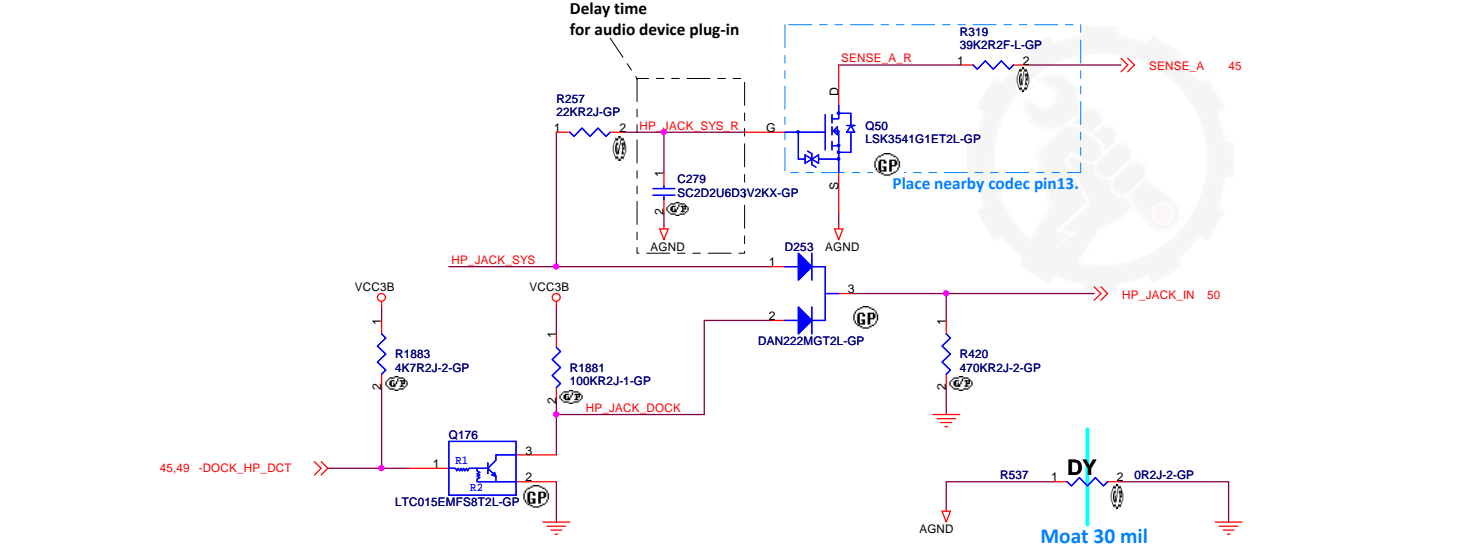
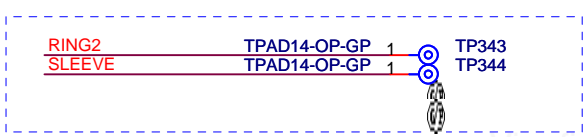


TABLE DOCK MIC HW ENABLE/DISABLE

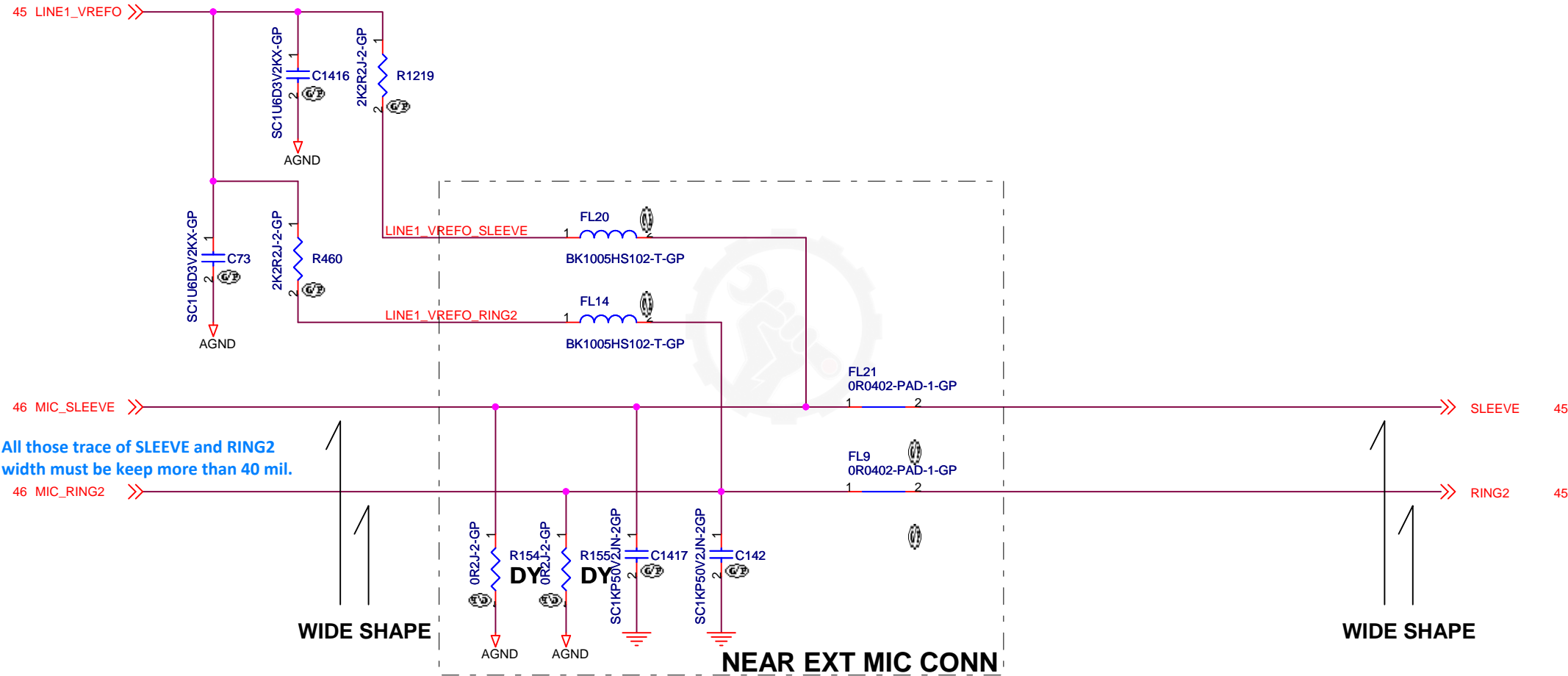
	ENABLE	DISABLE	NOTE
R881	ASM	NO ASM	
R951	ASM	NO ASM	
R260	ASM	NO ASM	
R292	ASM	NO ASM	
Q117	ASM	NO ASM	
Q30	ASM	NO ASM	

↑
LOGIC

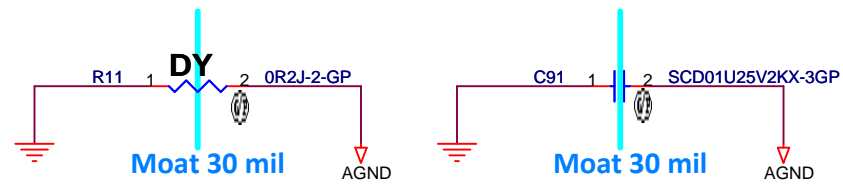




Vinafix.com



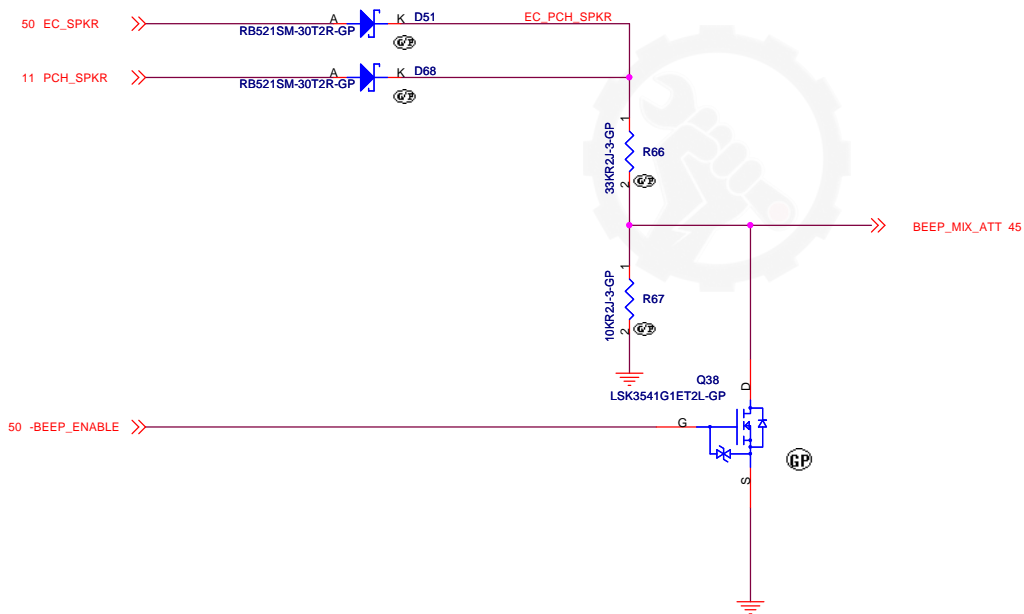
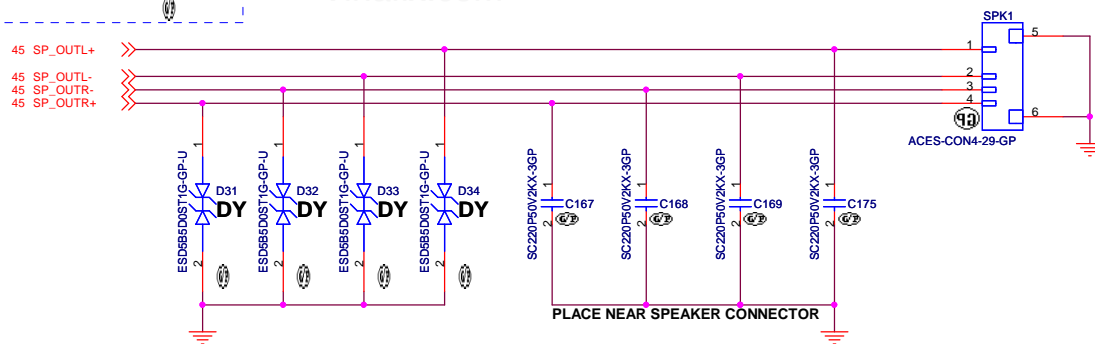
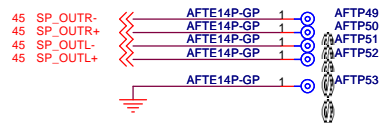
All those trace of SLEEVE and RING2 width must be keep more than 40 mil.



<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Audio EXT MIC I/F			
Size A4	Document Number Kome-1 SWG2		Rev -1
Date: Thursday, September 12, 2013		Sheet 47 of	104

Near SPEAKER CONN: SPK1 (p.048)



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title AUDIO SPEAKER I/F , BEEP		
Size A3	Document Number Kome-1 SWG2	Rev -1
Date: Thursday, September 12, 2013	Sheet 48	of 104



<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

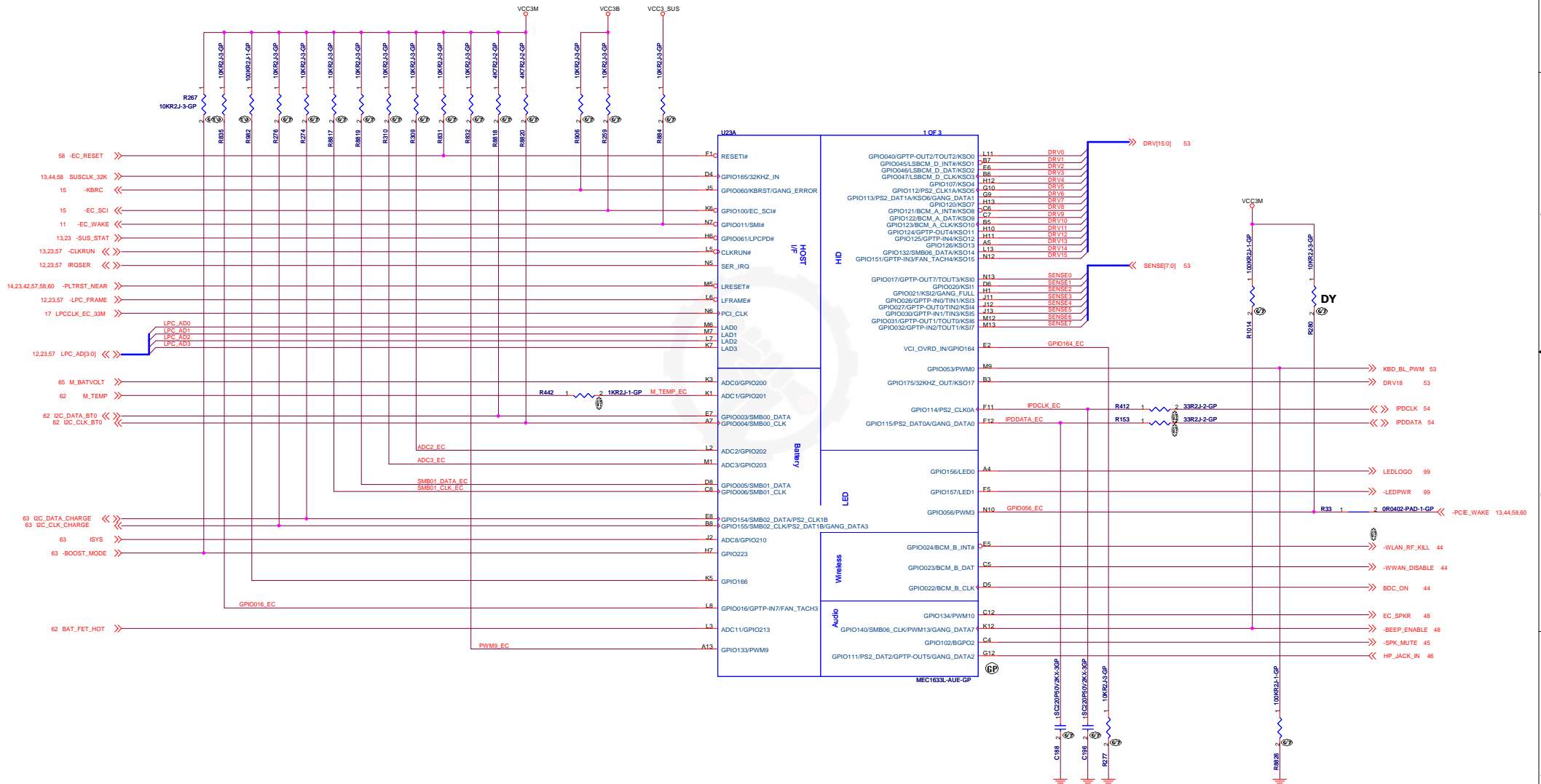


TABLE Graphics Matrix					
	UMA Entry	UMA Enhance	SWG	WS NVIDIA	WS AMD
GPIO172 (VIDEO_ID)	L	L	H	H	L
GPIO105 (WS_DTCT)	L	L	L	H	H

TABLE WS_DTCT (GPIO105)			
	UMA/SWG	WS	
R8815	NO-ASM	ASM	

LOGIC

Crystal 32.768KHz 9pF 20ppm				
KDS	DST3105	1TJF090DP1A100T	82.30001.C21	
TXC	9H T10	9H03200033	82.30001.F31	
EPSON	FC-135	Q13FC1350000300	82.30001.C01	

TABLE VIDEO_ID (GPIO172)			
	UMA/AMD WS	SWG/nVIDIA WS	
R940	NO-ASM	ASM	

LOGIC

TABLE JTAG debug port			
	Enable	Disable	
R8796	ASM	NO-ASM	
R8797	NO-ASM	ASM	

LOGIC

TABLE Trace FIFO debug port			
	Enable	Disable	
R8807	ASM	NO-ASM	
R8808	ASM	NO-ASM	
R8950	NO-ASM	ASM	

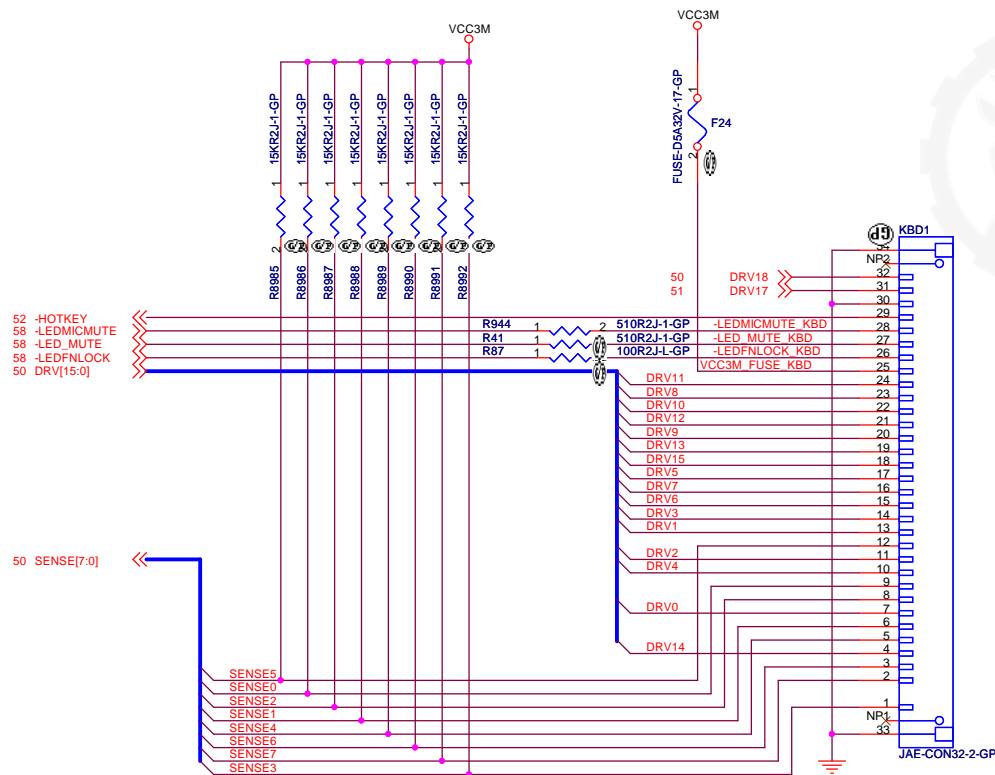
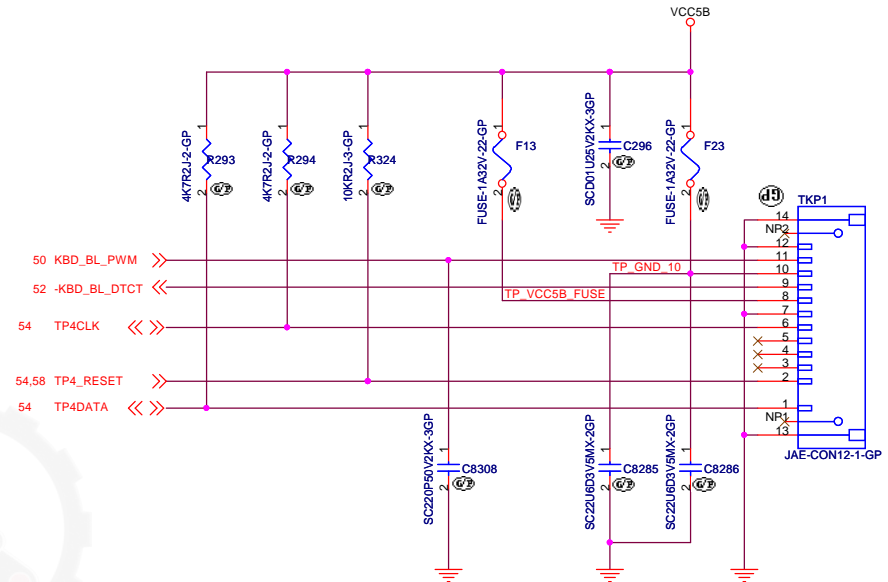
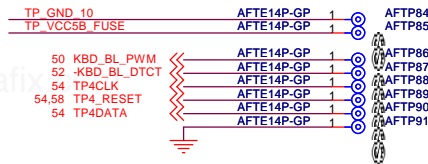
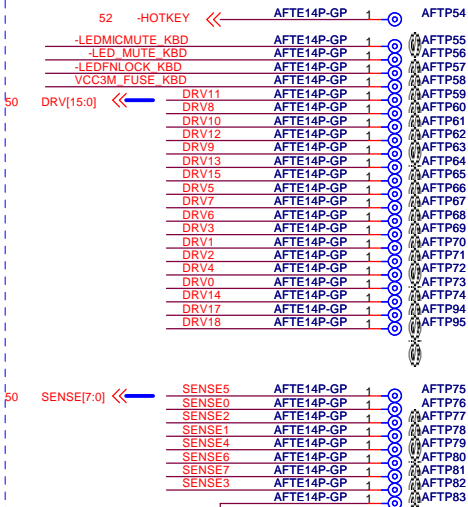
LOGIC

<Variant Name>

緯創資通 Wistron Corporation	
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsuehshui, Taipei Hsien 221, Taiwan, R.O.C.	
File EC MEC1633L (3/3)	
Size A2	Document Number Kome-1 SWG2
Date: Thursday, September 12, 2013	Sheet 52 of 104

Near Keyboard CONN: KBD1 (p.053)

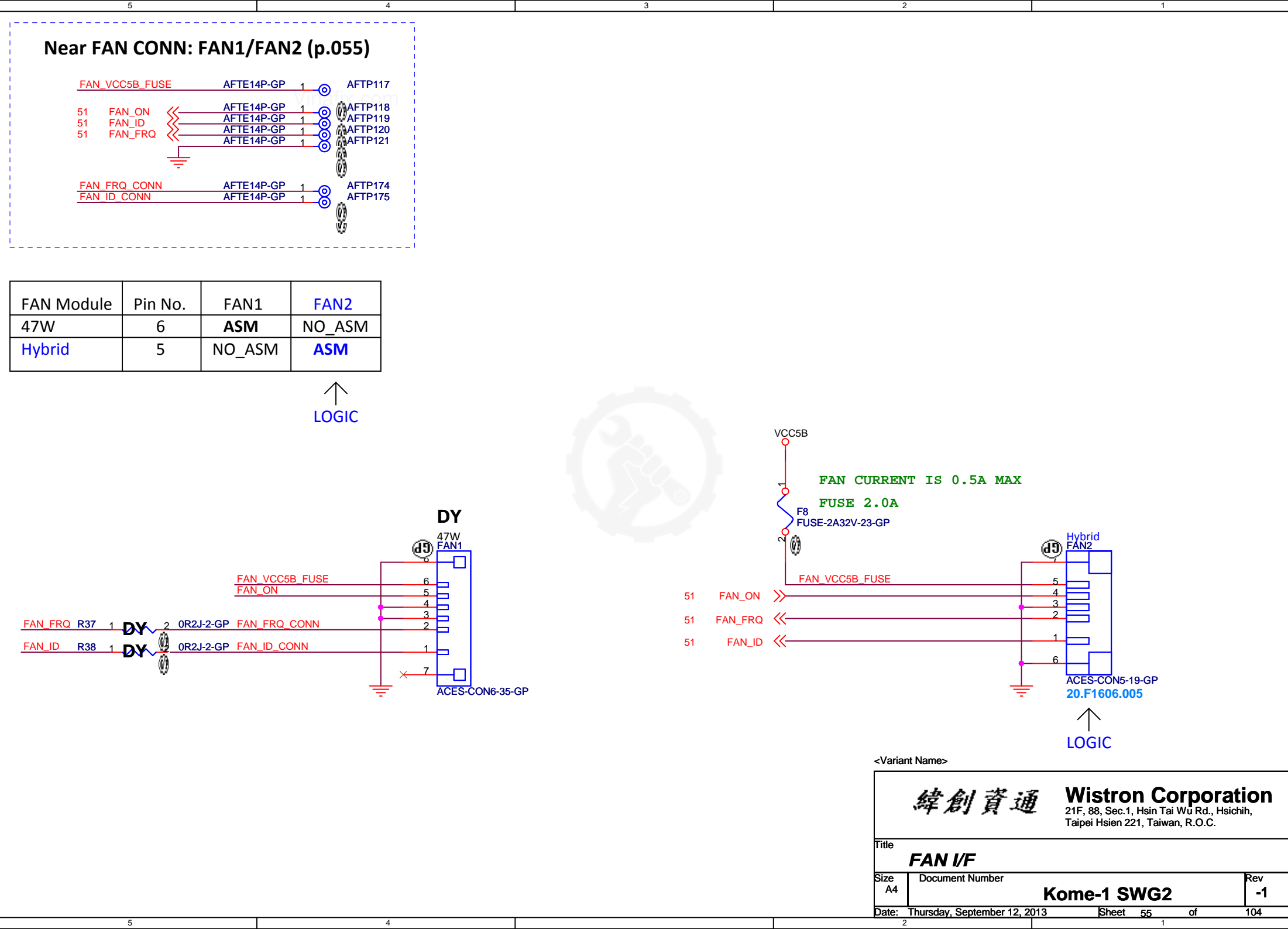
Near TrackPoint CONN: TKP1 (p.053)

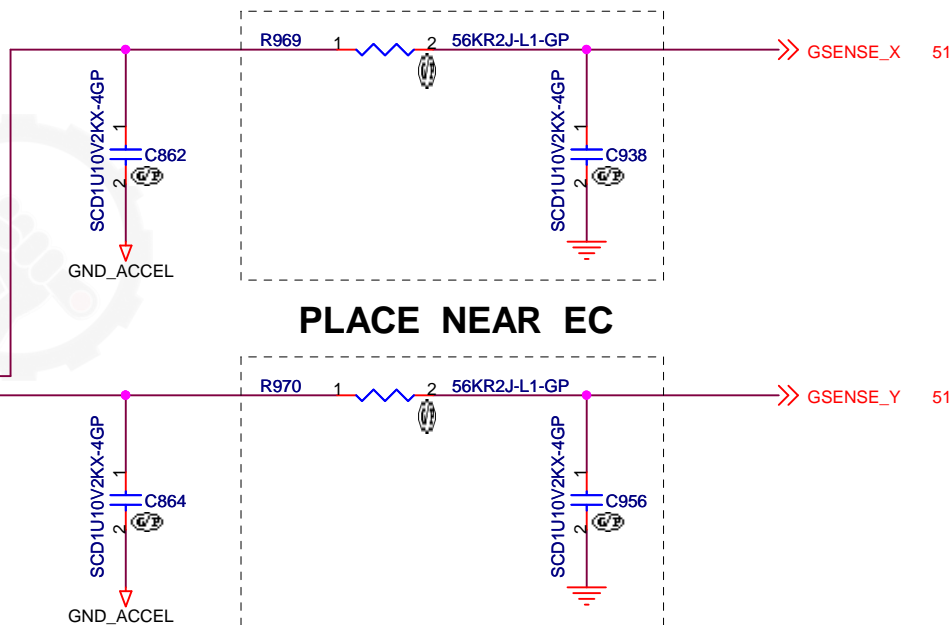
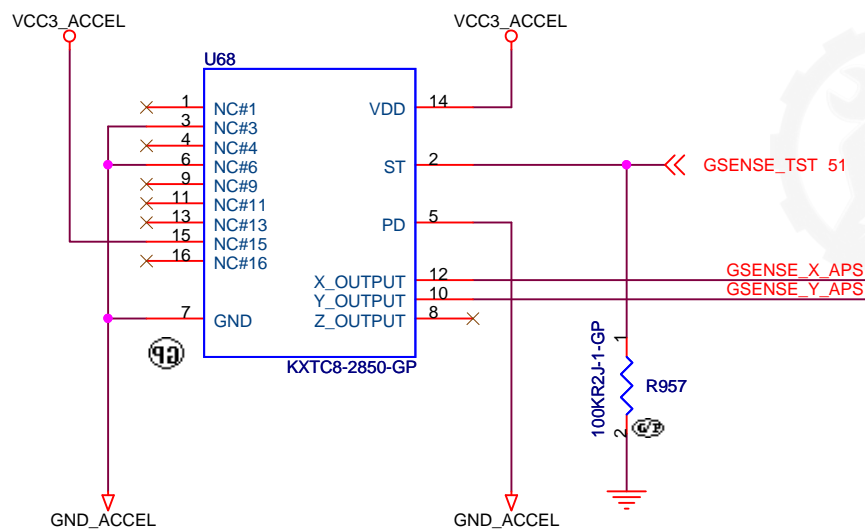
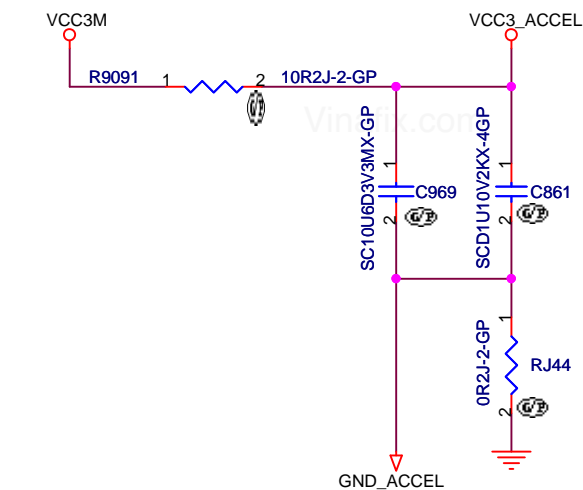


<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title KEYBOARD I/F		
Size A3	Document Number Kome-1 SWG2	Rev -1
Date: Thursday, September 12, 2013	Sheet 53 of 104	





These components must be placed near HDD and same location for UMA/SWG/WS.

Do not use Z-axis monitor on APS

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

APS(G SENSOR)

Size
A4

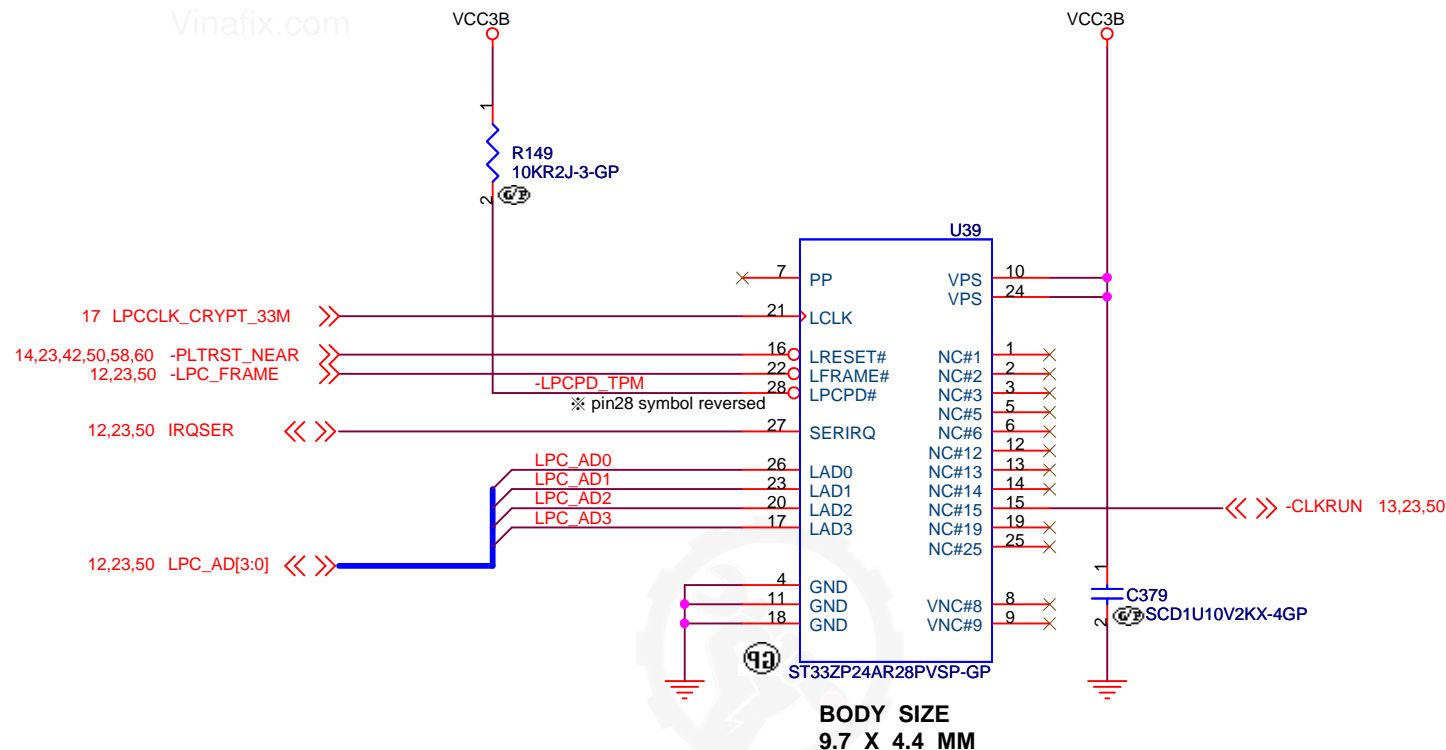
Document Number

Kome-1 SWG2

Rev
-1

Date: Thursday, September 12, 2013

Sheet 56 of 104



Need to update the firmware to improve system boot up time.

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

TPM

Size
A4

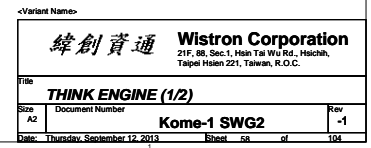
Document Number

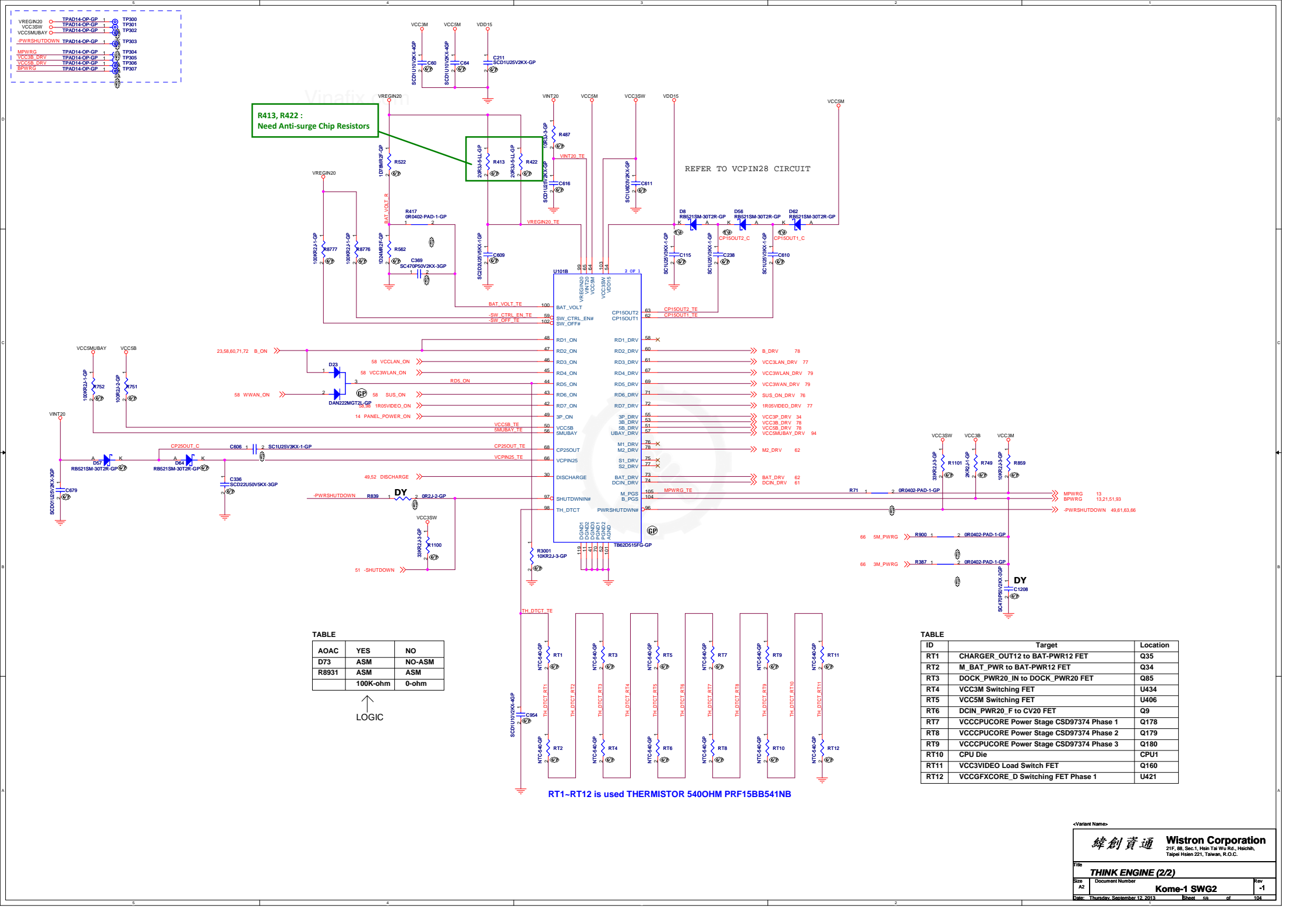
Kome-1 SWG2

Rev
-1

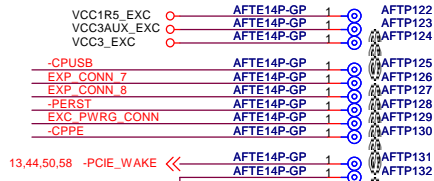
Date: Thursday, September 12, 2013

Sheet 57 of 104



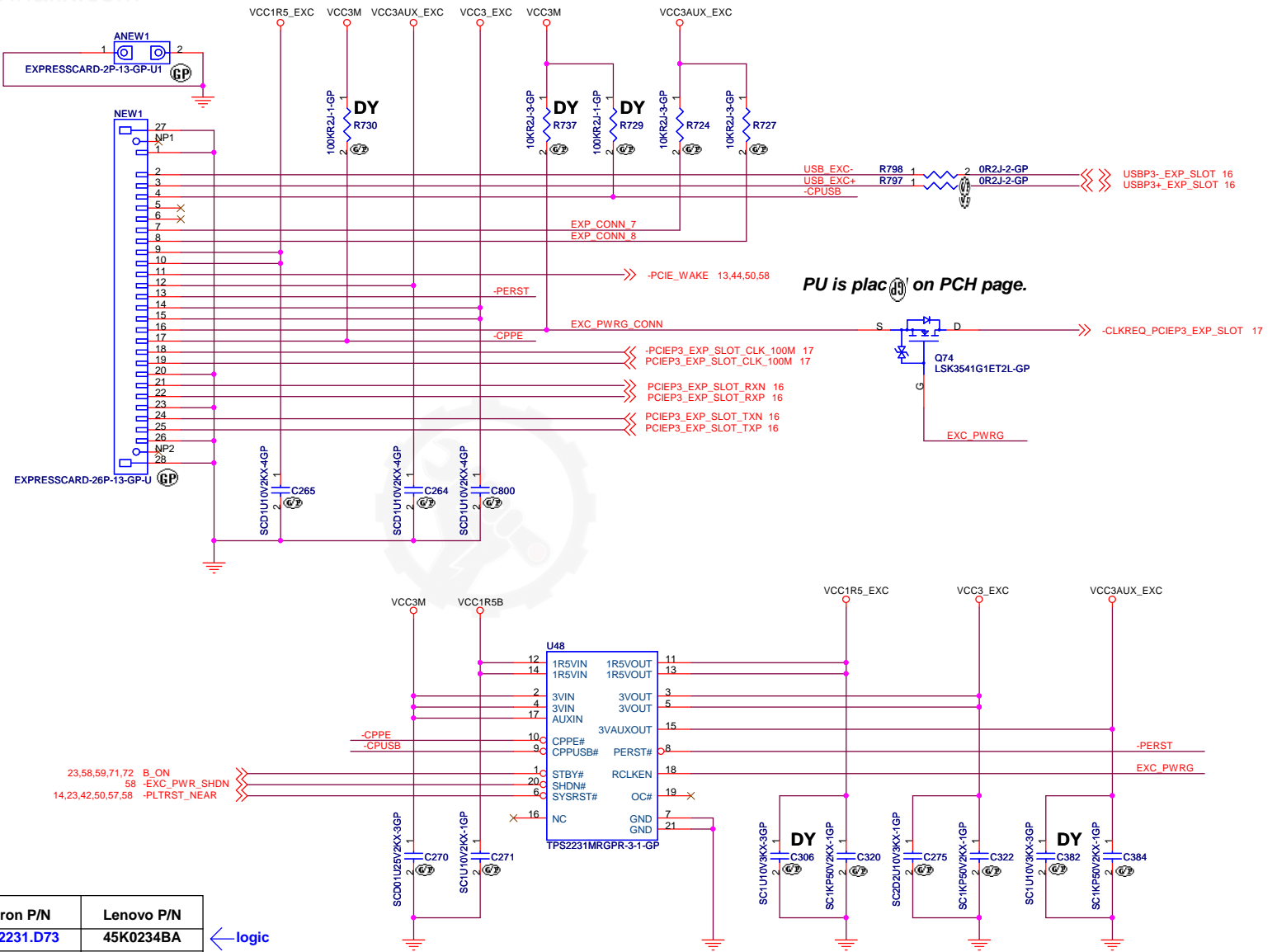


Near ExpressCard Slot: NEW1 (p.060)



Vinafix.com

EXC POWER IC has internal PU for
PIN#9(-CPUSB), PIN#10(-CPPE)



EXPRESS POWER SW Table

	Supplier	Vendor P/N	Wistron P/N	Lenovo P/N
U48	TI	TPS2231MRGPR-3	74.02231.D73	45K0234BA
	NUVOTON	W83L351YG V.ASA	74.83351.A73	
	ROHM	BD4157MUV-GE2	74.04157.A73	
	ROHM	BD4156MUV-SGE2	74.04156.A73	

← logic

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	EXPRESS SLOT AND POWER		
Size	Document Number	Kome-1 SWG2	Rev -1
A3			
Date:	Thursday, September 12, 2013	Sheet 60	of 104

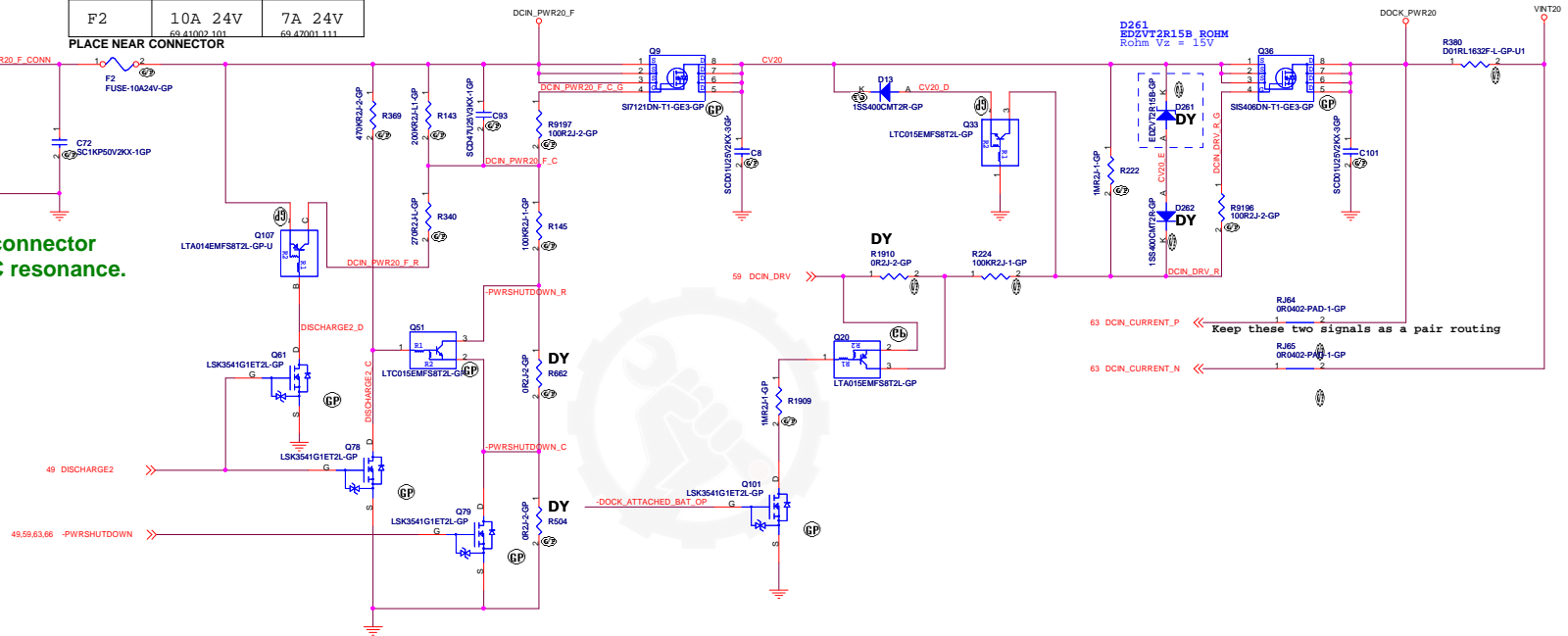
[illegible]

Pin connection diagram for the DCIN1 connector. The diagram shows a 10-pin connector with pins labeled 1A through 9A and 1B through 9B. Pin 1A is connected to DCIN_PWR20_F_CONN. Pin 1B is connected to SC10PWR12_0A_1GP. Pins 2A through 9A are connected to ETY-CONN18C_6P. Pins 2B through 9B are connected to SC10PWR12_0A_1GP.

Parts Assignment

	SWG	UMA
F2	10A 24V 69.41002 101	7A 24V 69.47001 111

PLACE NEAR CONNECTOR



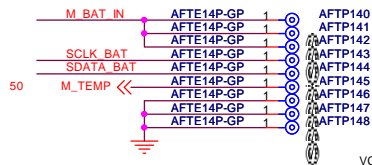
PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title DC IN			
Size A2	Document Number Kome-1 SWG2	Rev -1	
Date: Thursday, September 12, 2013	Sheet 61	of 104	

Near MAIN BAT CONN: BAT1 (p.062)



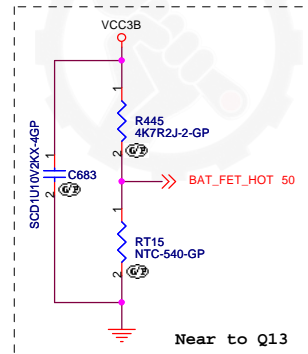
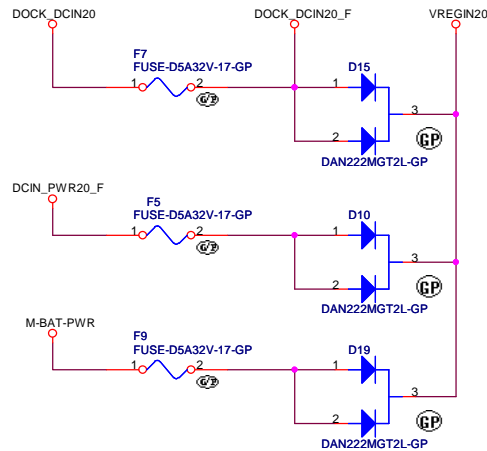
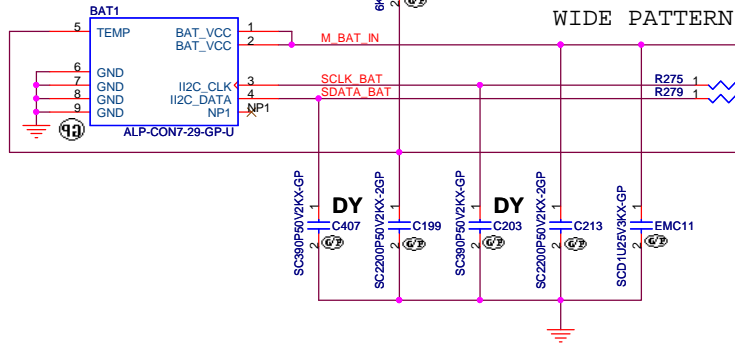
Vinafix.com

Parts Assignment

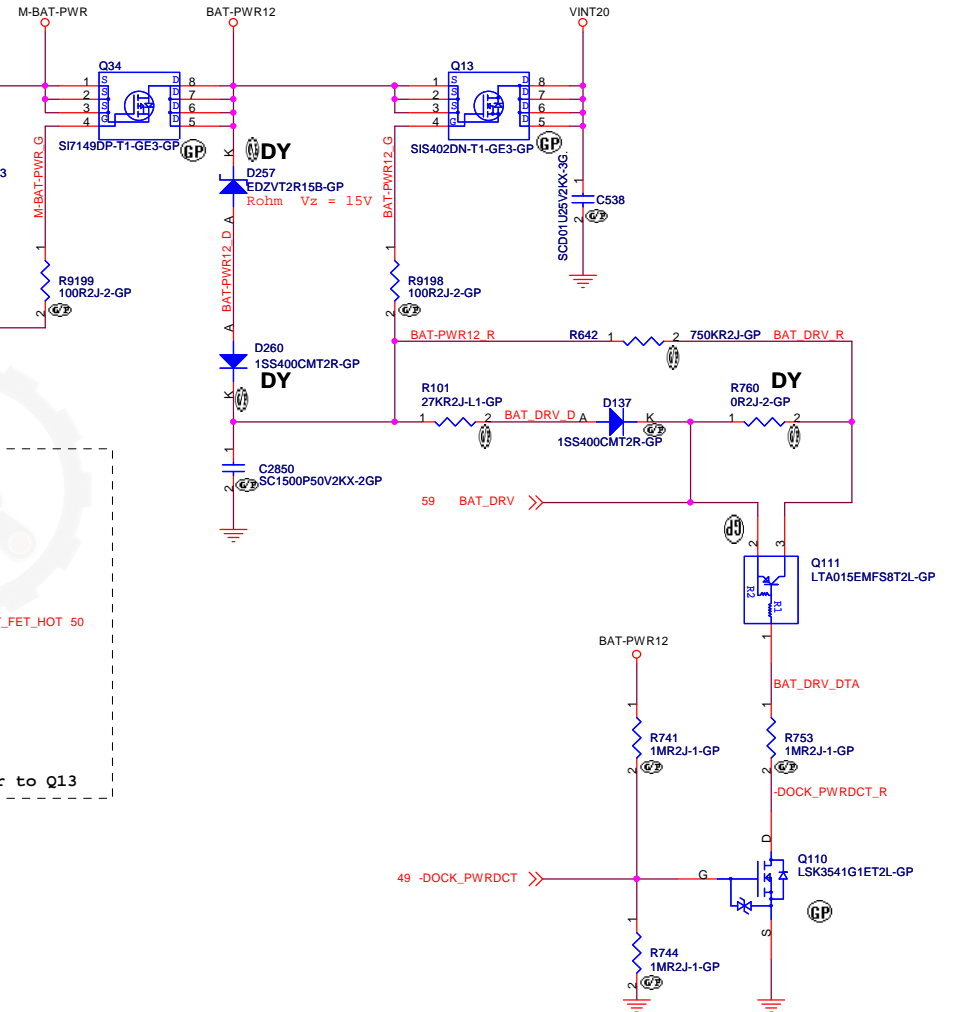
	SWG	UMA
F12	20A 24V 69.42002.031	10A 24V 69.41002.101

Littlefuse
0501020
24V 20A

MAIN BAT CONN



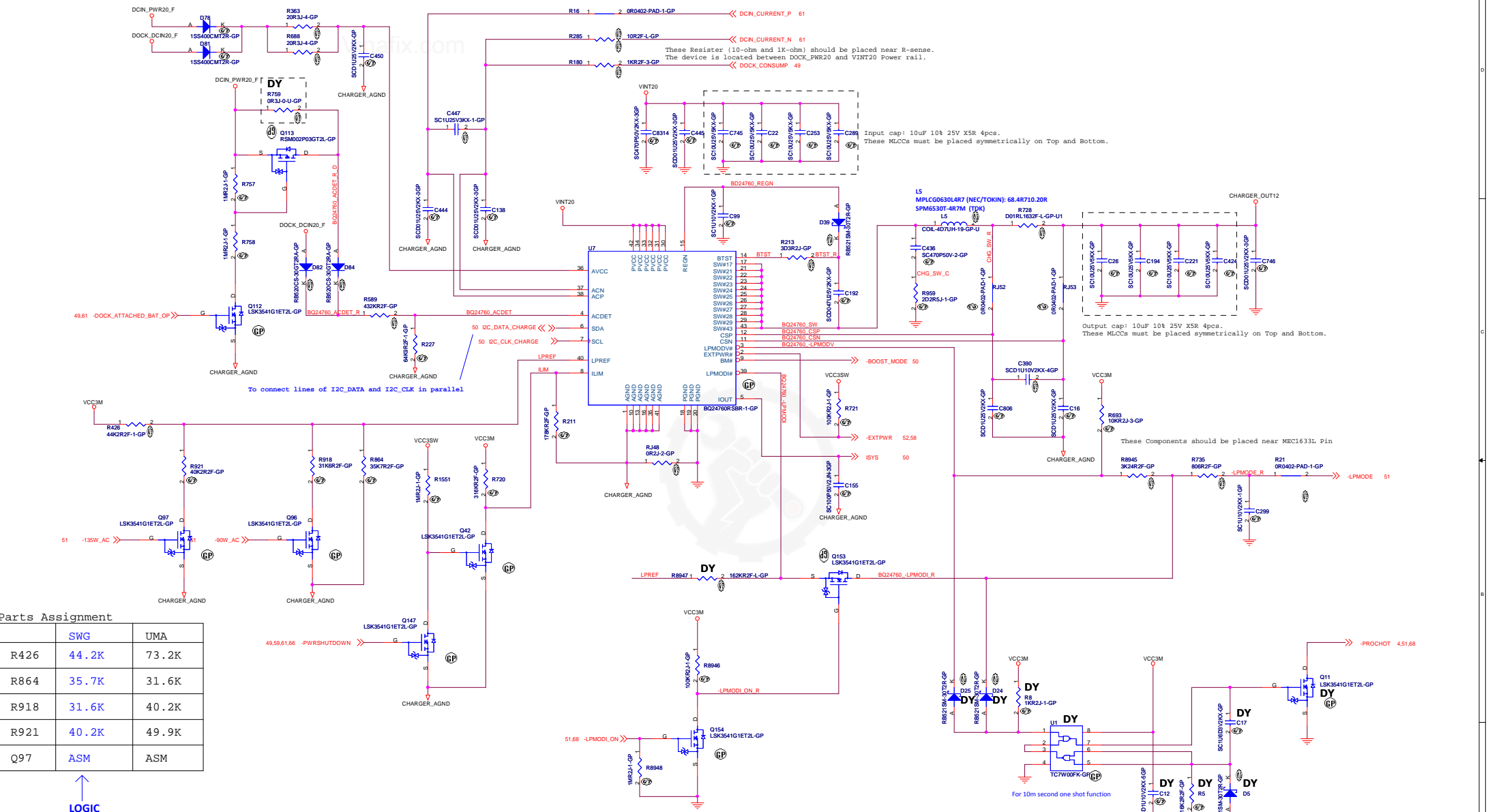
Near to Q13



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title	BATTERY INPUT		
Size	Document Number	Rev	
A3		Kome-1 SWG2	
Date:	Thursday, September 12, 2013	Sheet	62 of 104



Parts Assignment

	SWG	UMA
R426	44.2K	73.2K
R864	35.7K	31.6K
R918	31.6K	40.2K
R921	40.2K	49.9K
Q97	ASM	ASM

Table for SWG

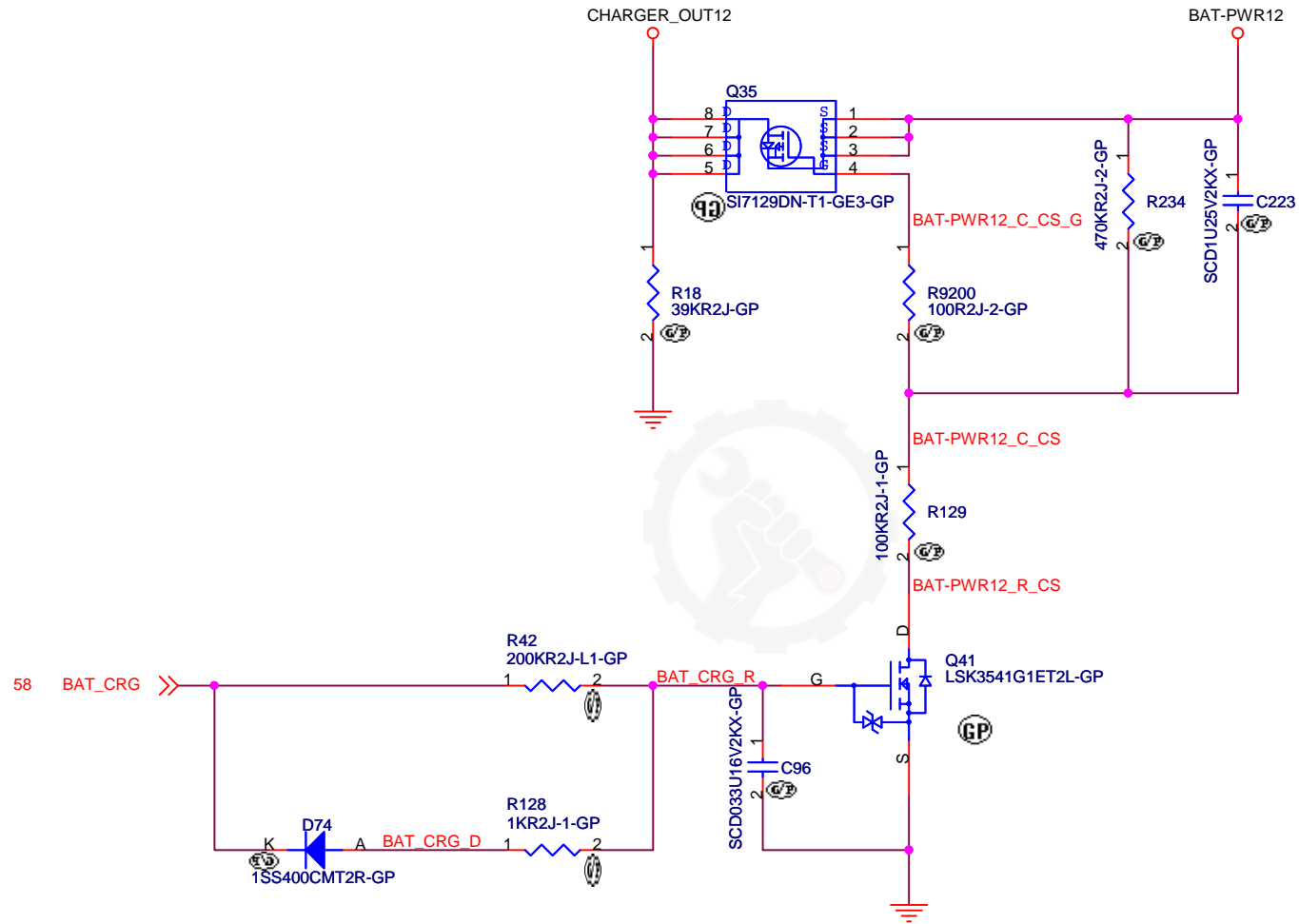
AC Adapter	-135W_AC	-90W_AC	System Power Limit
135W	L	L	135W
90W	H	L	90W
65W	H	H	65W

Table for UMA

AC Adapter	-135W_AC	-90W_AC	System Power Limit
90W	L	L	90W
65W	H	L	65W
45W	H	H	45W

Table

	One Shoot Enable	One Shoot Disable
R693	DY	ASM
D24	ASM	DY
D25	ASM	DY
Q11	ASM	DY



<Variant Name>

緯創資通

Wistron Corporation21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

CHARGER SELECTSize
A4

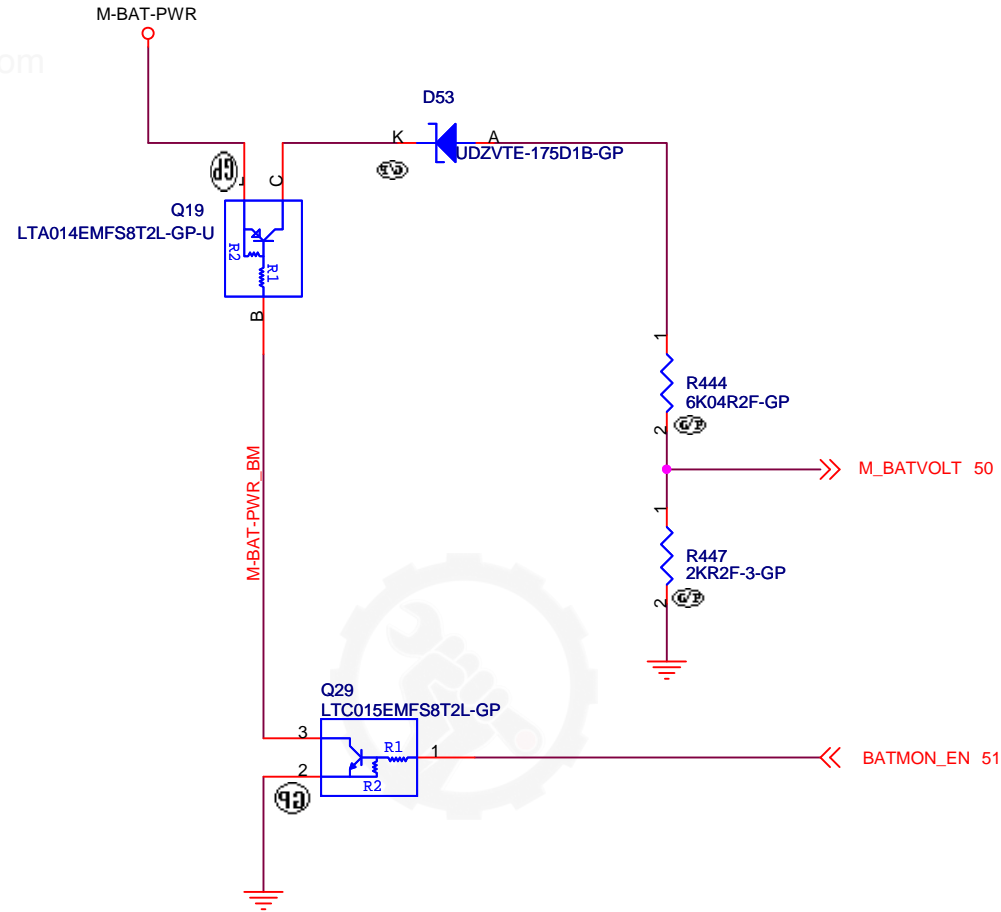
Document Number

Kome-1 SWG2Rev
-1

Date: Thursday, September 12, 2013

Sheet 64 of 104

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<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BATTERY MONITOR

Size
A4

Document Number

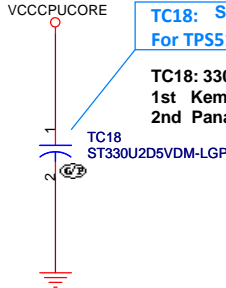
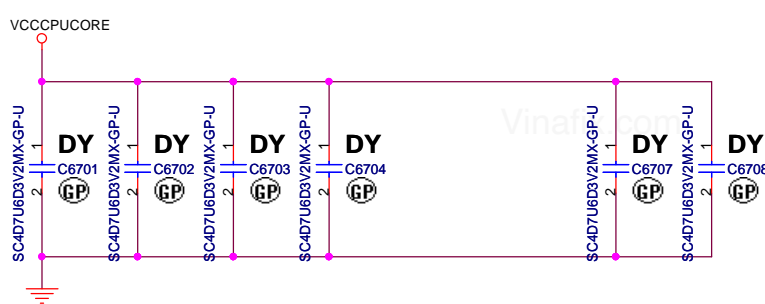
Kome-1 SWG2

Rev
-1

Date: Thursday, September 12, 2013

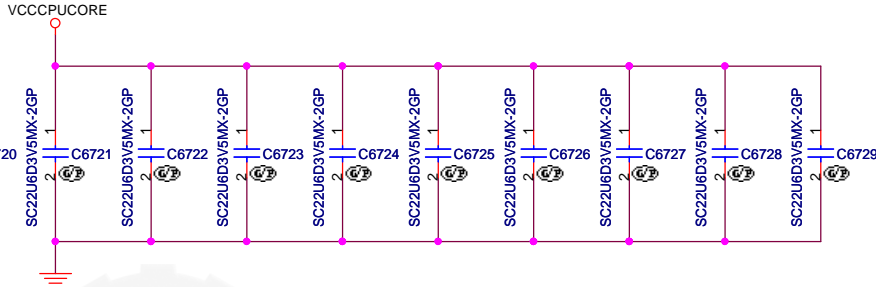
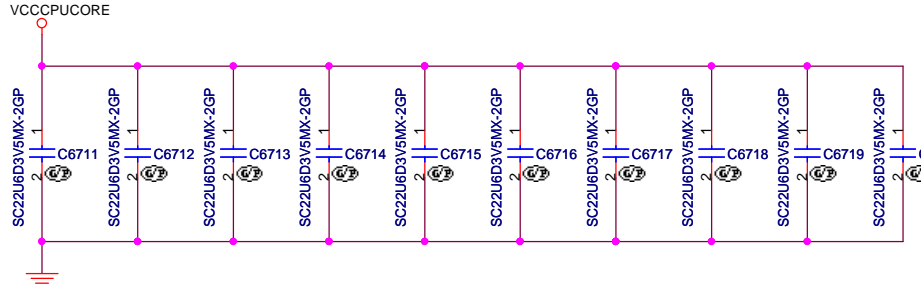
Sheet 65 of 104



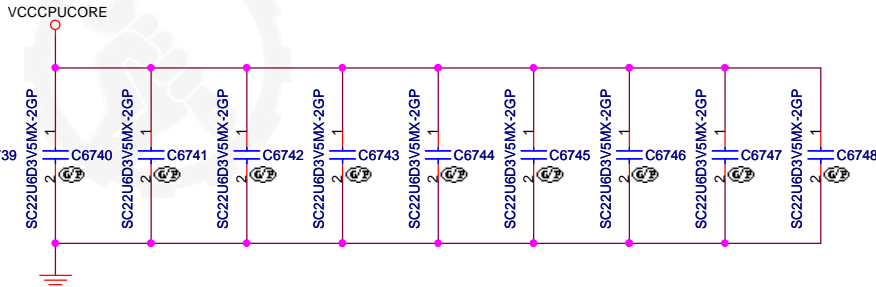
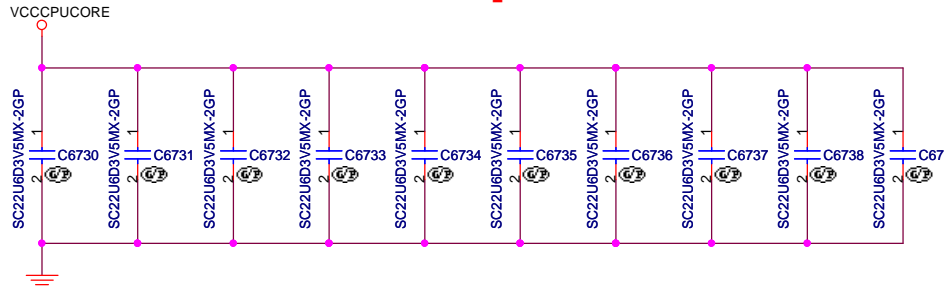


TC18: Symbol is temporarily
For TP551631 undershoot voltage issue

TC18: 330uF 2.5V 9mOhm 7343-size
1st Kemet T520V337M2R5ATE009 80.3371V.L01
2nd Panasonic EEFSX0E331ER 077.53371.0001

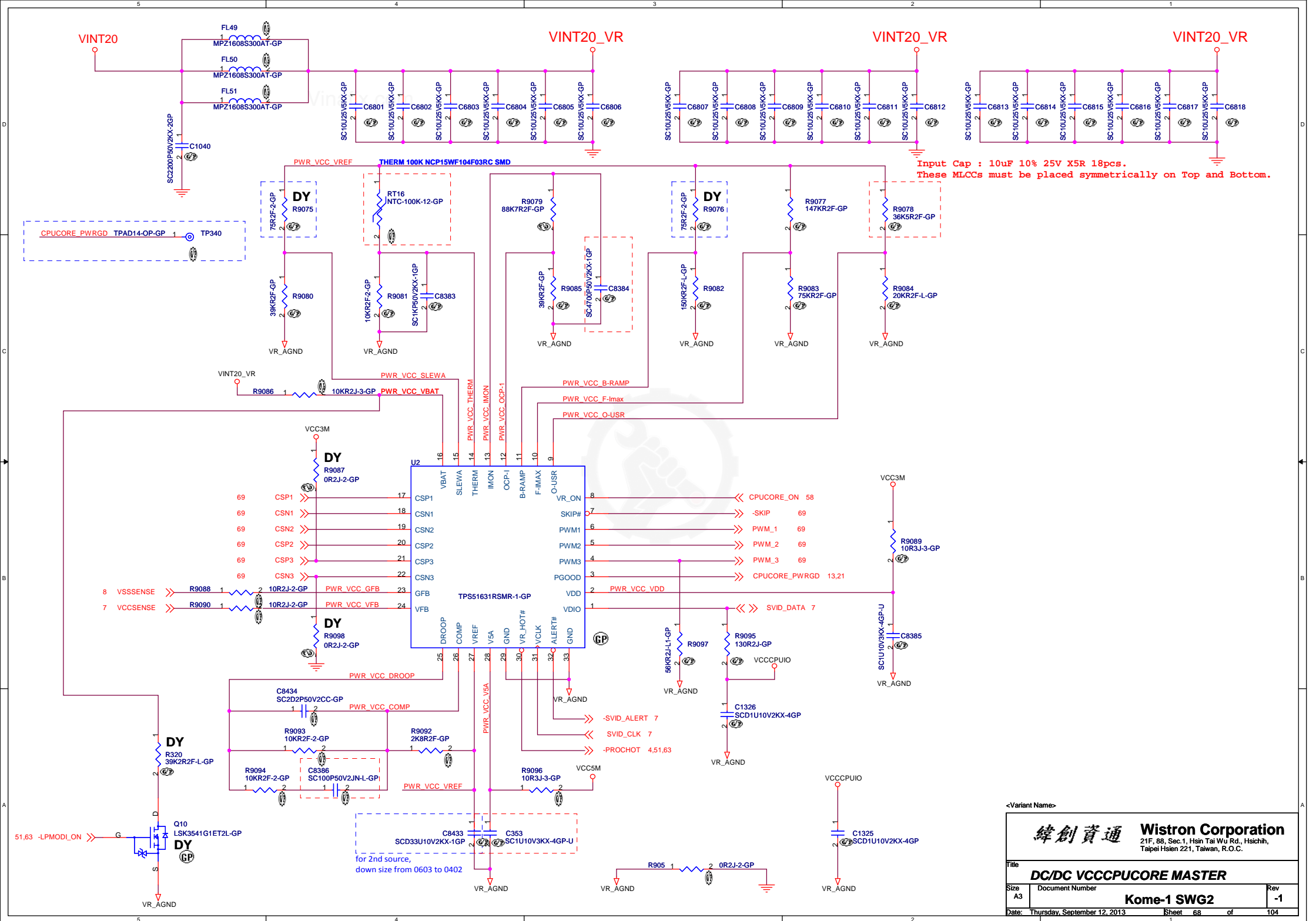


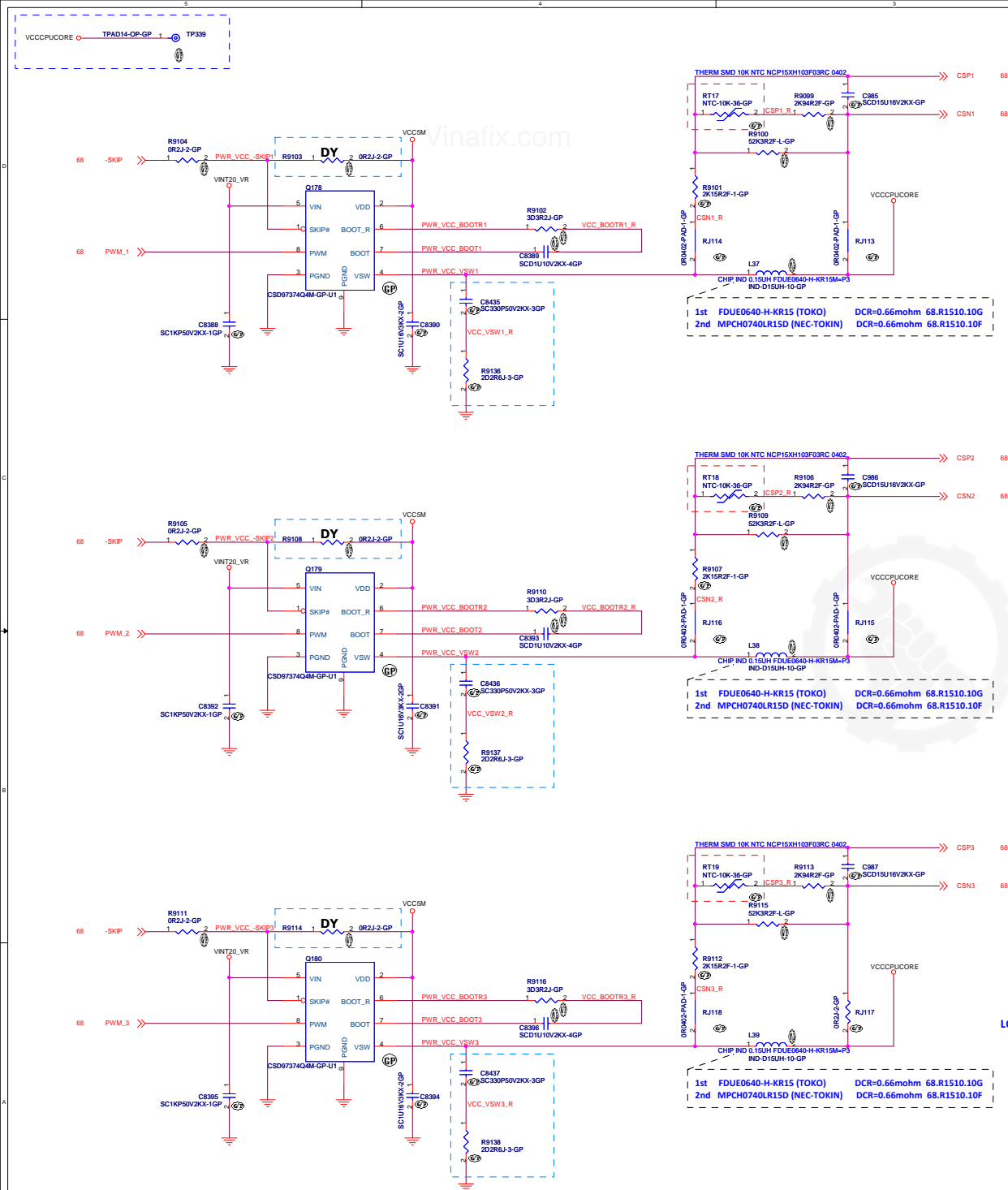
38pcs 22uF for VCCCPUCORE



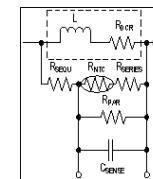
<Variant Name>

緯創資通 Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title DC/DC VCCCPUCORE DECOUPLING		
Size Custom	Document Number Kome-1 SWG2	Rev -1
Date: Monday, July 22, 2013	Sheet 67	of 104





Vin (Max) 20V
 Inductor MPCH0740LR15D or FDUE0640-H-KR15 (DCR=0.66mOhm)
 Output Capacitor 22uF x 38pcs
 Controller IC TPS51631
 ※ Remove phase-3 power stage components and connect CSP3 to 3.3V and CSN3 to GND for 2-phase 37W Vcore.



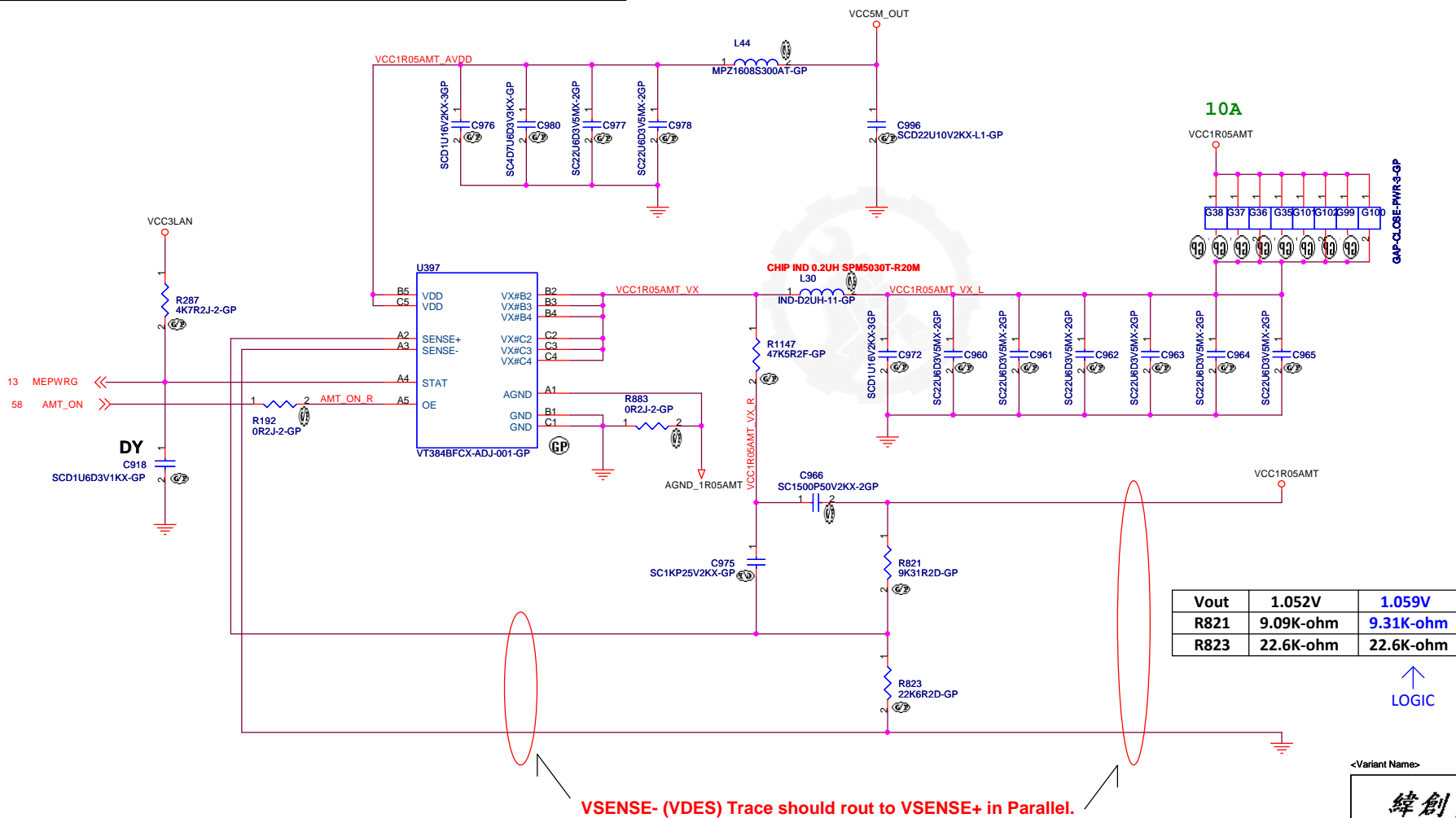
Connection	Component Name	37W 2 Phase	47W 3 Phase	57W 3 Phase	Unit	
O-USR to GND	R9084	20	20	20	K-Ohm	
O-USR to VREF	R9078	64.9	36.5	36.5	K-Ohm	
F-IMAX to GND	R9083	150	75	75	K-Ohm	
F-IMAX to VREF	R9077	549	147	127	K-Ohm	
B-RAMP to GND	R9082	150	150	150	K-Ohm	
B-RAMP to VREF	R9076	Open	Open	Open	K-Ohm	
OCF-I to GND	R9085	39	39	39	K-Ohm	
IMON to OCF-I	R9079	169k	88.7	82.5	K-Ohm	
THERM to GND	R9081	10	10	10	K-Ohm	
THERM to VREF	RT16	100	100	100	K-Ohm	NTC
SLEWA to GND	R9080	39	39	39	K-Ohm	
SLEWA to VREF	R9075	Open	Open	Open	K-Ohm	
DROOP to COMP	R9093	10	10	10	K-Ohm	
DROOP to COMP	C8434	2.2	2.2	2.2	pF	
DROOP to COMP	R9094 + C8386	10K + 330p	10K + 100p	10K + 330p		
COMP to VREF	R9092	2.7k	2.8	2.8	K-Ohm	
VREF to GND	C8433	0.33	0.33	0.33	uF	
PWM3 to GND	R9097	56	56	56	K-Ohm	Place 56K-Ohm to enable OSR
Rsequ	R9101, R9107, R9112	2.15	2.15	2.15	K-Ohm	R9112 = NO_ASM in 2-phase 37W
Rseries	R9099, R9106, R9113	2.94	2.94	2.94	K-Ohm	R9113 = NO_ASM in 2-phase 37W
Rpar	R9100, R9109, R9115	52.3	52.3	52.3	K-Ohm	R9115 = NO_ASM in 2-phase 37W
Csense	C985, C986, C987	150	150	150	nF	C987 = NO_ASM in 2-phase 37W
Rntc	RT17, RT18, RT19	10	10	10	K-Ohm	RT19 = NO_ASM in 2-phase 37W (NTC)
	R9087	ASM	NO_ASM	NO_ASM		
	R9098	ASM	NO_ASM	NO_ASM		
	Q180	NO_ASM	ASM	ASM		
	R9114	NO_ASM	NO_ASM	NO_ASM		
	C8395	NO_ASM	ASM	ASM		
	C8394	NO_ASM	ASM	ASM		
	R9116	NO_ASM	ASM	ASM		
	C8396	NO_ASM	ASM	ASM		
	R9138	NO_ASM	ASM	ASM		
	C8437	NO_ASM	ASM	ASM		
	RT19	NO_ASM	ASM	ASM		
	R9113	NO_ASM	ASM	ASM		
	R9115	NO_ASM	ASM	ASM		
	R9112	NO_ASM	ASM	ASM		
	C987	NO_ASM	ASM	ASM		
	L39	NO_ASM	ASM	ASM		
	RJ117	NO_ASM	ASM	ASM		
	TC18	NO_ASM	ASM	ASM		

NOTES:
 Min Over Current Limit is set to 70A for 37W and 110A for both 47W and 57W.
 Switching Frequency is set to 1MHz for 37W and 800KHz for 47W and 57W.
 Fine-tuning at motherboard evaluation level might still be needed to get the best performance.

1st Source				2nd Source			
TOKO : 68.R1510.10G (L37, L38, L39)				NEC : 68.R1510.10F (L37, L38, L39)			
	R9092	R9079	C8386		R9092	R9079	C8386
37W	2.7k	169k	330p	37W	3.01k	178k	330p
47W	2.8k	88.7k	100p	47W	3.16k	105k	100p

Parts Assignment

	SWG(10A)	UMA(7A)
U397	VT384B-001 74.00384.B3Z	VT382B-001 74.00382.B3Z
L30	SPM5030T-R20M 68.R2010.10I MPLCG0530LR22 68.R2210.10F	SPM5030T-R47M 68.R471A.10X MPLCG0530LR47 68.R4710.20N



緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

DC/DC VCC1R05AMT

Size

A3

Document Number

Kome-1 SWG2

Date

Thursday, September 12, 2013

Sheet

70

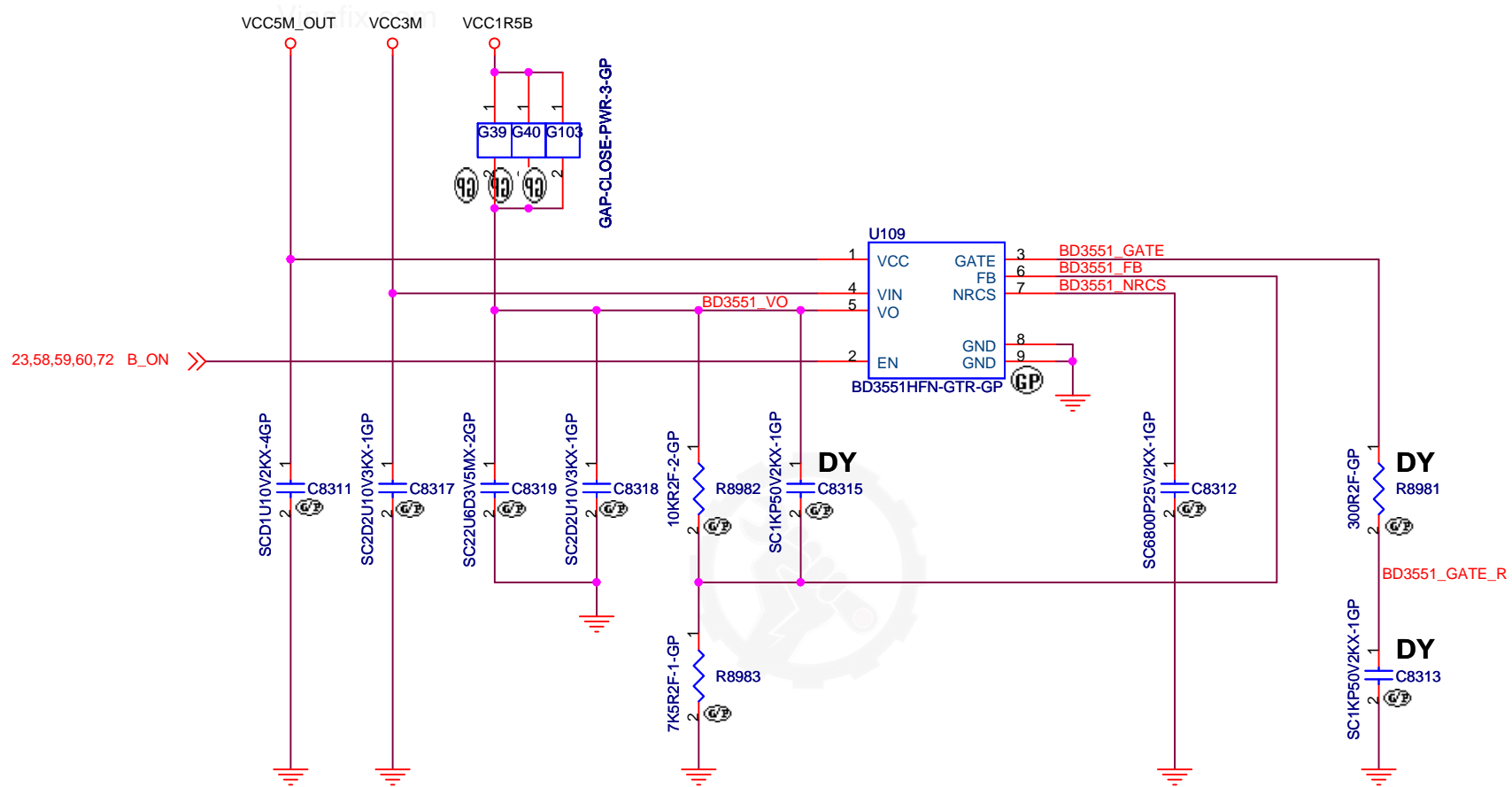
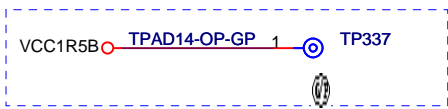
of

104

Rev

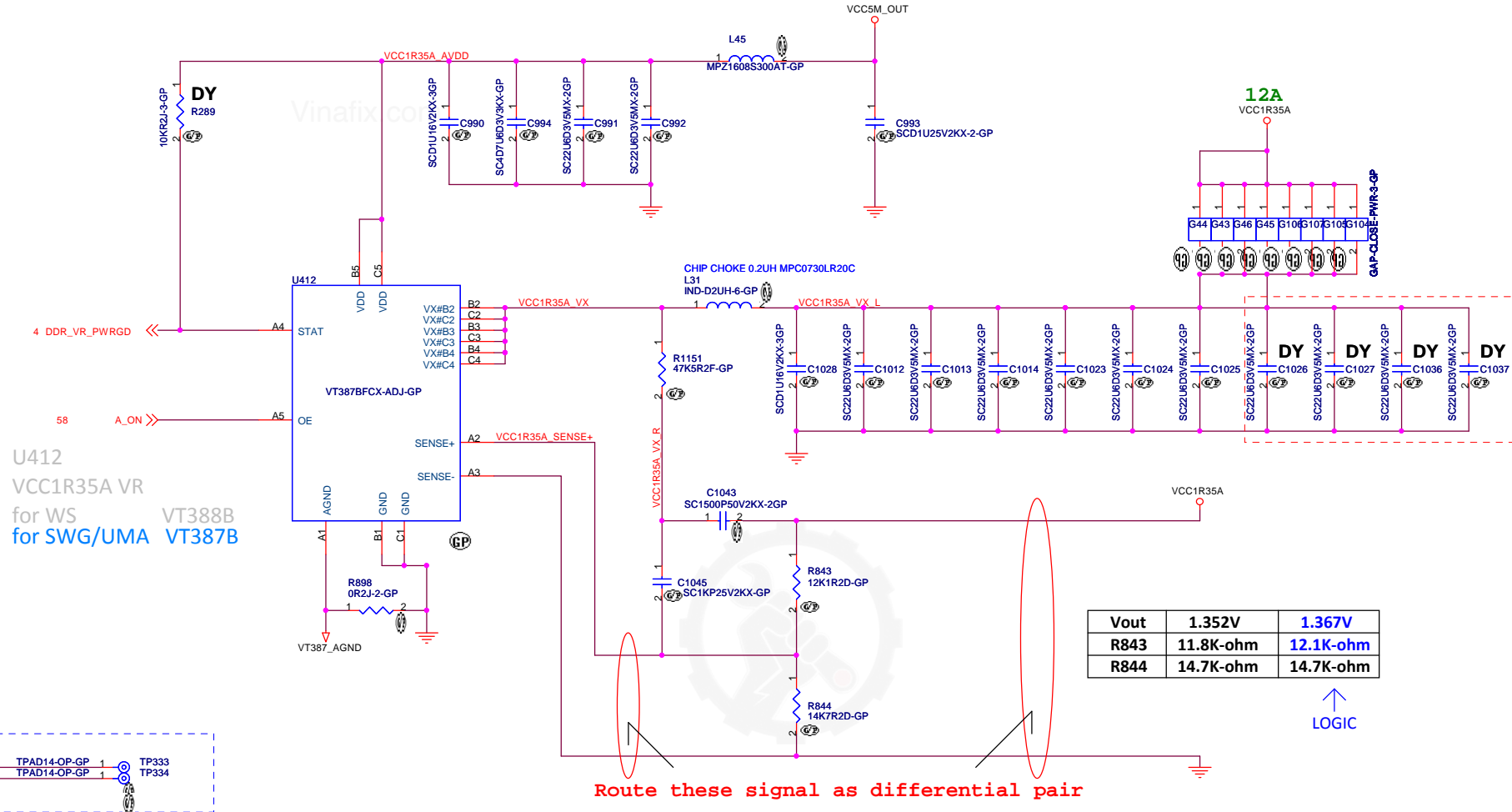
-1

VSENSE- (VDES) Trace should rout to VSENSE+ in Parallel.



<Variant Name>

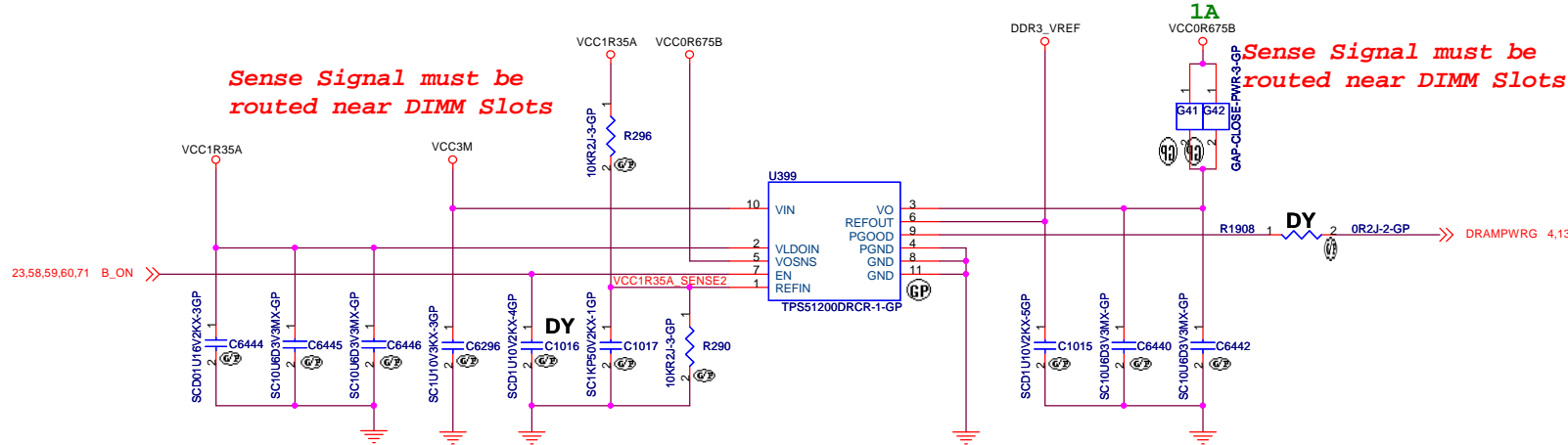
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
DC/DC VCC1R5B			
Size	Document Number		Rev
A4	Kome-1 SWG2		-1
Date: Thursday, September 12, 2013		Sheet 71 of 104	

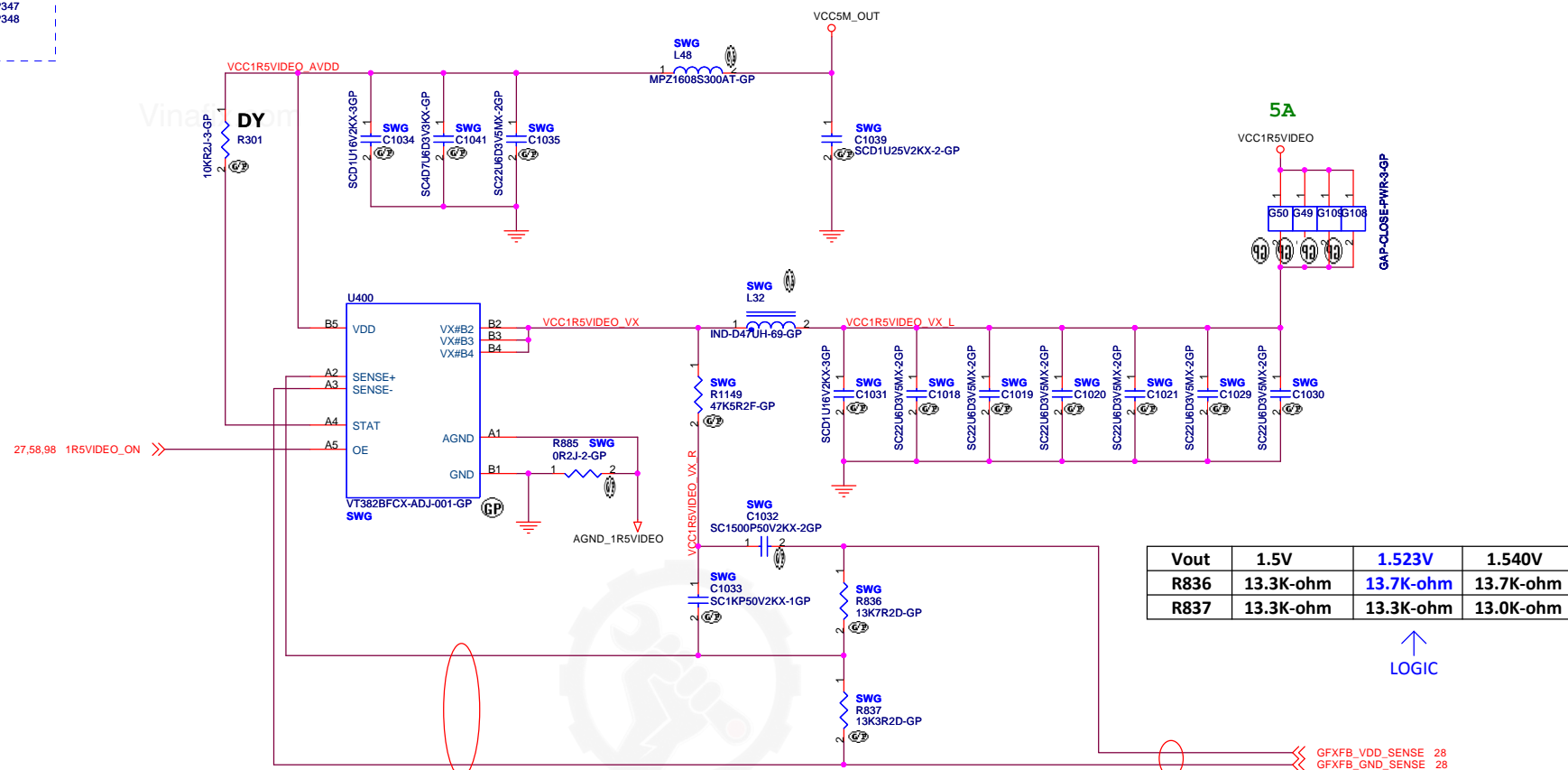


Route these signal as differential pair

Sense Signal must be routed near DIMM Slots

Sense Signal must be routed near DIMM Slots





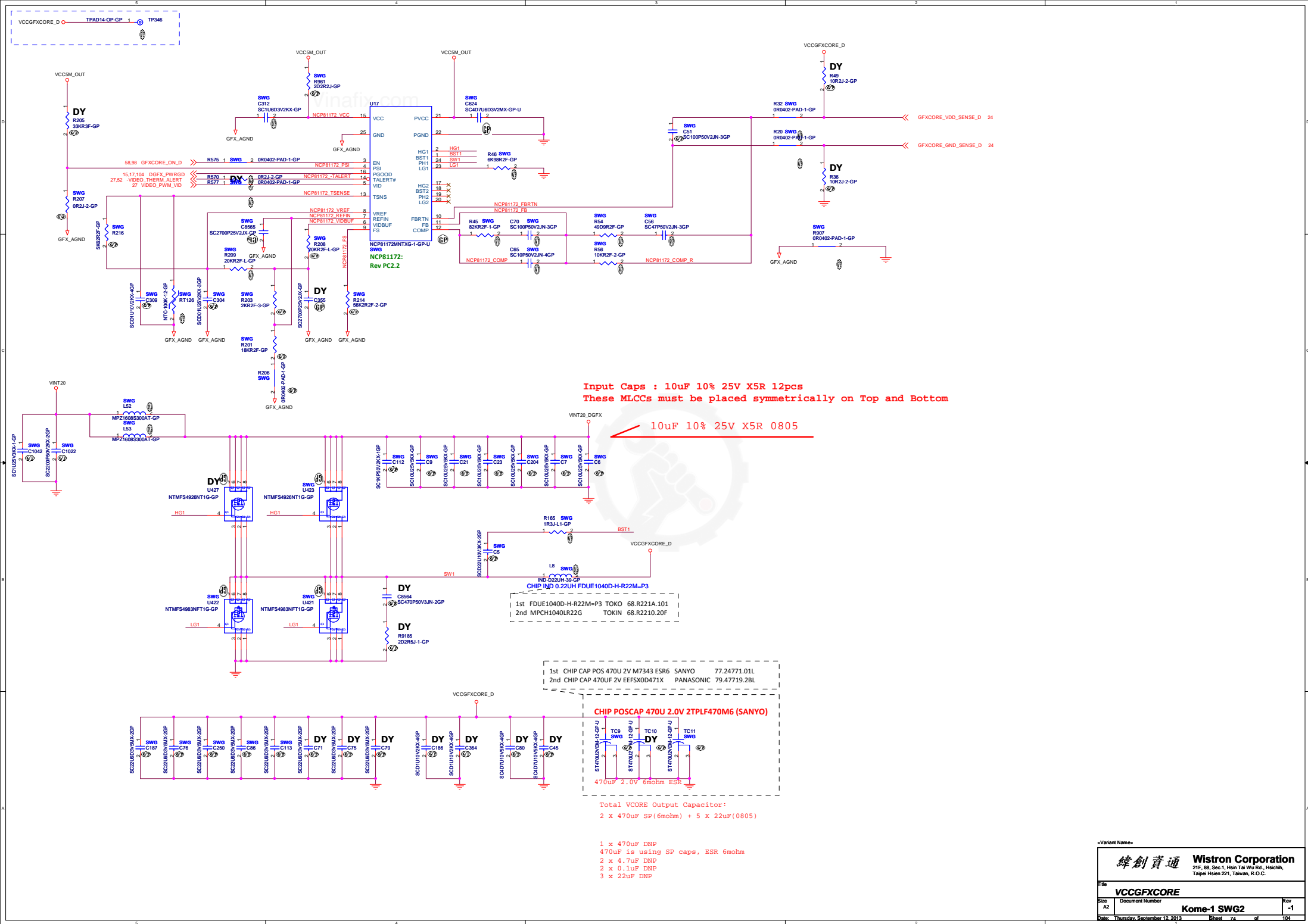
The schematic diagram illustrates the TPS74801-SS regulator circuit. The regulator (U4) is configured with the following connections:

- Input:** VCC1R35A is connected to the input of the regulator through a 10V3KX-1GP capacitor (C6452).
- Output:** The output of the regulator is connected to VCC5M_OUT through a 10V2KX-4GP capacitor (C6454).
- Feedback Network:** A 1K372F-GP resistor (R8716) and a 10V3IN-GPU capacitor (C6453) are connected between the output and the feedback pin (pin 11).
- Output Filter Network:** A 4K12R2F-GP resistor (R8715) and a 10V3KX-1GP capacitor (C6455) are connected between the output and ground.
- Control Pins:** The SS (pin 7), BIAS (pin 4), PG (pin 3), EN (pin 5), and FB (pin 8) pins are connected to ground. The IN#1 (pin 1) and IN#2 (pin 2) pins are connected to the input.
- Grounding:** The GND (pin 6) and GND (pin 11) pins are connected to ground.

The circuit is powered by VCC1R35A and VCC5M_OUT, and the output is VCC5M_OUT. The current is 1.0A.

Vout	1.5V	1.523V	1.540V
R836	13.3K-ohm	13.7K-ohm	13.7K-ohm
R837	13.3K-ohm	13.3K-ohm	13.0K-ohm

↑
LOGIC



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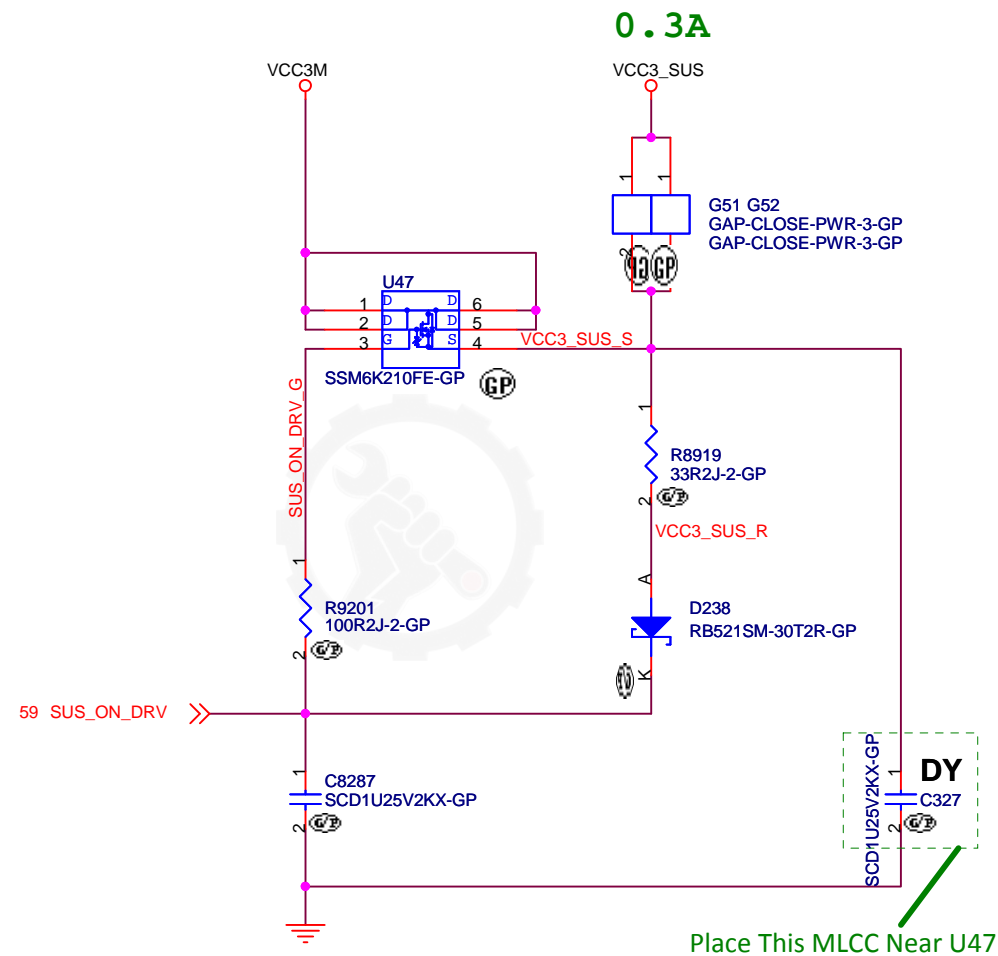
<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title		
BLANK		
Size	Document Number	Rev
A4	Kome-1 SWG2	-1
Date: Thursday, July 18, 2013		Sheet 75 of 104

VCC3_SUS is provided to PCH

PCH requires following power.

- VCCSUSHDA for Audio codec : 0.010 A
- VCCSUS3_3 for Internal VR : 0.261A



<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

LOAD SW PCH SUS

Size
A4

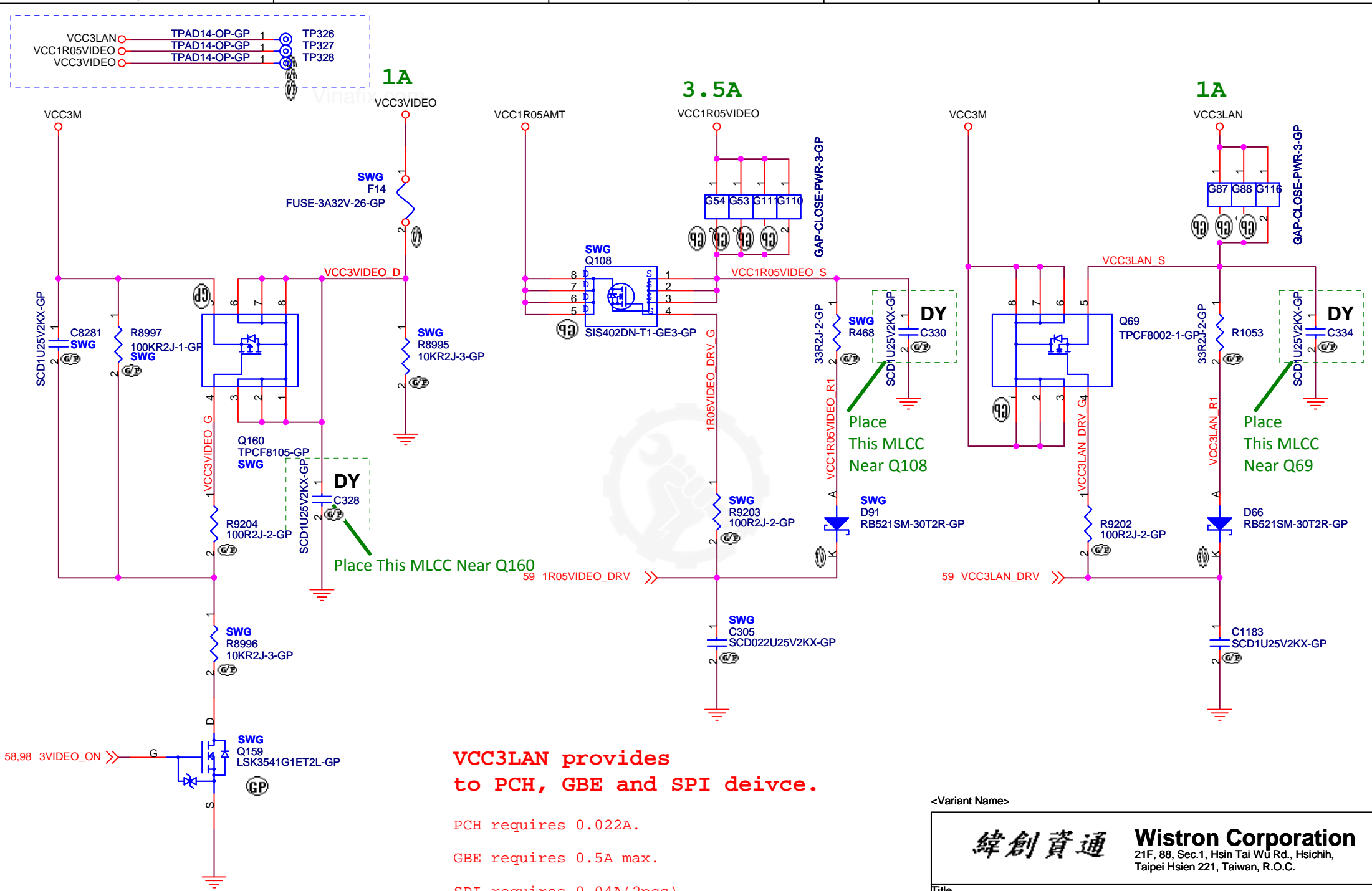
Document Number

Kome-1 SWG2

Rev
-1

Date: Thursday, September 12, 2013

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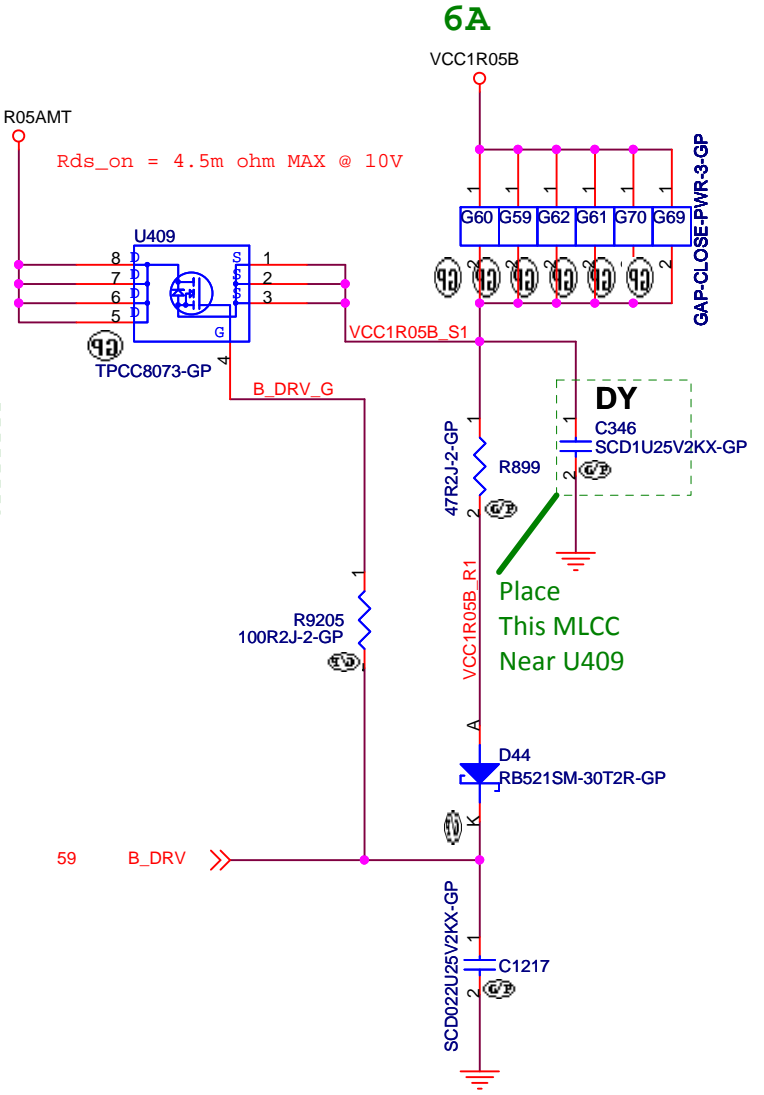
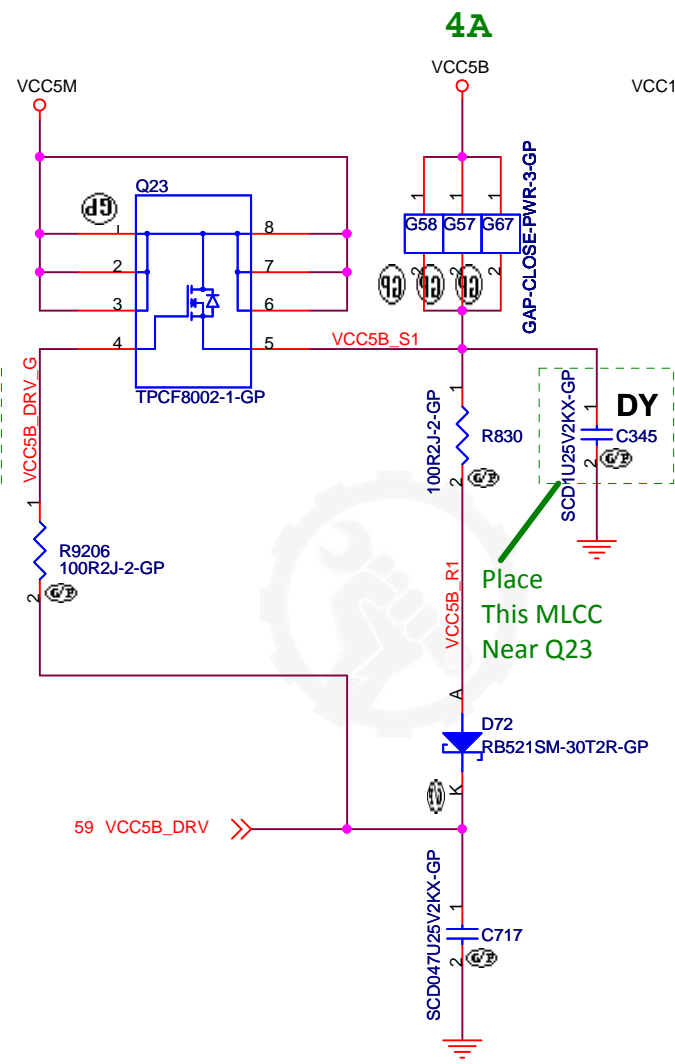
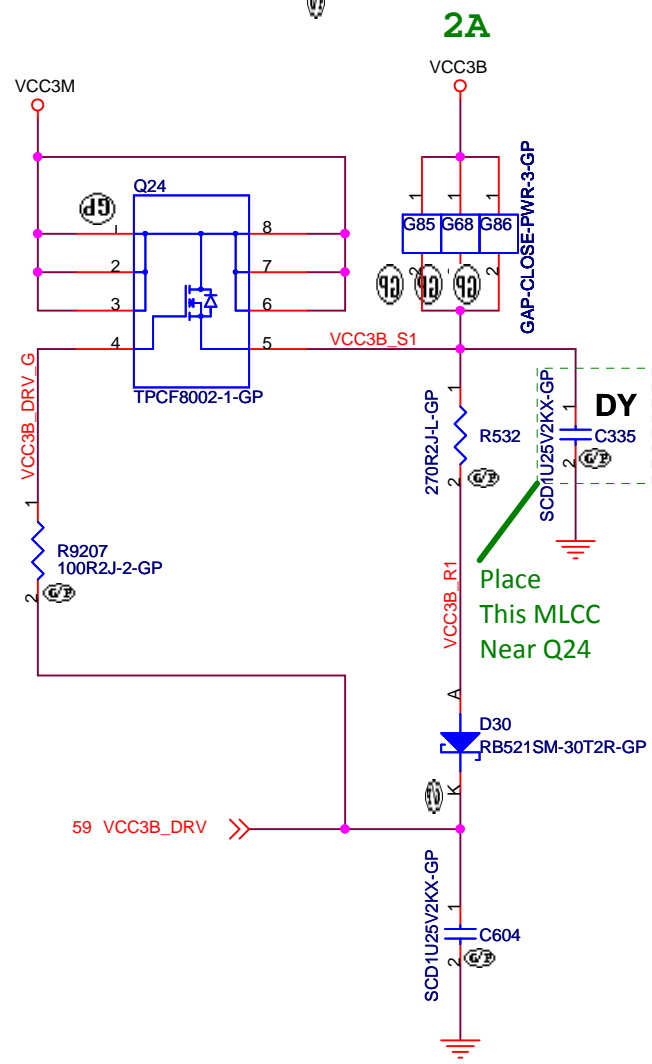
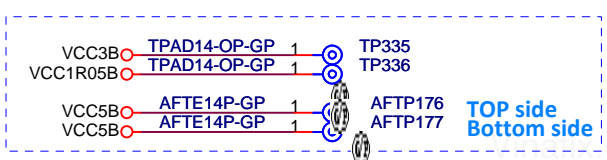
VCC3LAN provides
to PCH, GBE and SPI deivce.

PCH requires 0.022A.

GBE requires 0.5A max.

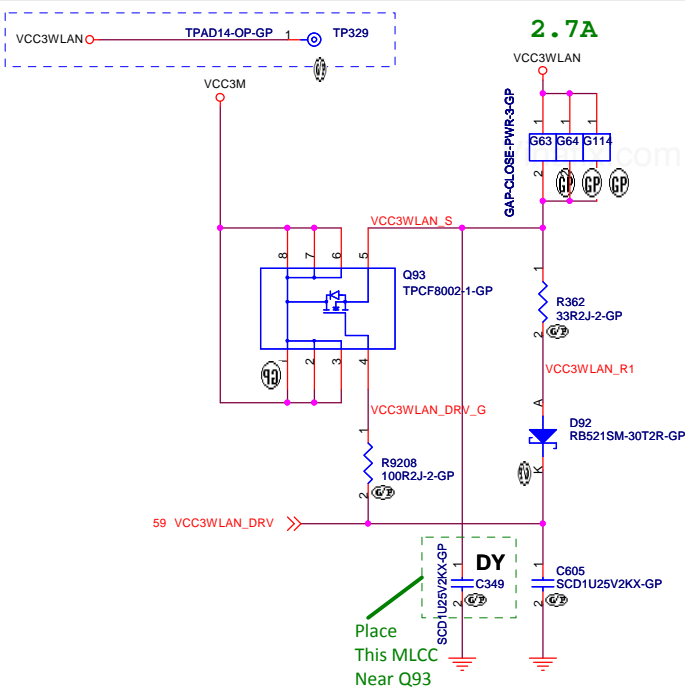
SPI requires 0.04A(2pcs)

<Variant Name>		
<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title		
LOAD SW VIDEO / LAN		
Size	Document Number	Rev
A4	Kome-1 SWG2	-1
Date: Thursday, September 12, 2013		
Sheet 77 of 104		

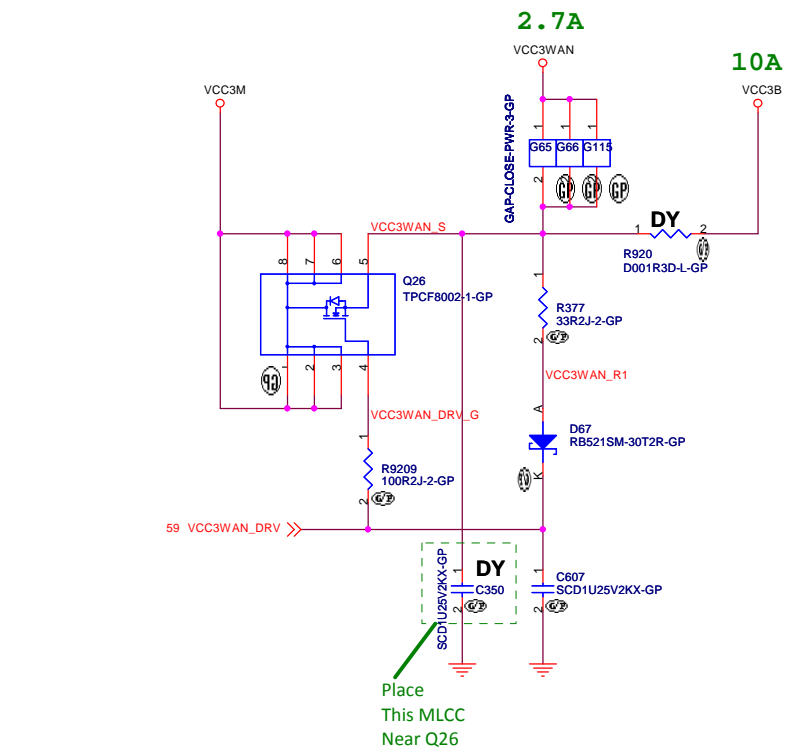


<Variant Name>

緯創資通 Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
LOAD SW VCCB		
Size	Document Number	Rev
A4	Kome-1 SWG2	-1
Date: Thursday, September 12, 2013		Sheet 78 of 104



VCC3WLAN

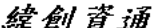


VCC3WAN

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BLANK

<Variant Name>			
		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Thunderbolt Redwood MISC (2/6)			
Size A3	Document Number	Rev	
	Kome-1 SWG2	-1	
Date:	Thursday, July 16, 2013	Sheet	81 of 104

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<Variant Name>

<div><div>緯創資通</div><div>Wistron Corporation</div><div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
Title <div>Thunderbolt Redwood GND (4/6)</div>		
Size <div>A4</div>	Document Number <div>Kome-1 SWG2</div>	Rev <div>-1</div>
Date: Thursday, July 18, 2013		Sheet 83 of 104

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<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title		
Thunderbolt Redwood PCIE-CPU (5/6)		
Size	Document Number	Rev
A4	Kome-1 SWG2	-1
Date: Thursday, July 18, 2013		Sheet 84 of 104

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<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title Thunderbolt VCC12M		
Size A4	Document Number Kome-1 SWG2	Rev -1
Date: Thursday, July 18, 2013		Sheet 86 of 104

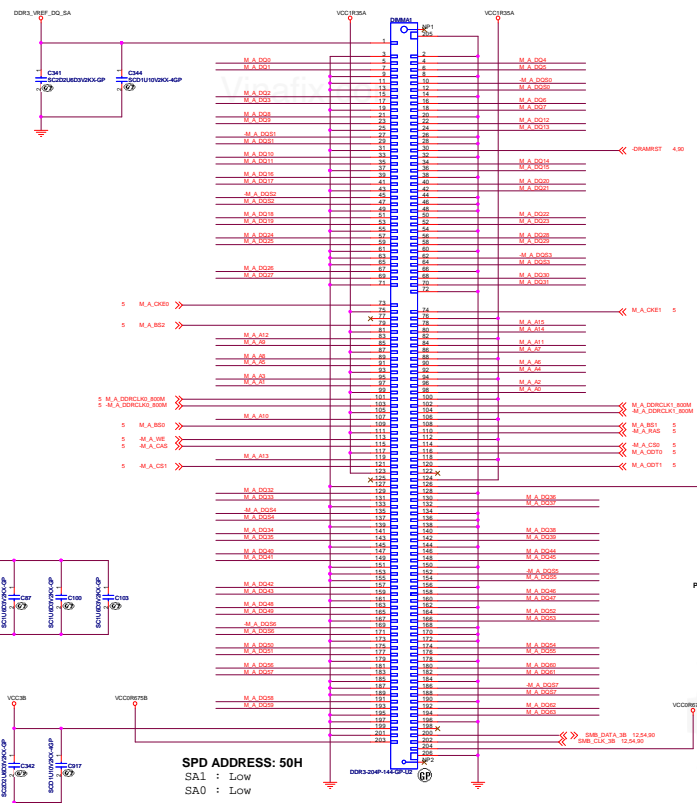
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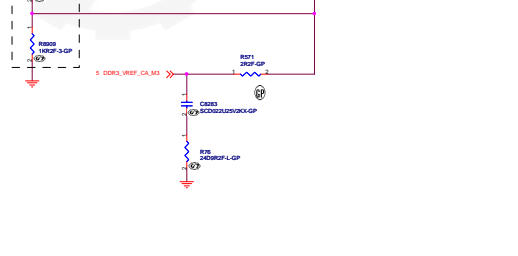
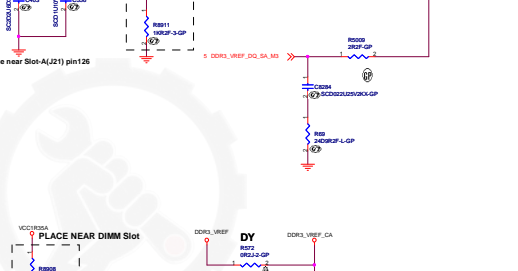
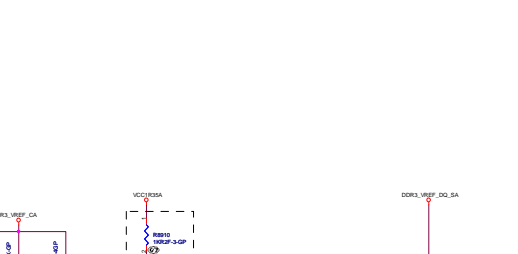
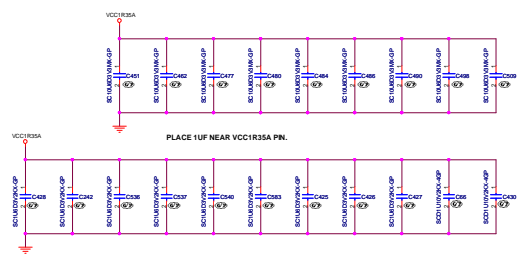
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<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title		
Thunderbolt Power Mux		
Size	Document Number	Rev
A4	Kome-1 SWG2	-1
Date: Thursday, July 18, 2013		Sheet 87 of 104

S M_A_DQ0B[0] <<
 S M_A_DQ0B[1] <<
 S M_A_DQ0B[2] <<
 S M_A_DQ0B[3] <<



SPD ADDRESS: 50H
 SA1 : Low
 SA0 : Low



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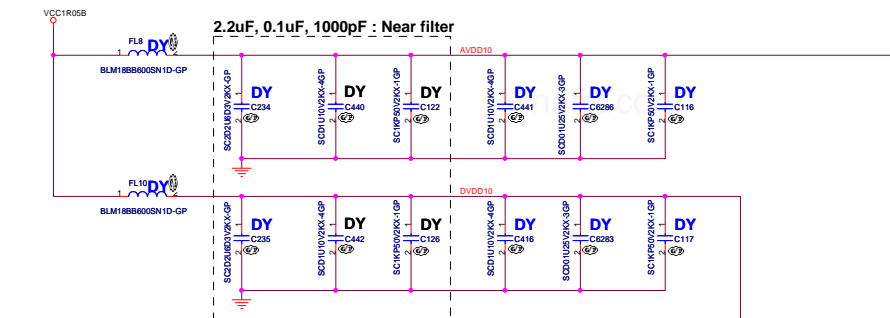


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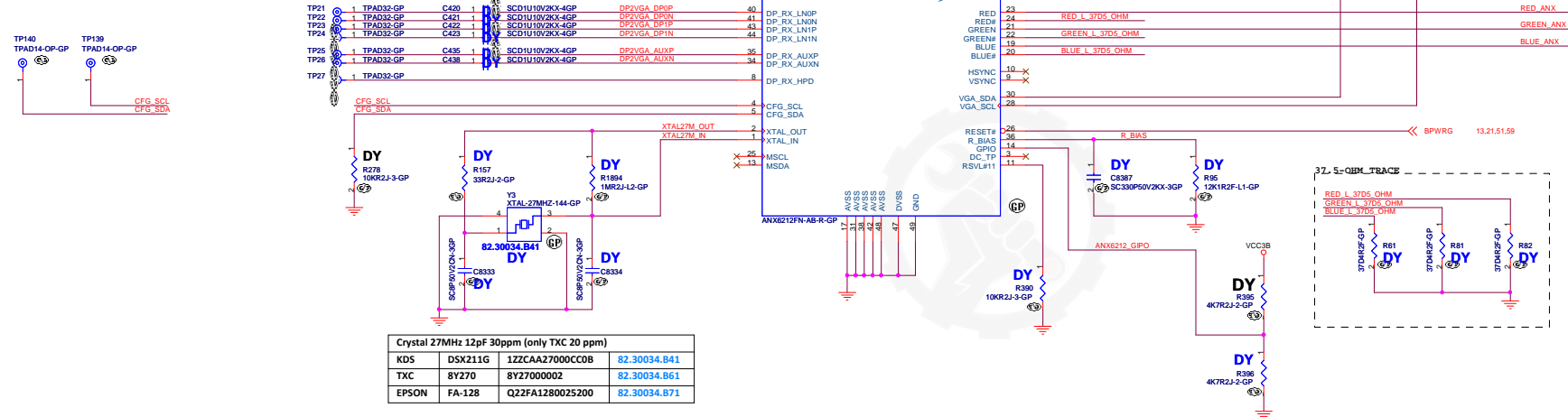
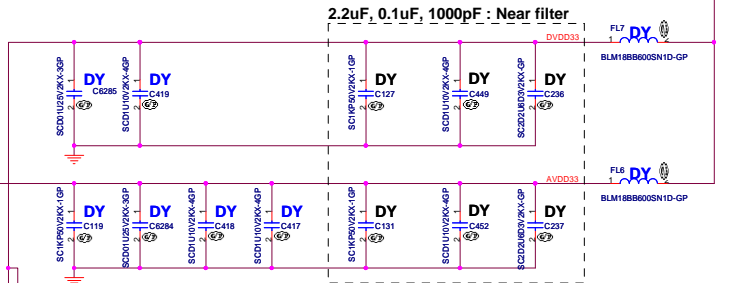
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Title <div>DIMM Topology</div>		
Size <div>A4</div>	Document Number <div>Kome-1 SWG2</div>	Rev <div>-1</div>
Date: Thursday, July 18, 2013		Sheet 92 of 104

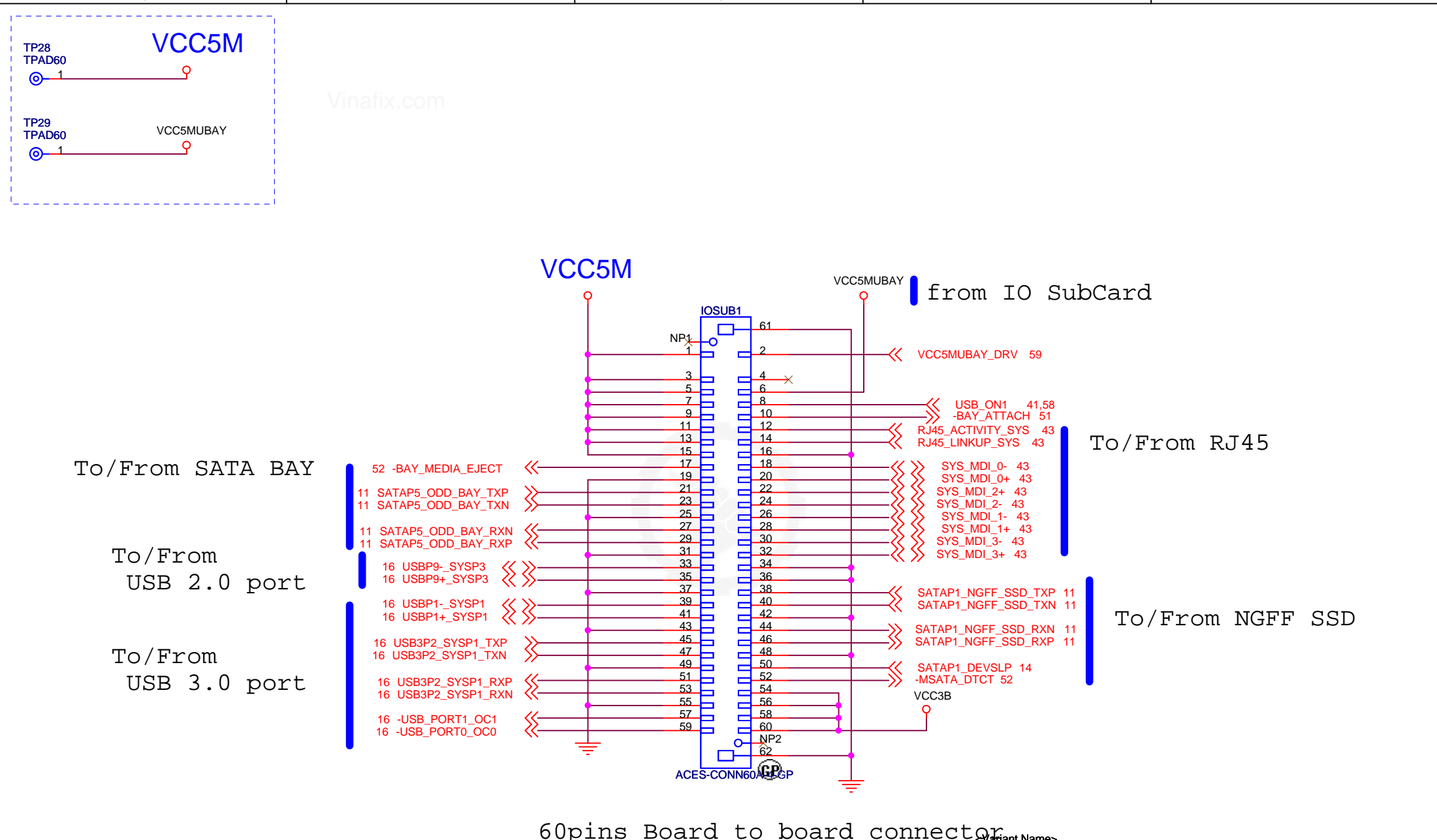
Max 130mA



Max 100mA



To keep planar area in case DDID for eDP has critical problem.



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To/From SATA BAY

To/From
USB 2.0 port

To/From
USB 3.0 port

VCC5M

VCC5MUBAY

from IO SubCard

To/From RJ45

To/From NGFF SSD

60pins Board to board connector

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

I/O SubCard Interface

Size
A4

Document Number

Kome-1 SWG2

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-1

Date: Thursday, September 12, 2013

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<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
A

Document Number

Kome-1 SWG2

Rev
-1

Date: Thursday, July 18, 2013

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<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
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Size A	Document Number Kome-1 SWG2	Rev -1
Date: Thursday, July 18, 2013		Sheet 96 of 104

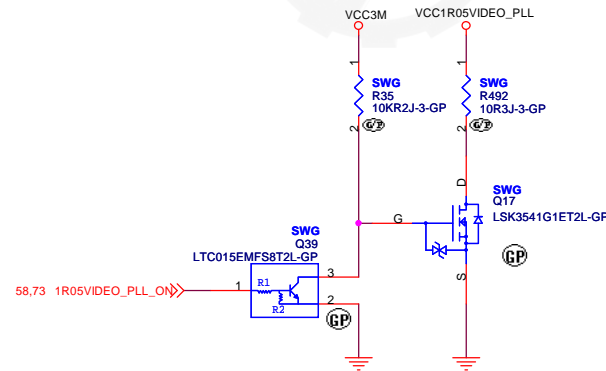
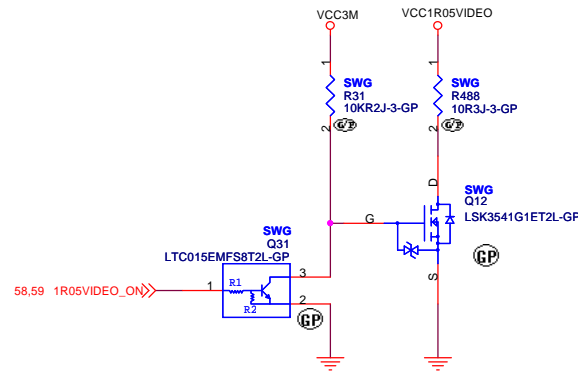
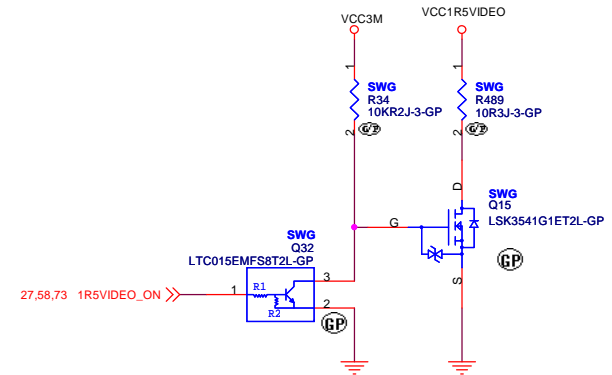
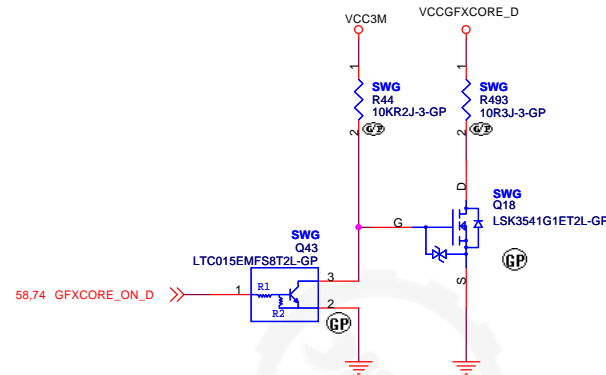
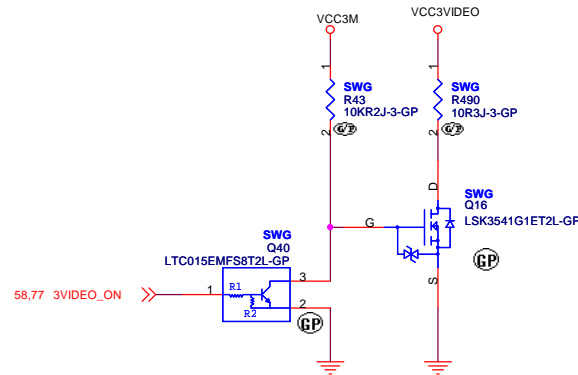
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Title BLANK		
Size A	Document Number Kome-1 SWG2	Rev -1
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Discharge
3VIDEO_ON
GFXCORE_ON_D
1R5VIDEO_ON
1R05VIDEO_ON
1R05VIDEO_PLL_ON

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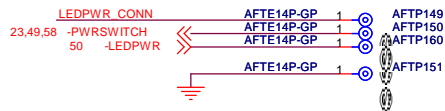


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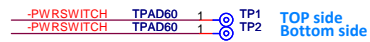
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title Discharge		
Size A3	Document Number Kome-1 SWG2	Rev -1
Date: Thursday, September 12, 2013 Sheet gg of 104		

Near Power SW CONN: PBTN1 (p.099)

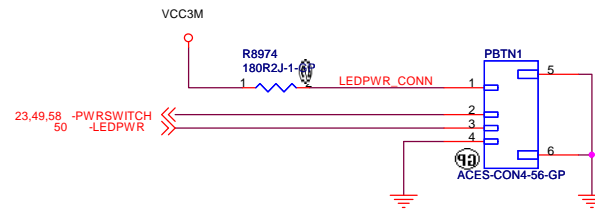
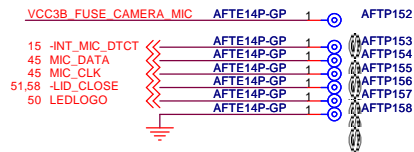


Near Power SW CONN: PBTN1 (p.099)



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Near Camera CONN: CMR1 (p.099)



A

Normal type SMT connector
Terminal is placed on PCB side



SIM Slot

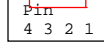
FFC from Power
button subcard

OR

B

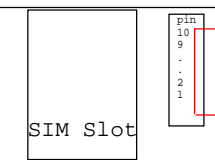
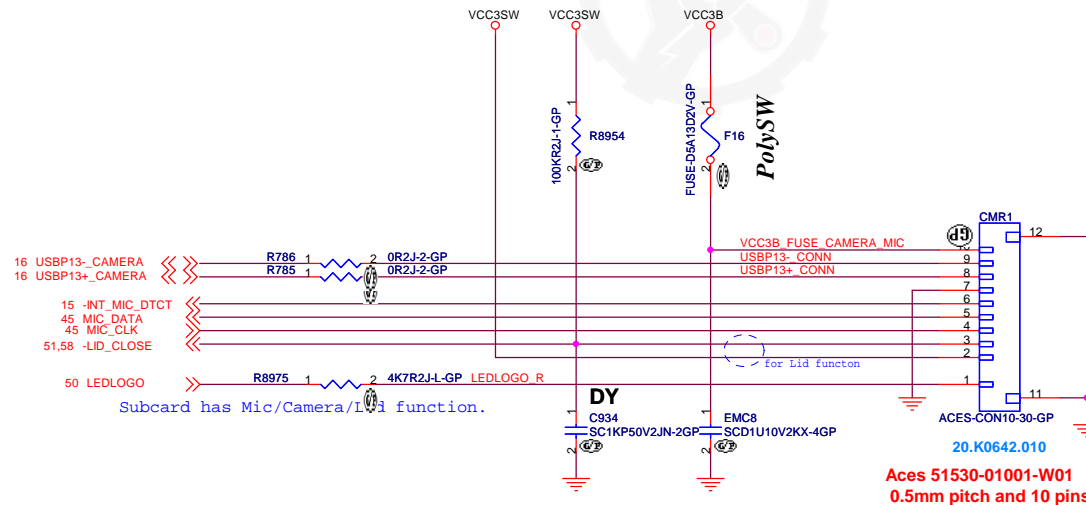
ME recommend "B"

Reversed Type connector
Terminal is placed on opposit side from PCB



SIM Slot

FFC from Power
button subcard



To/From
Camera/Mic/Lid
LOGO_LED

<Variant Name>

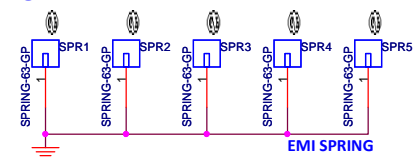
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
SubCard Power SW, Camera/Mic/Lid Interface

Size A3 Document Number **Kome-1 SWG2** Rev -1

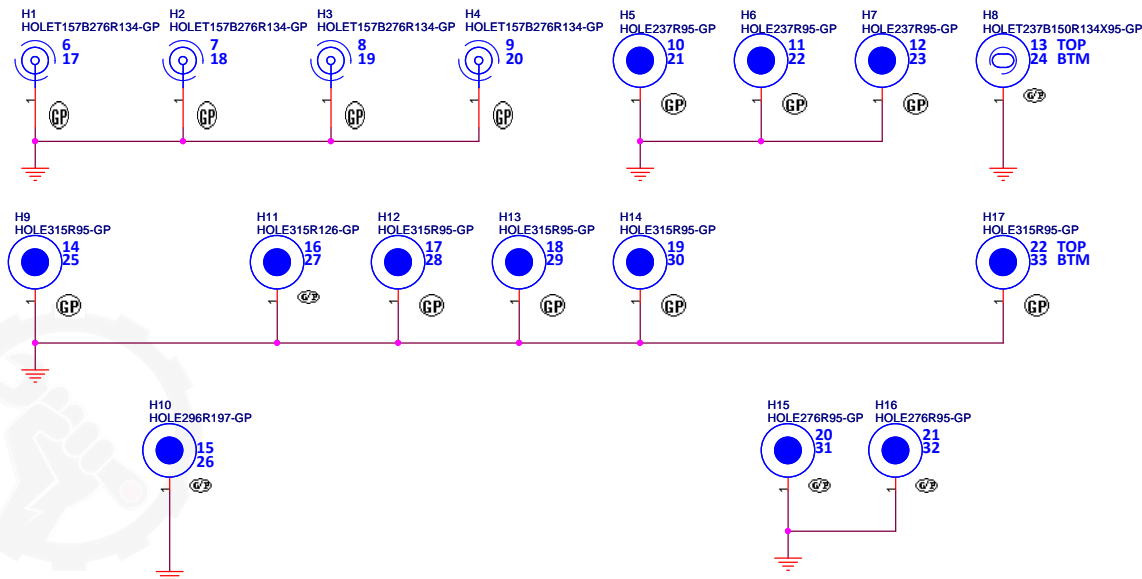
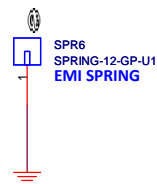
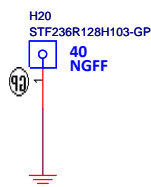
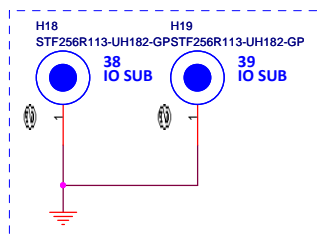
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TOP



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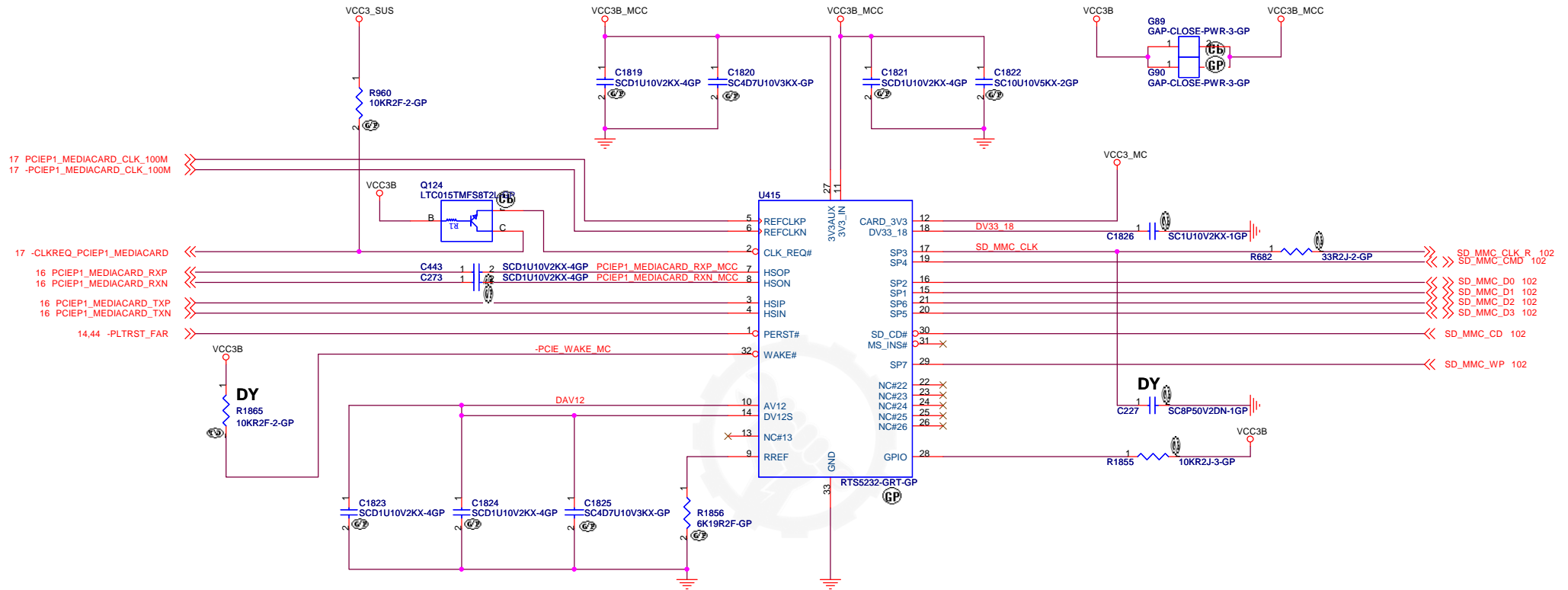
BTM



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			PTH FOR SCREW HOLES
Size	Document Number	Kome-1 SWG2	
A3			-1
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TABLE

Pin name	SD/MMC	MEMORSTICK
SP1	SD_D1	MS_D1
SP2	SD_D0	MS_D0
SP3	SD_CLK	MS_D0
SP4	SD_CMD	MS_D2
SP5	SD_D3	MS_D3
SP6	SD_D2	MS_CLK
SP7		

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Media Card Controller

Size
A3

Document Number

Kome-1 SWG2

Rev
-1

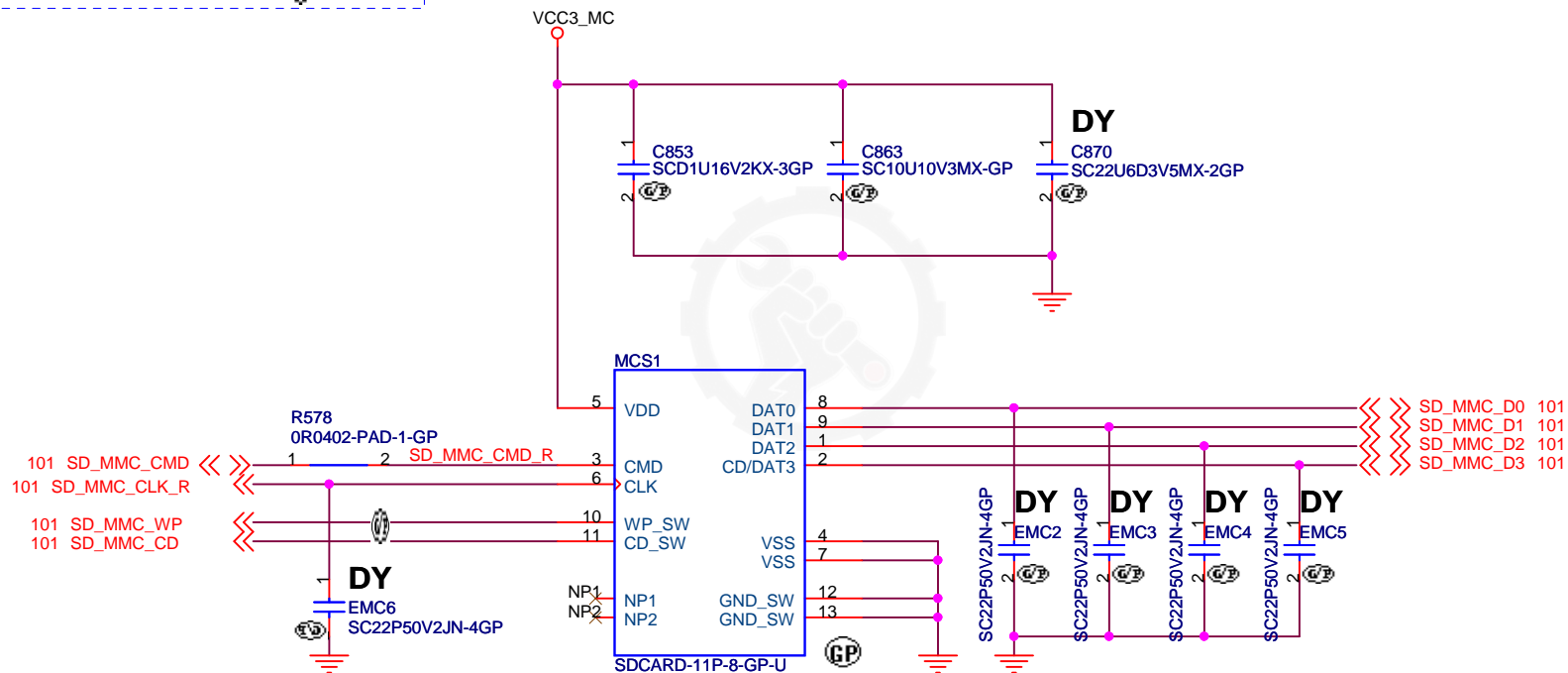
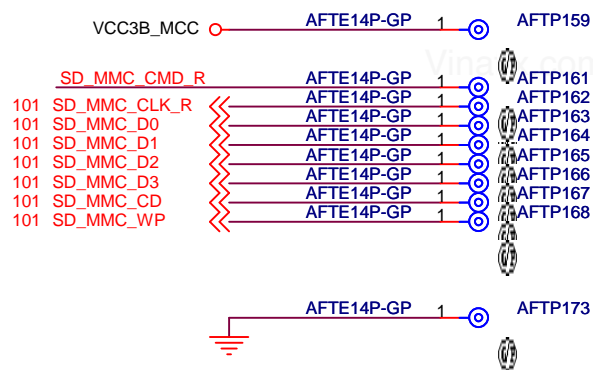
Date: Thursday, September 12, 2013

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104

Near MediaCard Slot: MCS1 (p.102)



<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

MEDIACARD SLOT

Size

Document Number

A4

Kome-1 SWG2

Rev

-1

Date: Thursday, September 12, 2013

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